REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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DFTR		
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REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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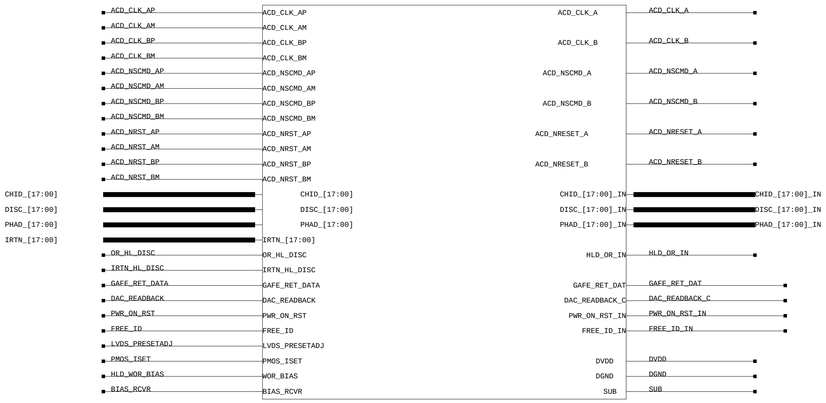

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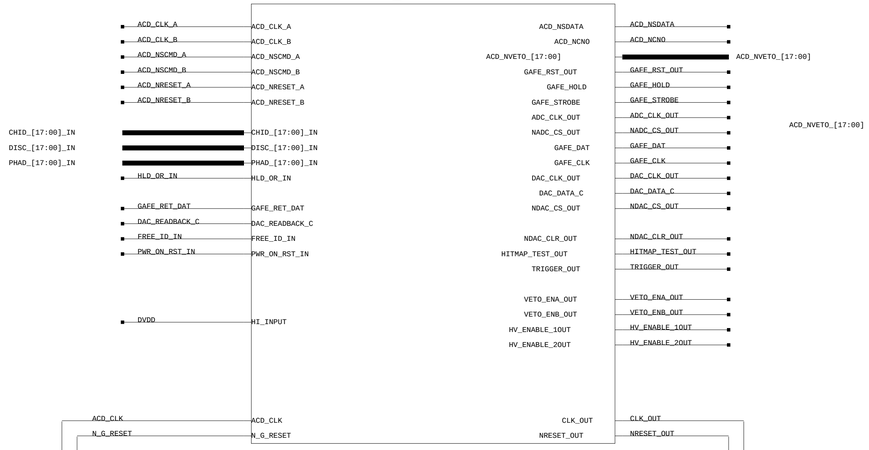

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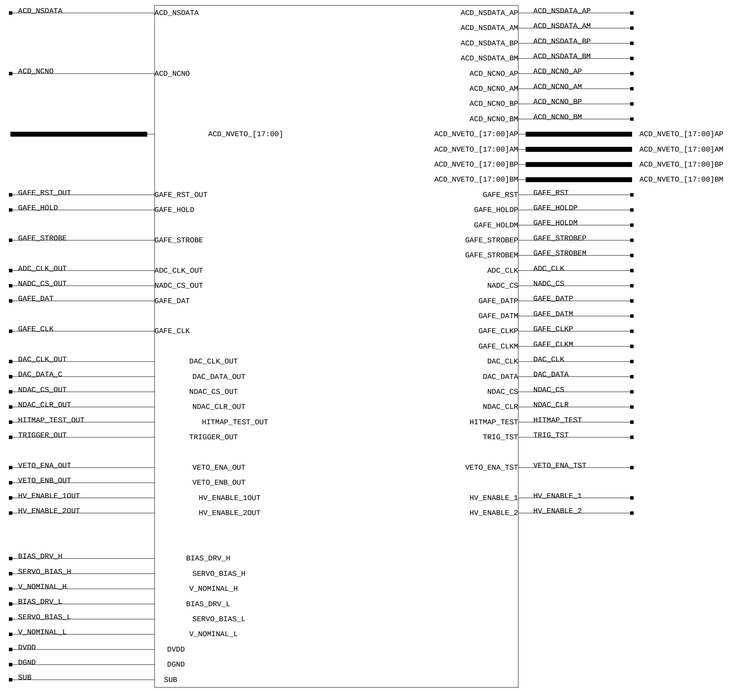

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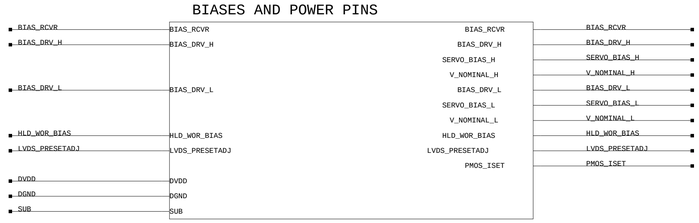

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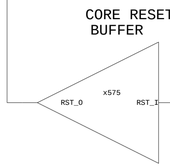

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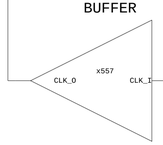

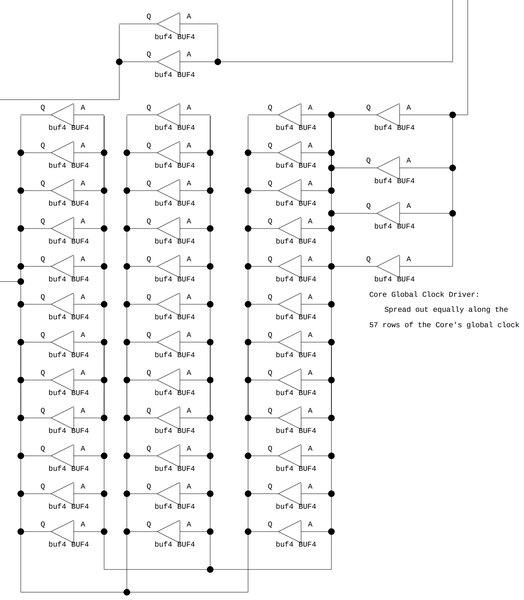

Core Global Clock Driver:
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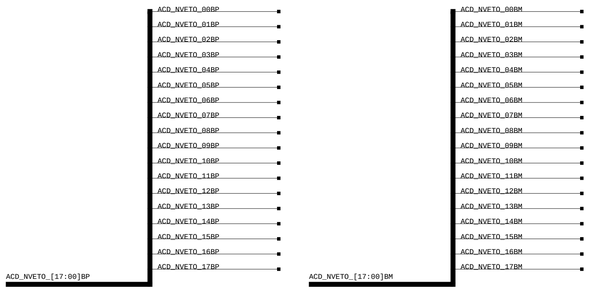

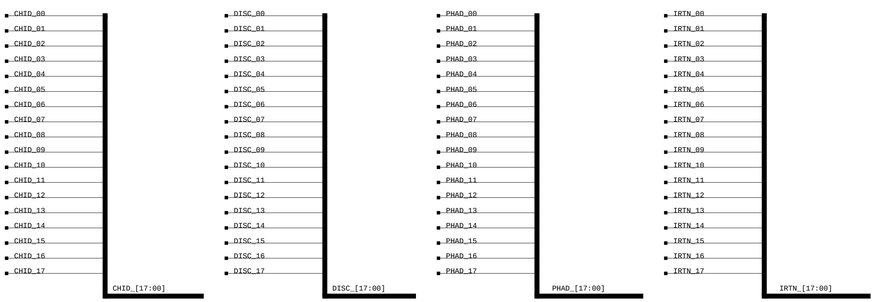

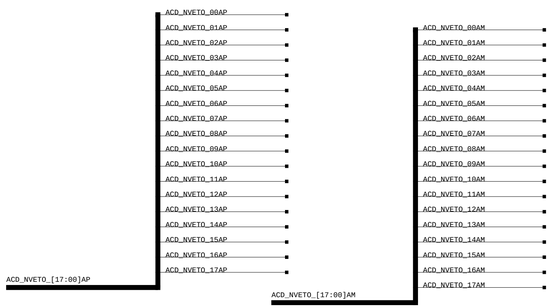

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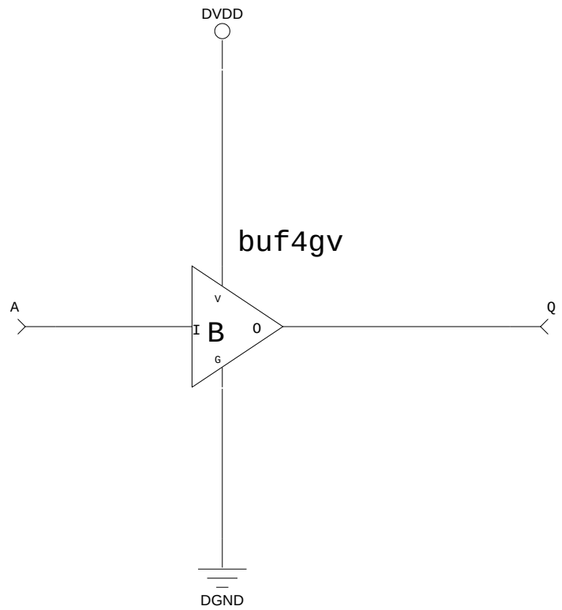

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TITLE: buf4

DATE: 11-1-2002_16:30

DRAWN BY: D. Freytag (SLAC)

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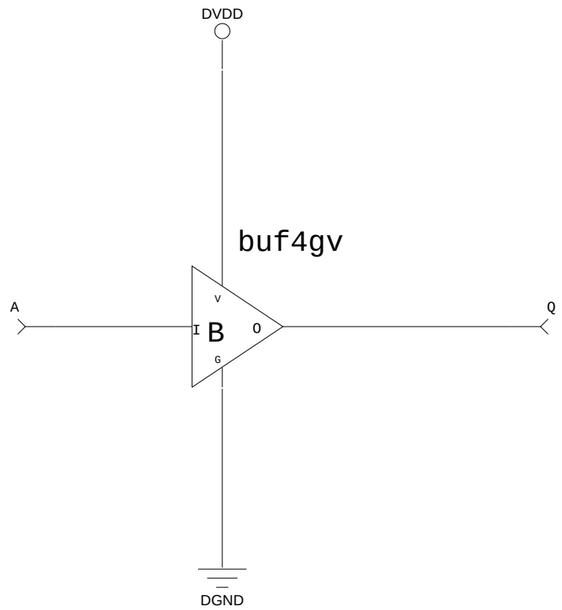

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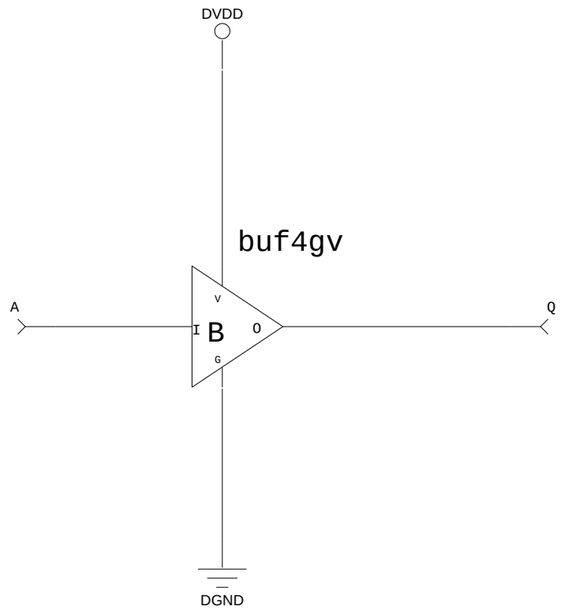

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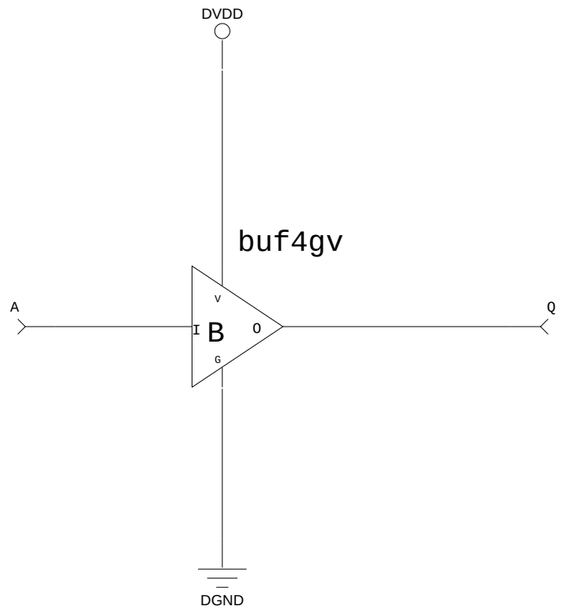

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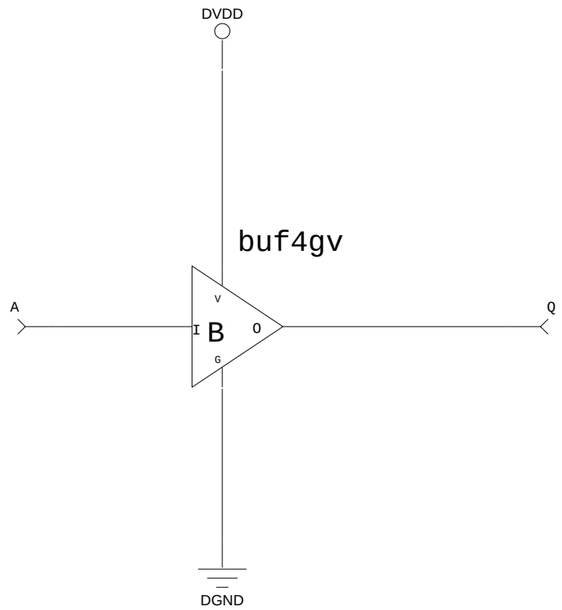

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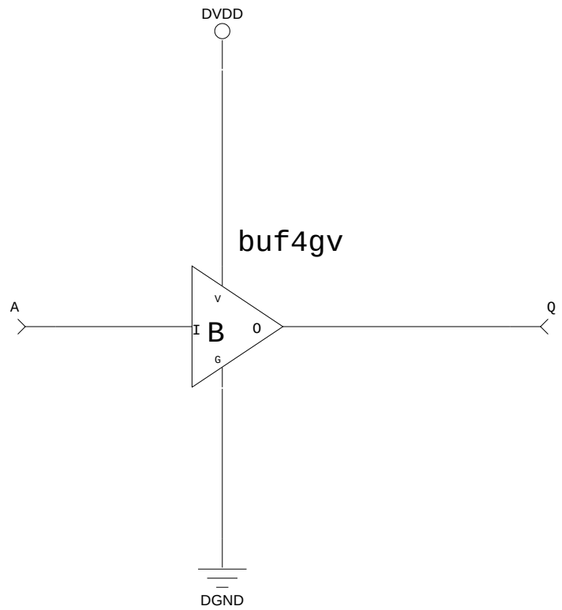

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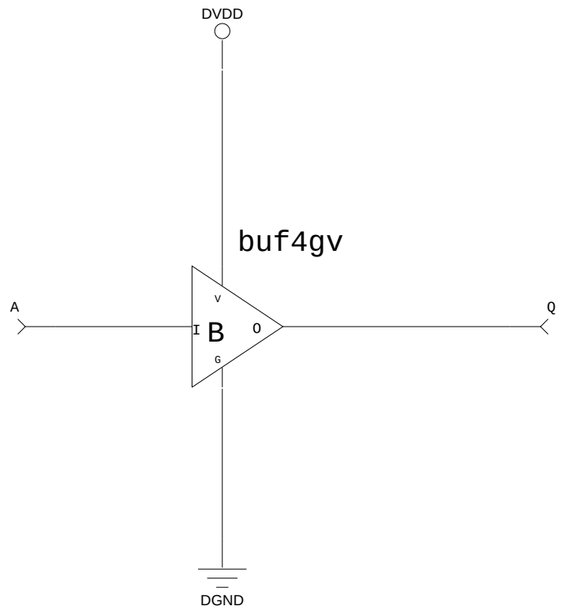

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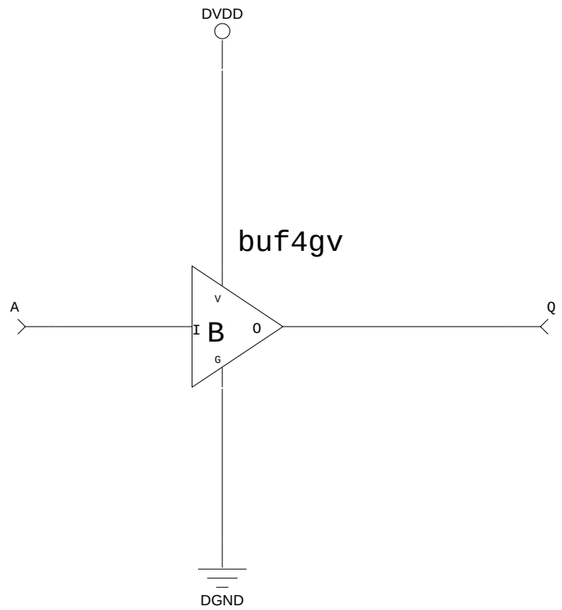

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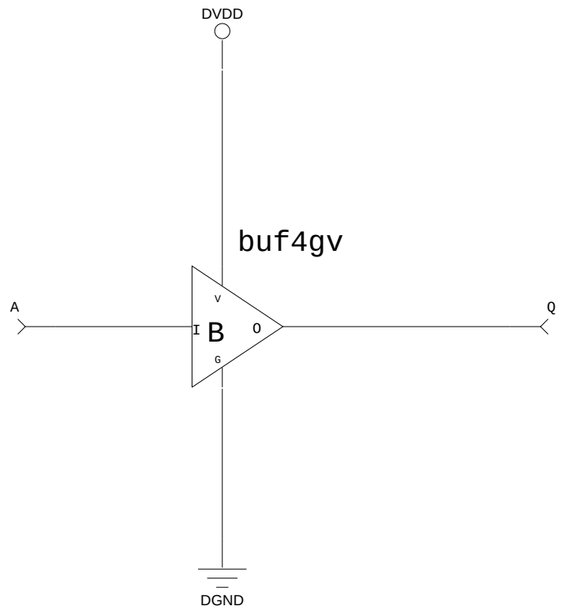

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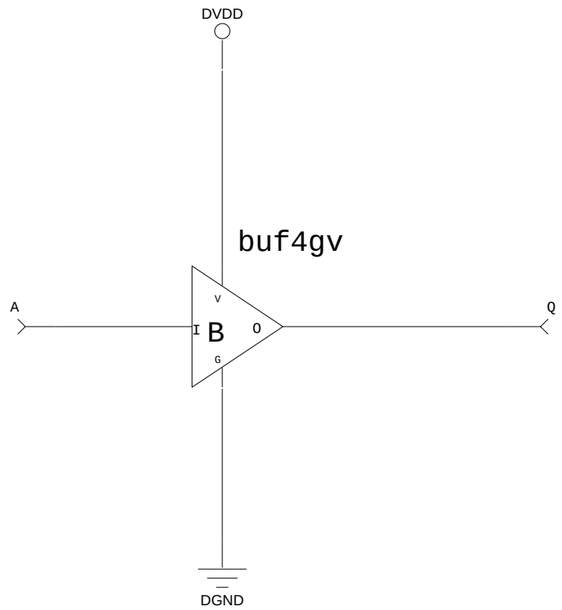

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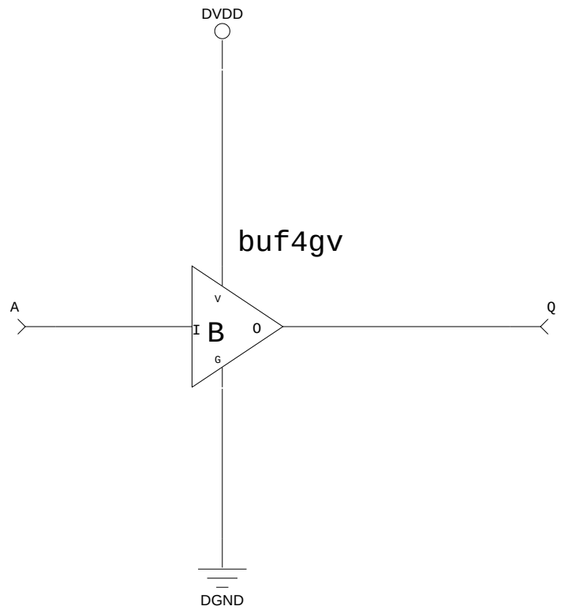

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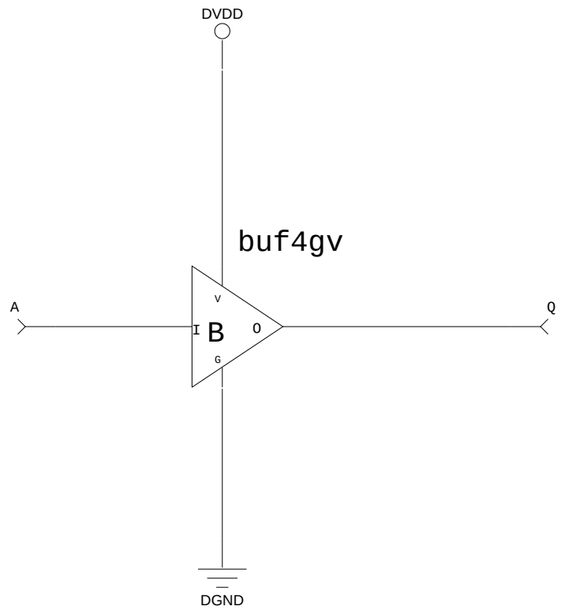

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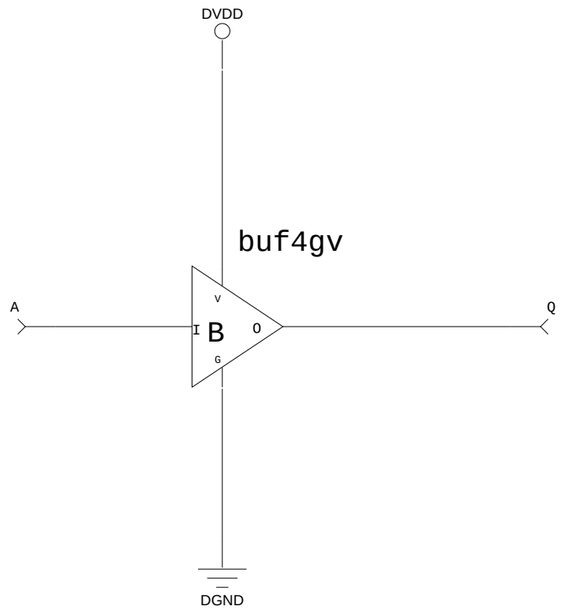

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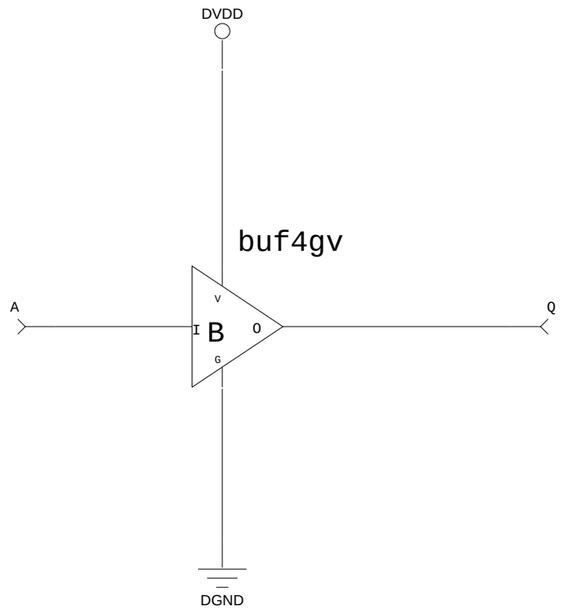

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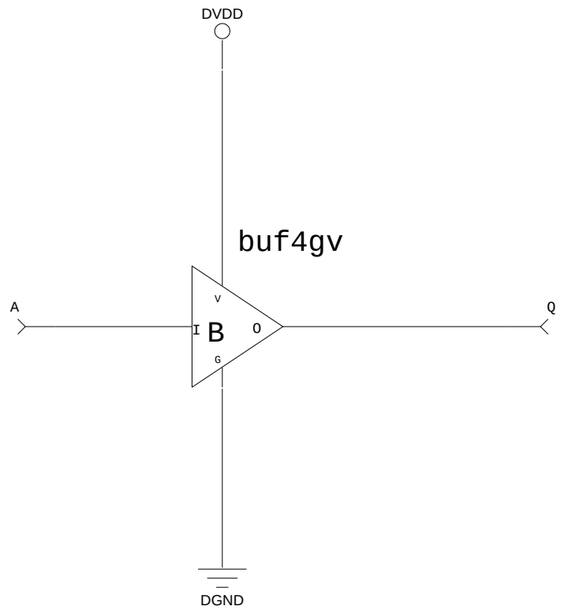

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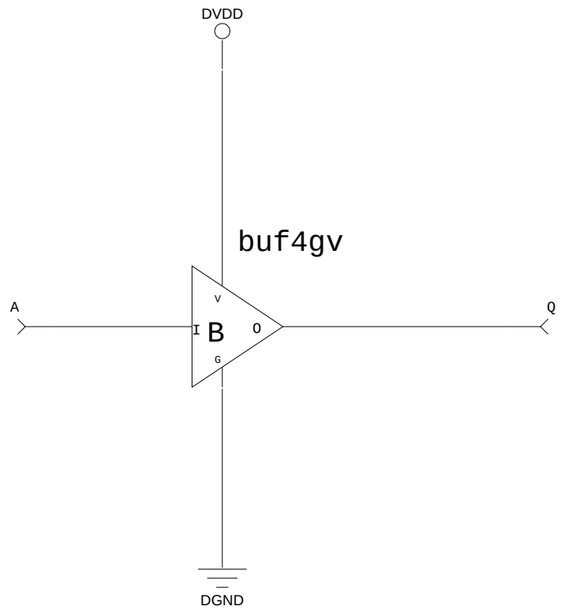

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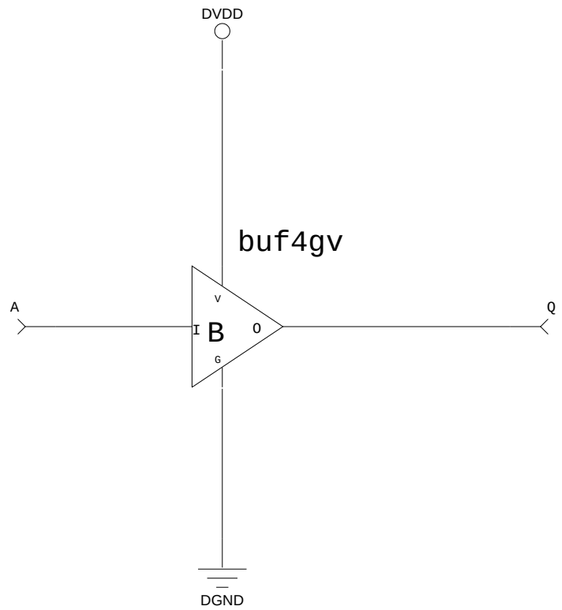

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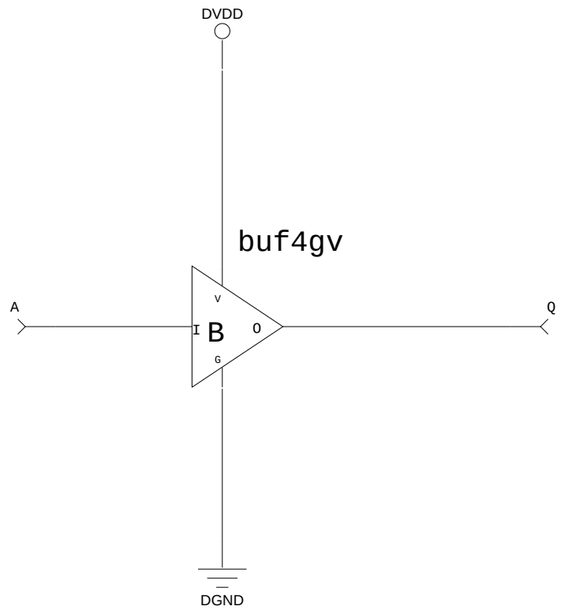

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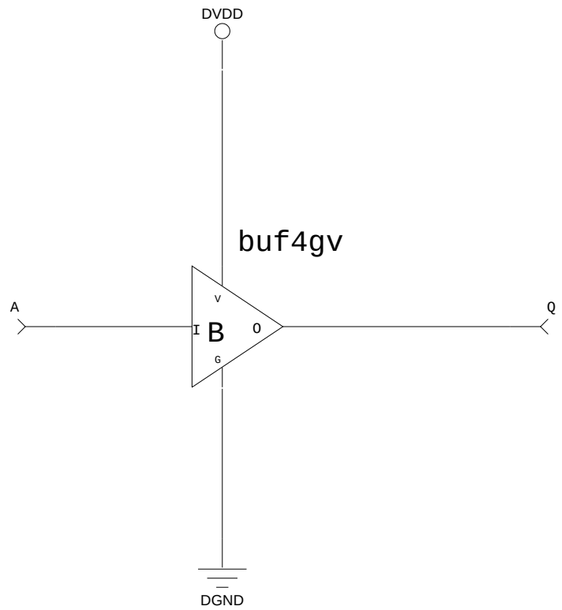

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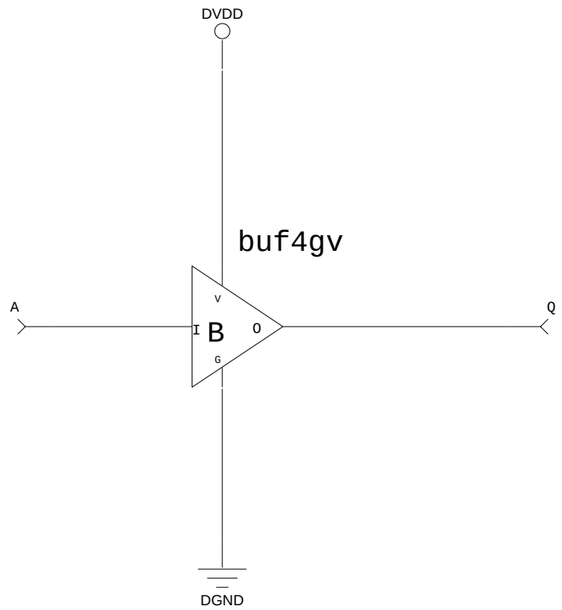

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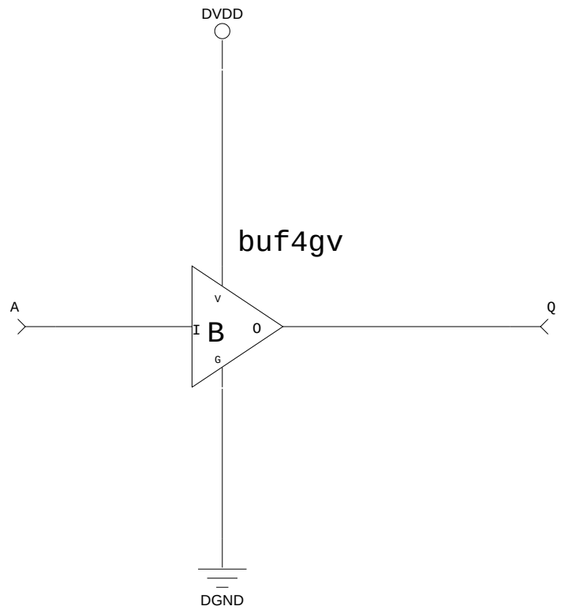

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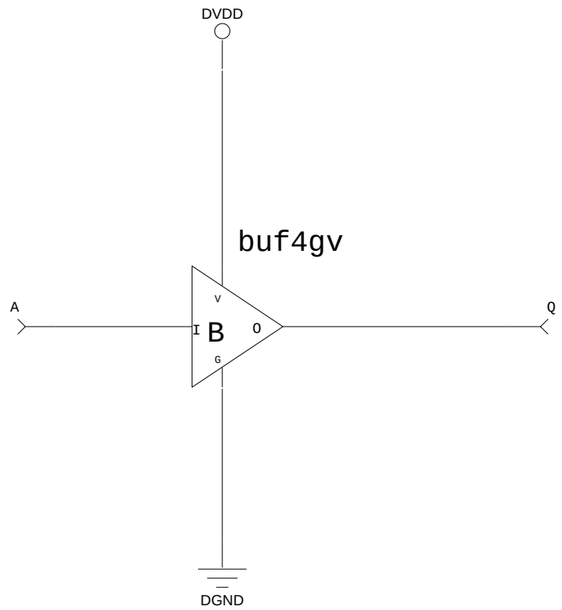

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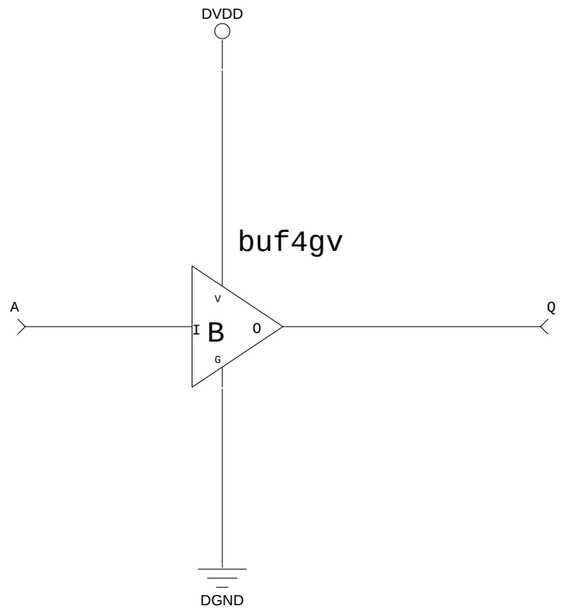

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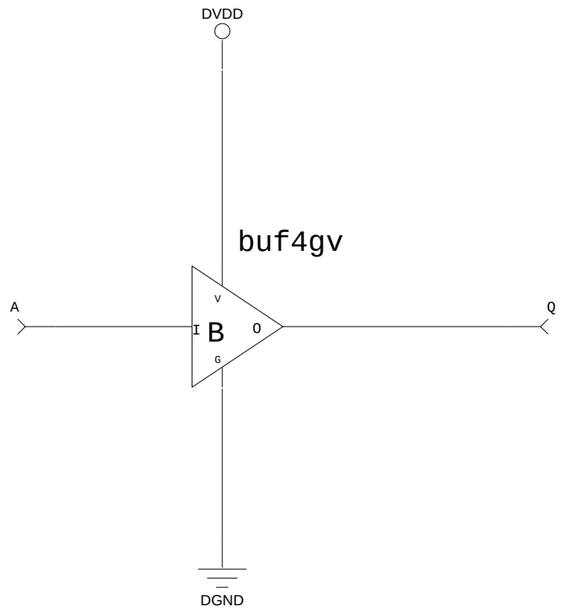

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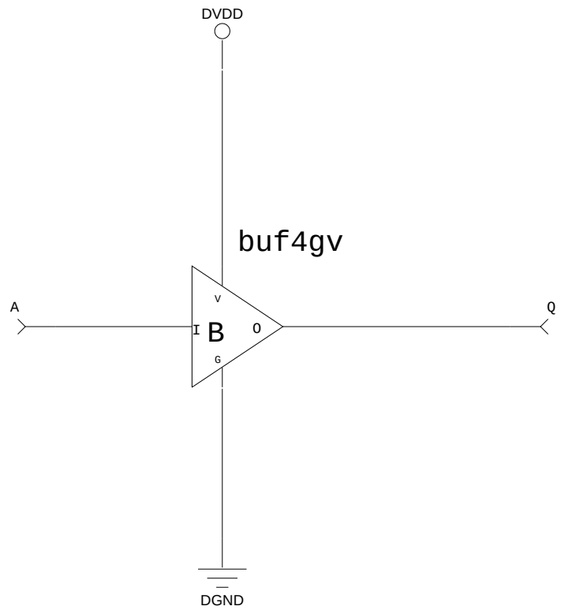

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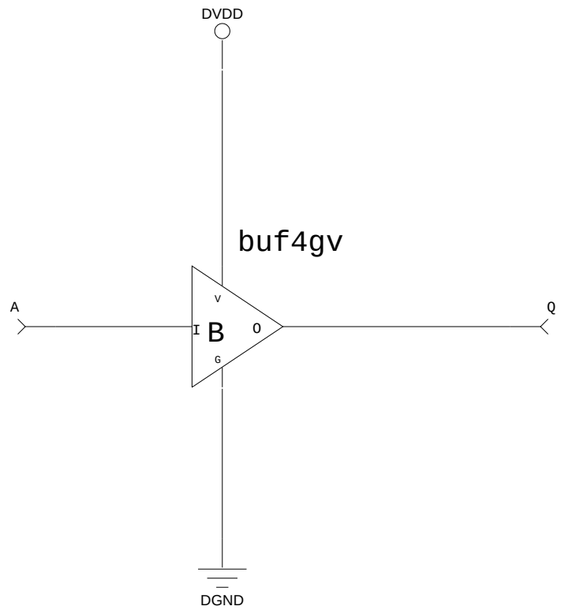

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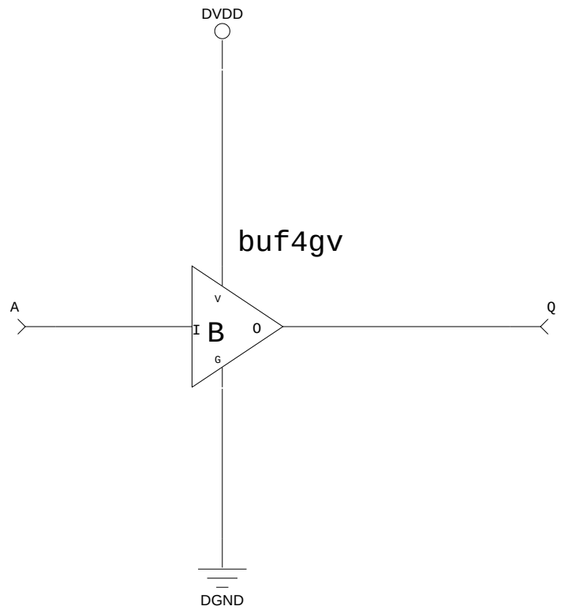

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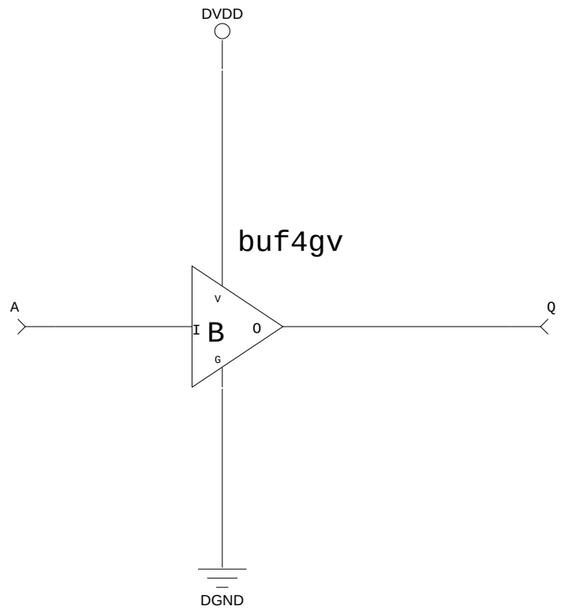

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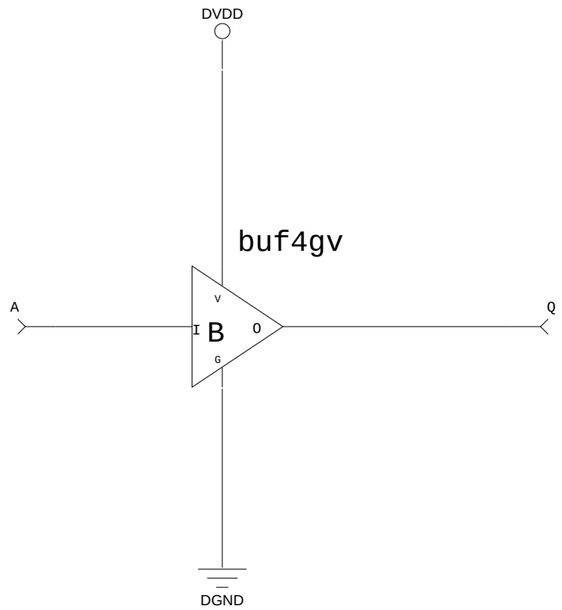

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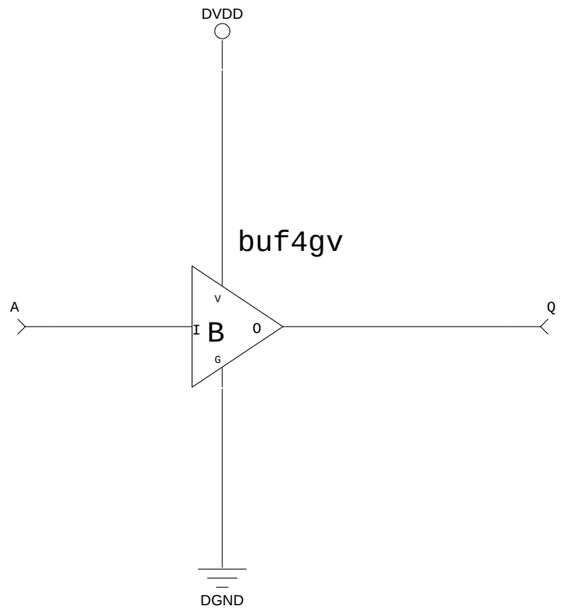

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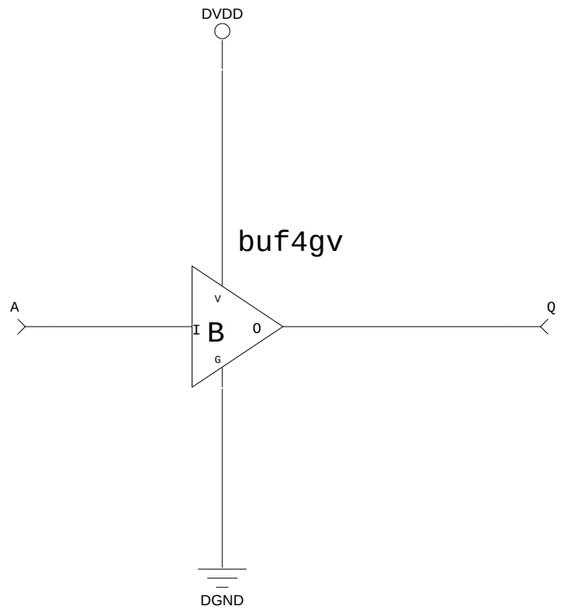

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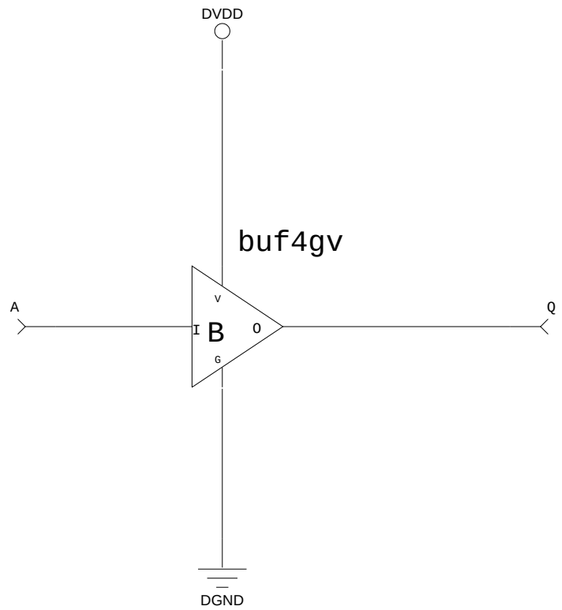
for Particle Physics

TITLE: buf4

DATE: 11-1-2002_16:30

DRAWN BY: D. Freytag (SLAC)

University of California at Santa Cruz



GLAST PROJECT

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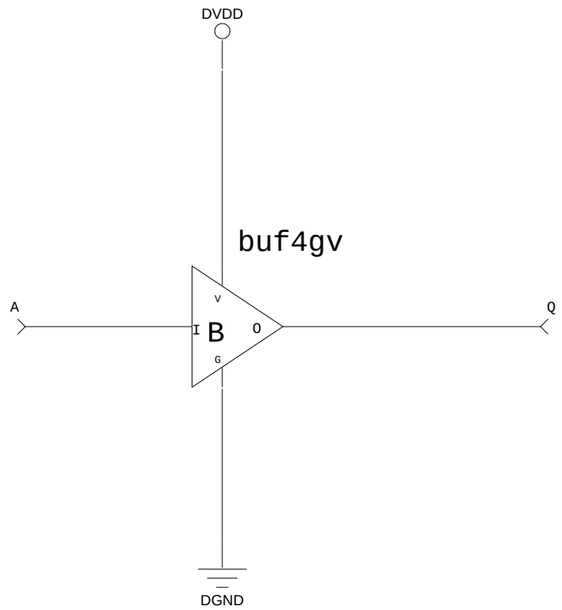

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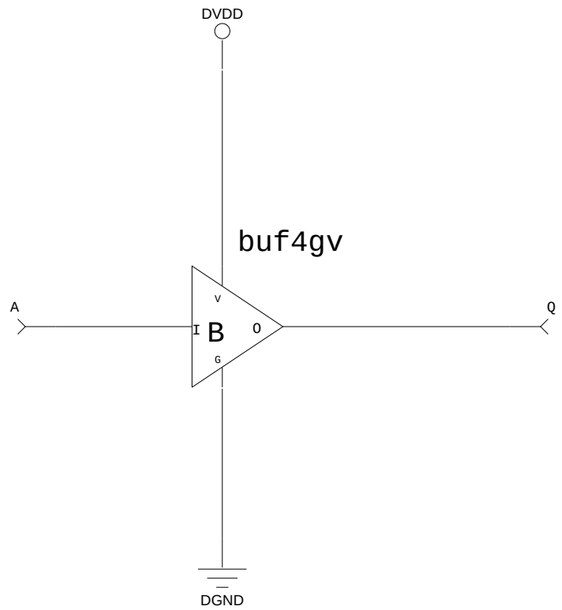

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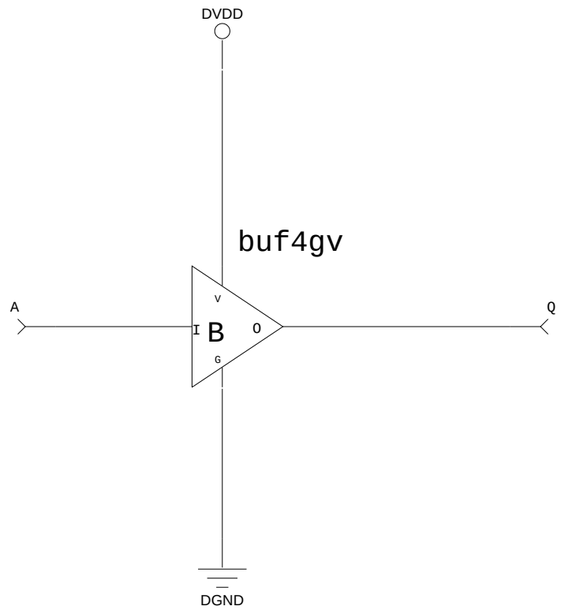

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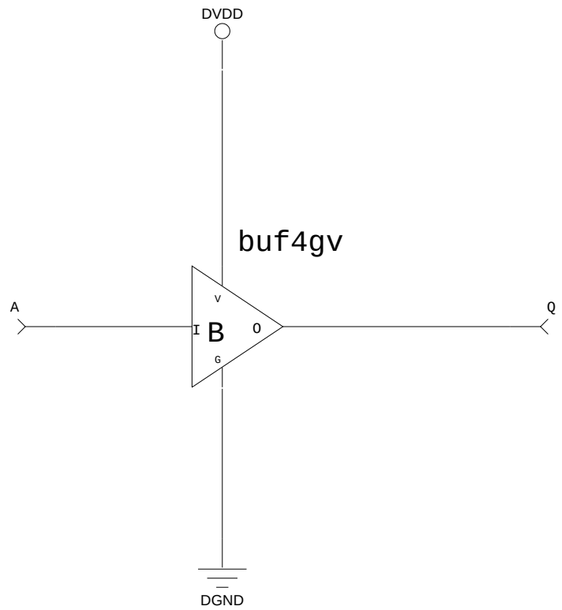

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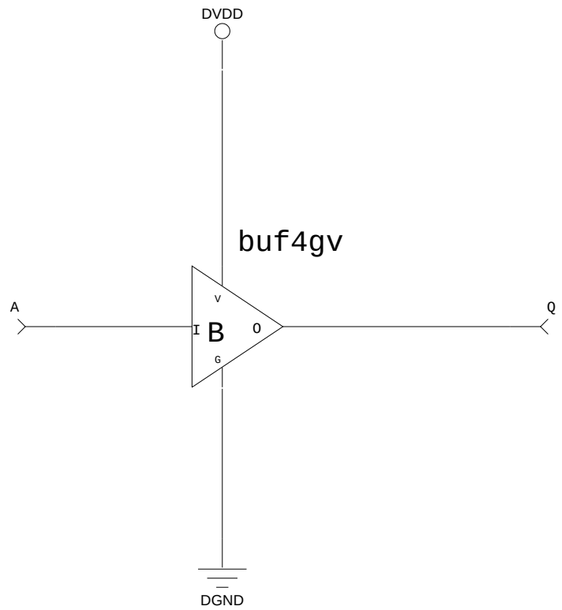

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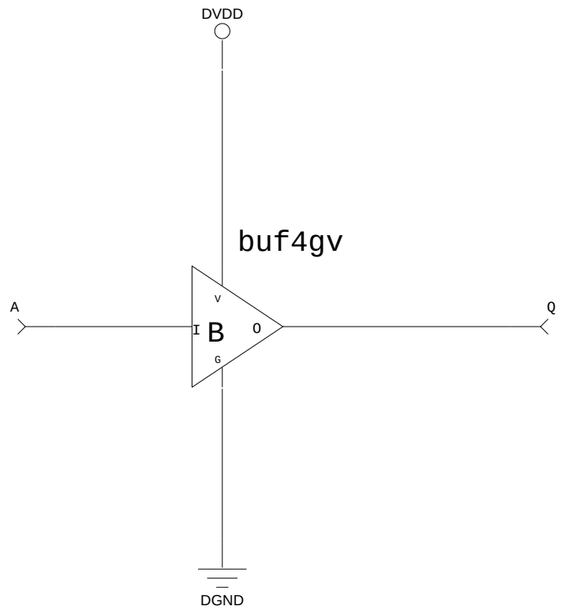

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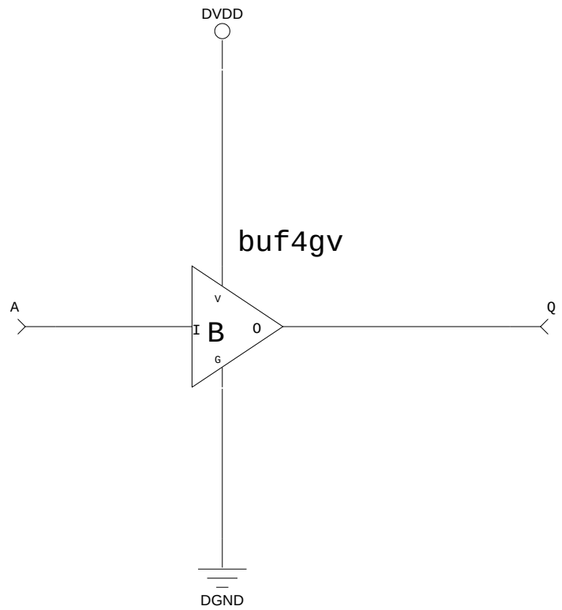

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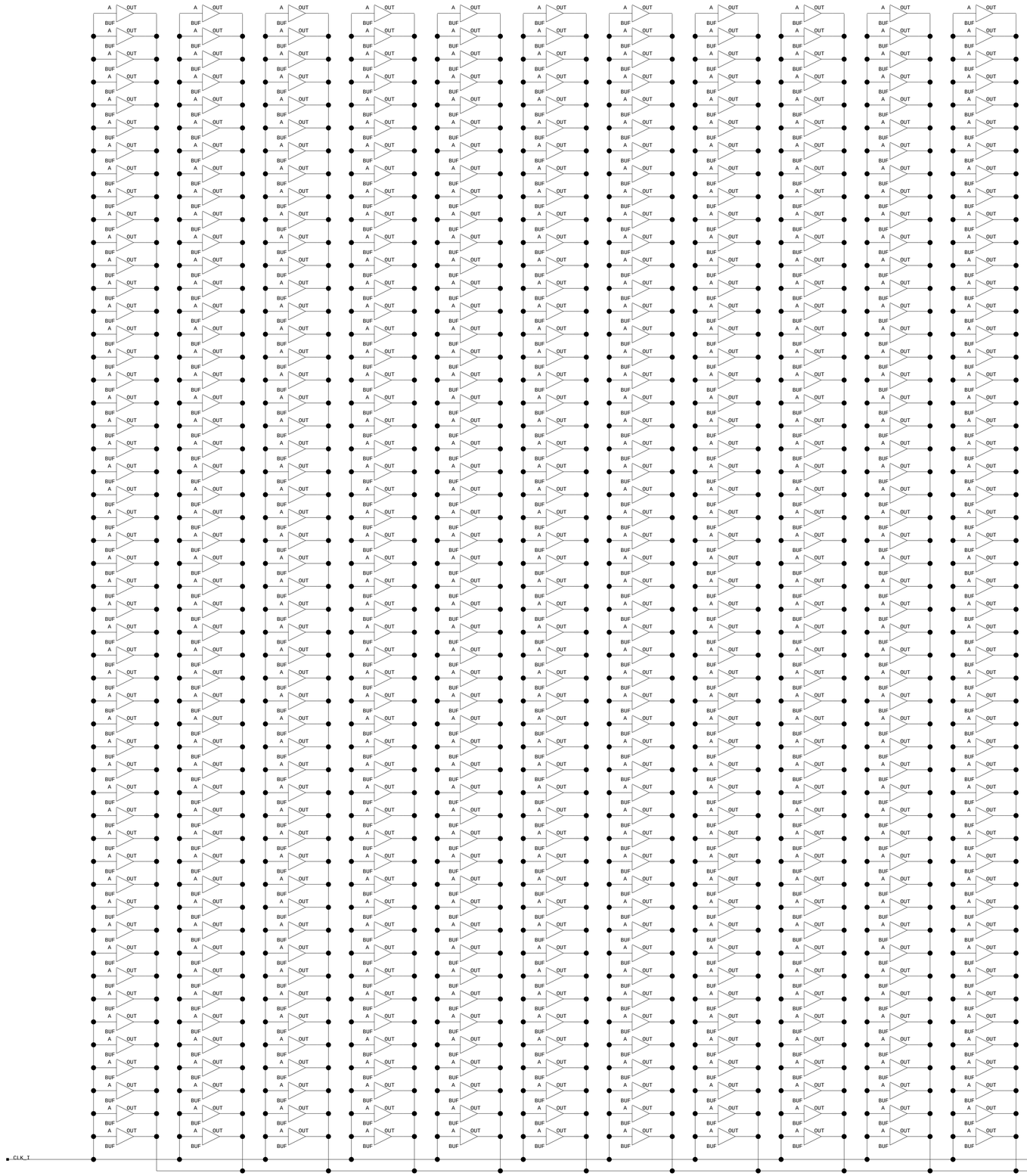

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	DATE	APPROVALS
ENGR		
DFTR		
CHKR		

SHEET OF

SLAC GLAST

GARC_VER2

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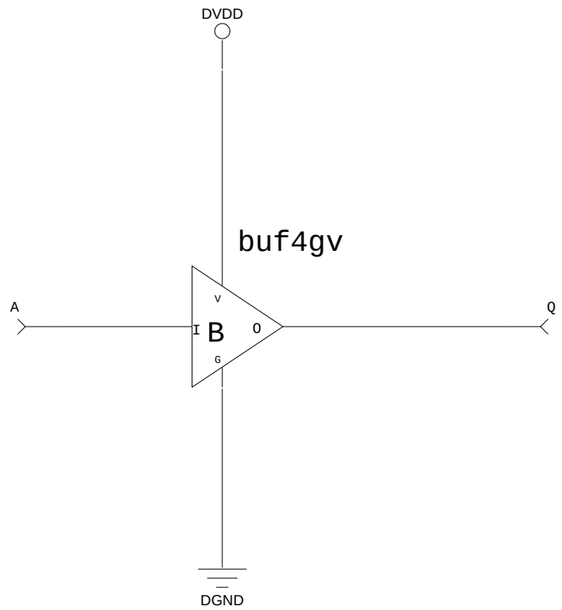

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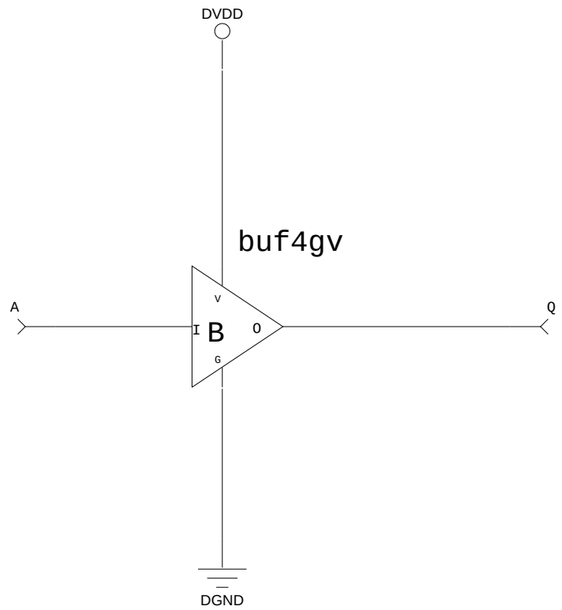

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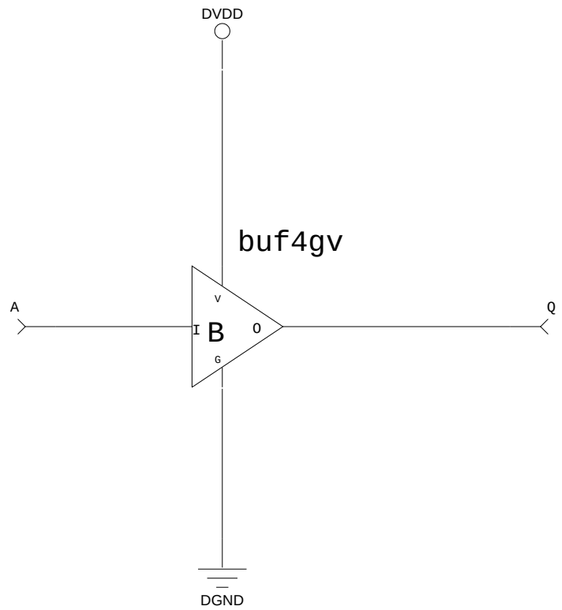

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DRAWN BY: D. Freytag (SLAC)

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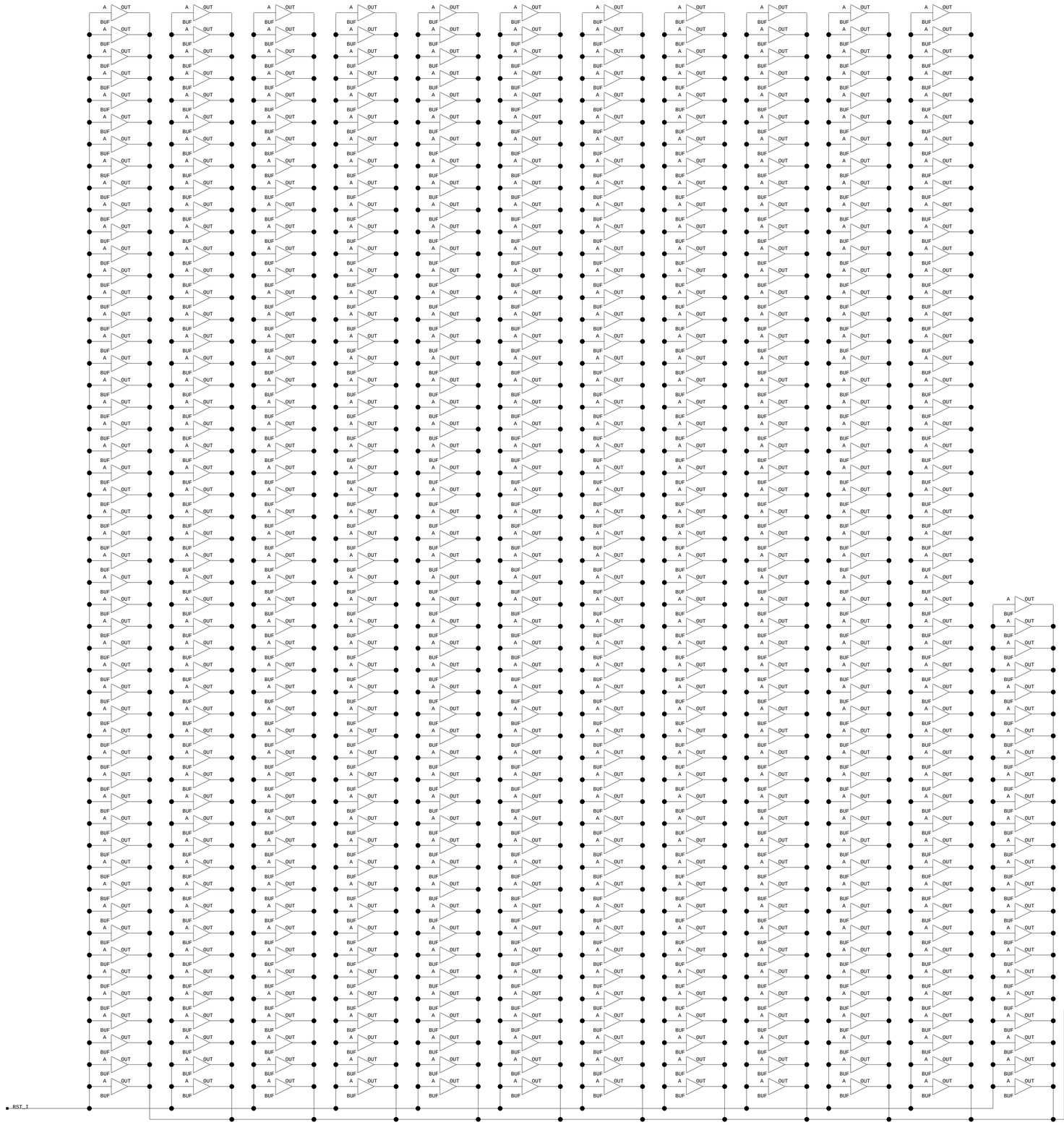
50 per column



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	DATE	APPROVALS
ENGR		
DFTR		
CHKR		

SHEET OF

SLAC GLAST

GARC_VER2

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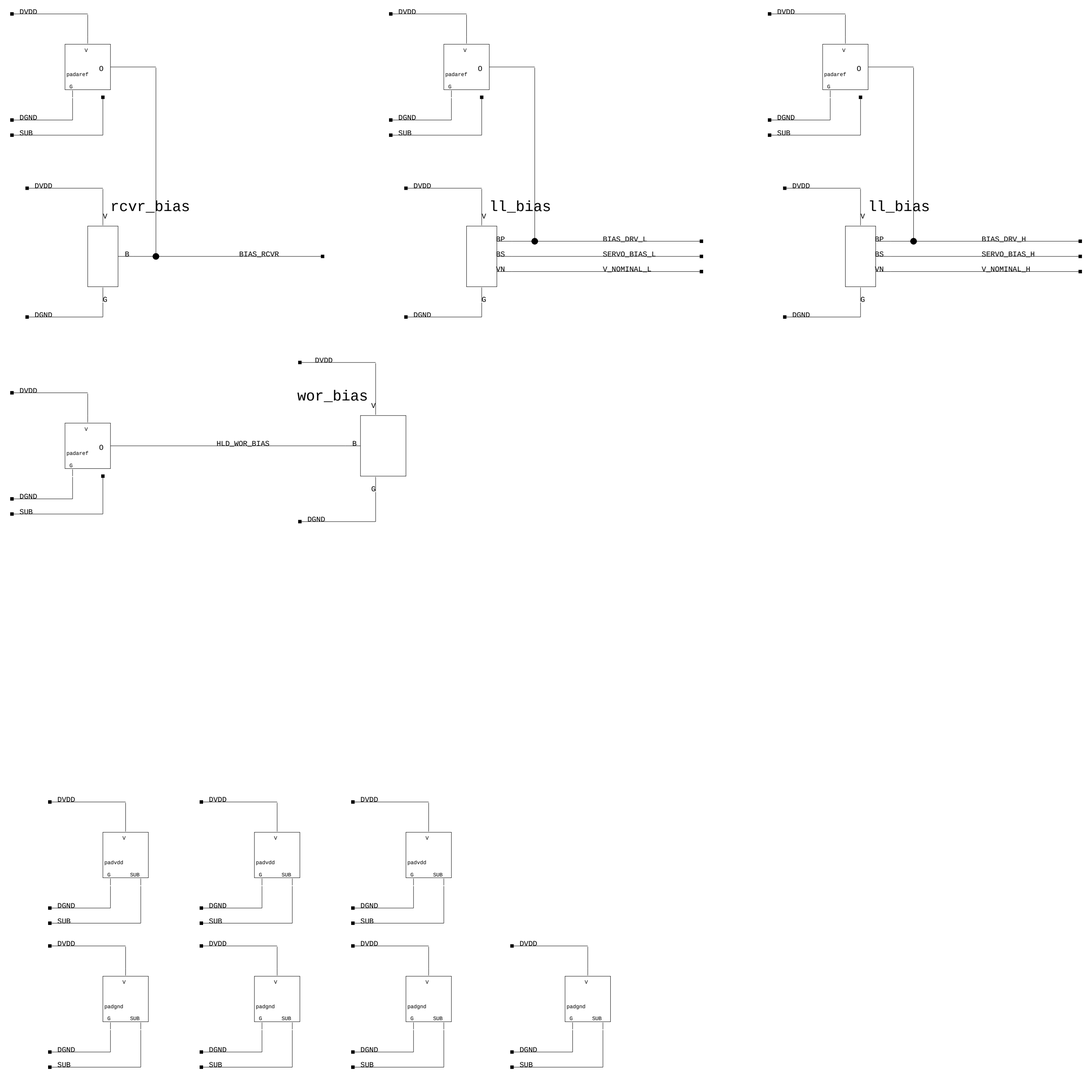

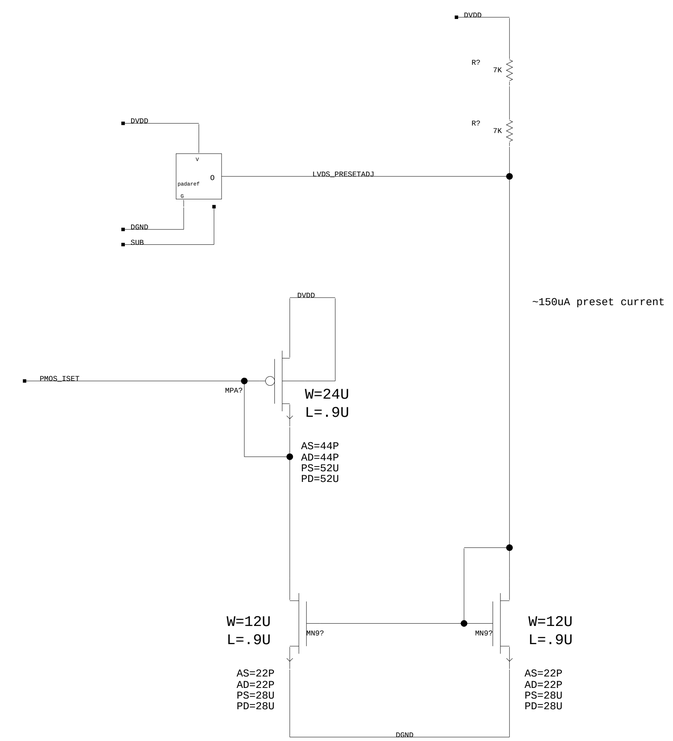

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	DATE	APPROVALS
ENGR		
DFTR		
CHKR		

SHEET OF

SLAC GLAST

GARC_VER2

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REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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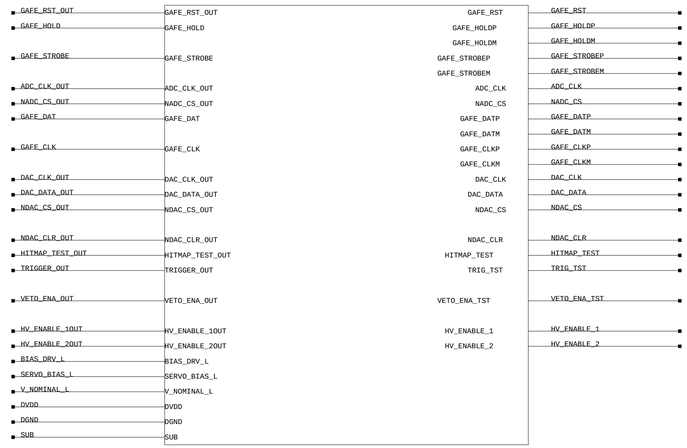

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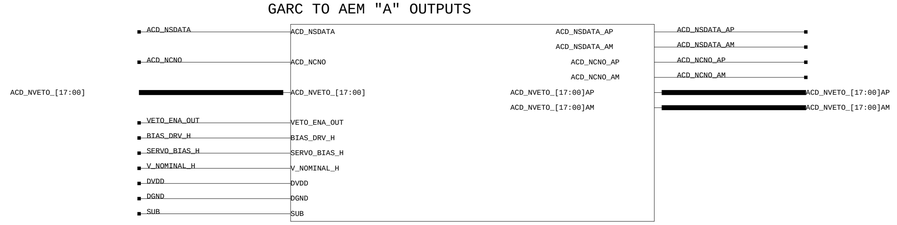

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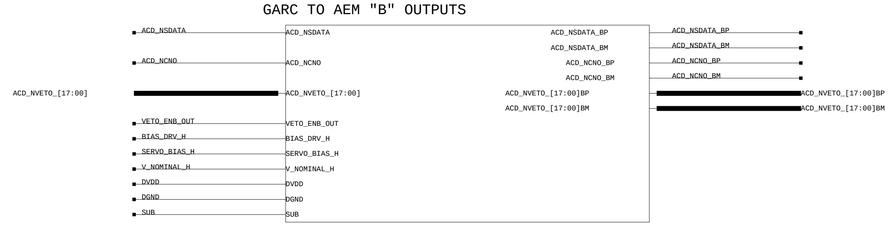

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	DATE	APPROVALS
ENGR		
DFTR		
CHKR		

SHEET OF

SLAC GLAST

GARC_VER2

GARC OUTPUTS

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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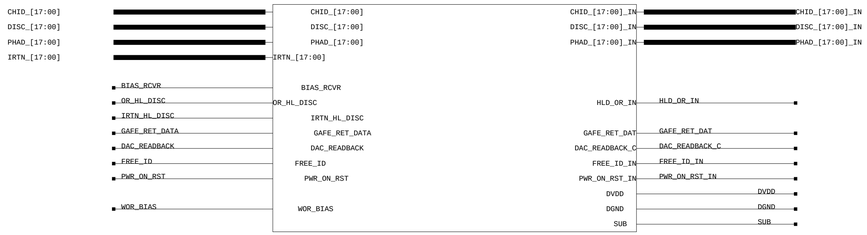

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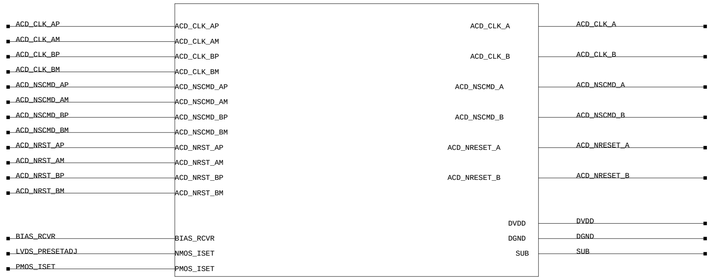

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	DATE	APPROVALS
ENGR		
DFTR		
CHKR		

SHEET OF

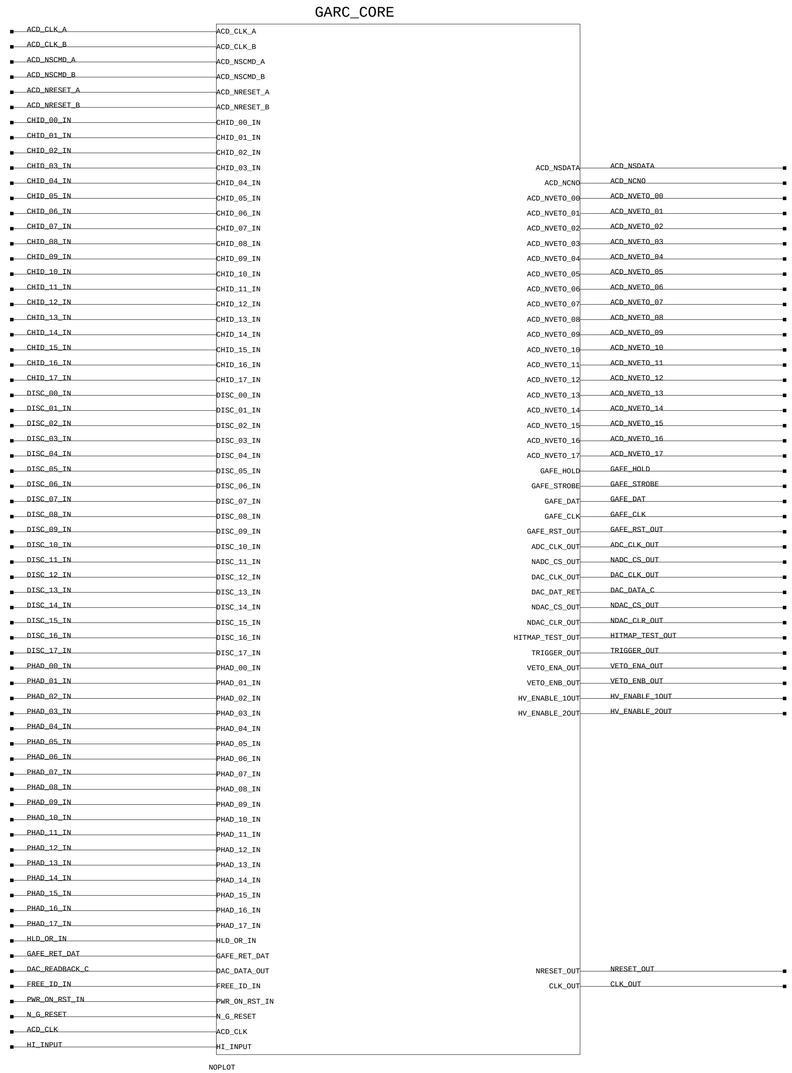
SLAC GLAST

GARC_VER2

INPUT PADS AND LVDS RECEIVERS

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE



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CHKR		

SHEET OF

SLAC GLAST

GARC_VER2

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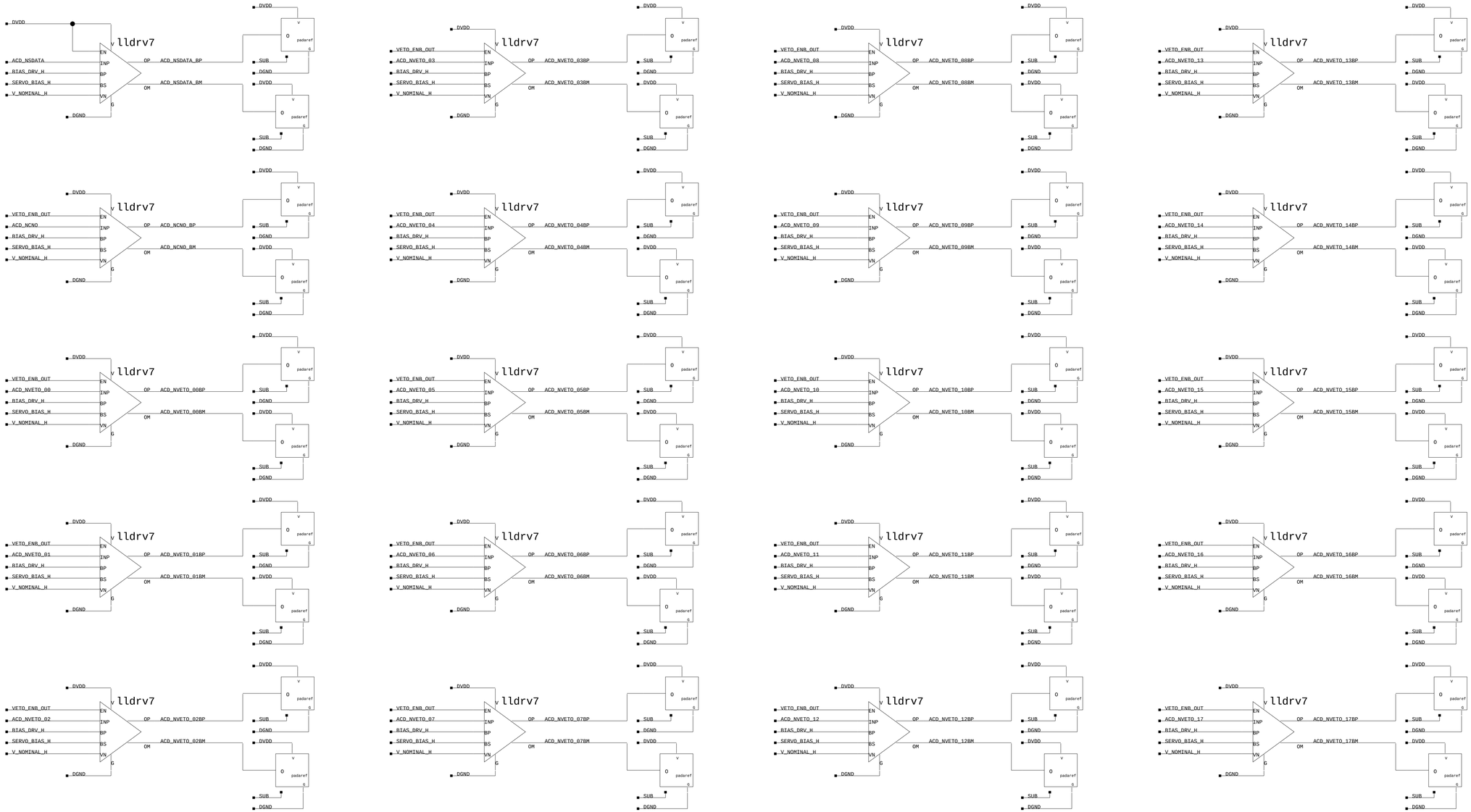

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	DATE	APPROVALS
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DFTR		
CHKR		

SHEET OF

SLAC GLAST

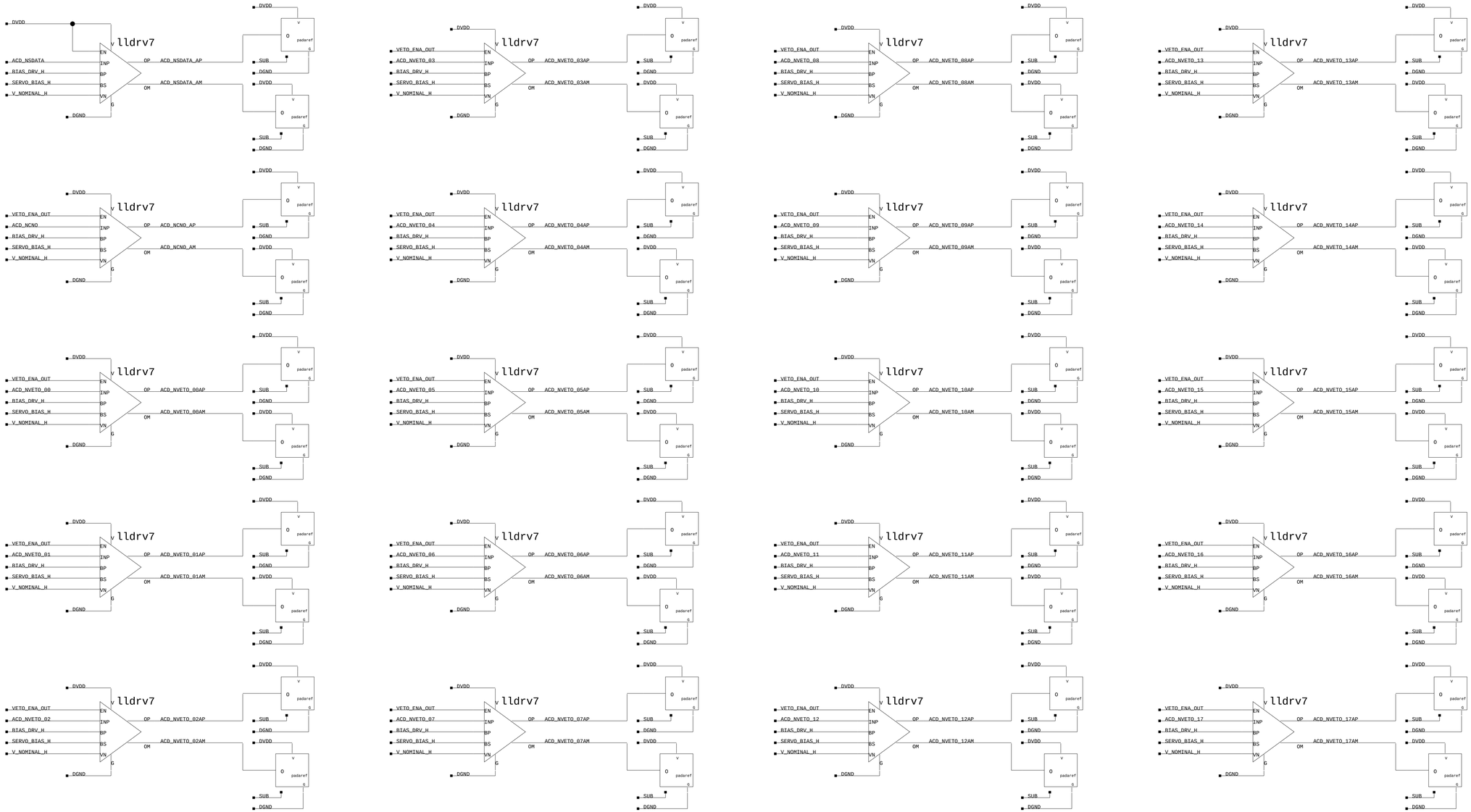
GARC_VER2

GARC AEM B OUTPUTS AND

LVDS DRIVERS (7 mA)

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE



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DFTR		
CHKR		

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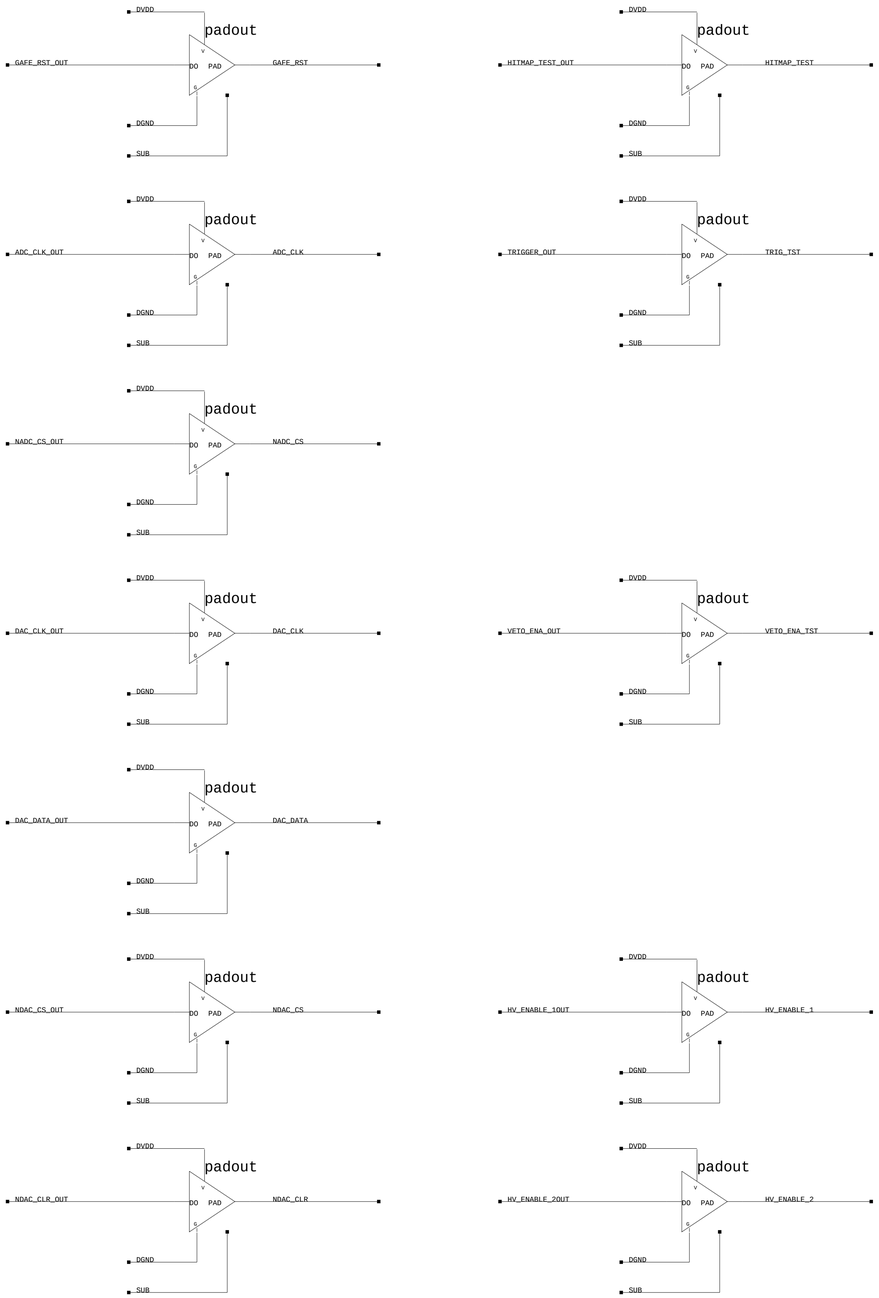
GARC_VER2

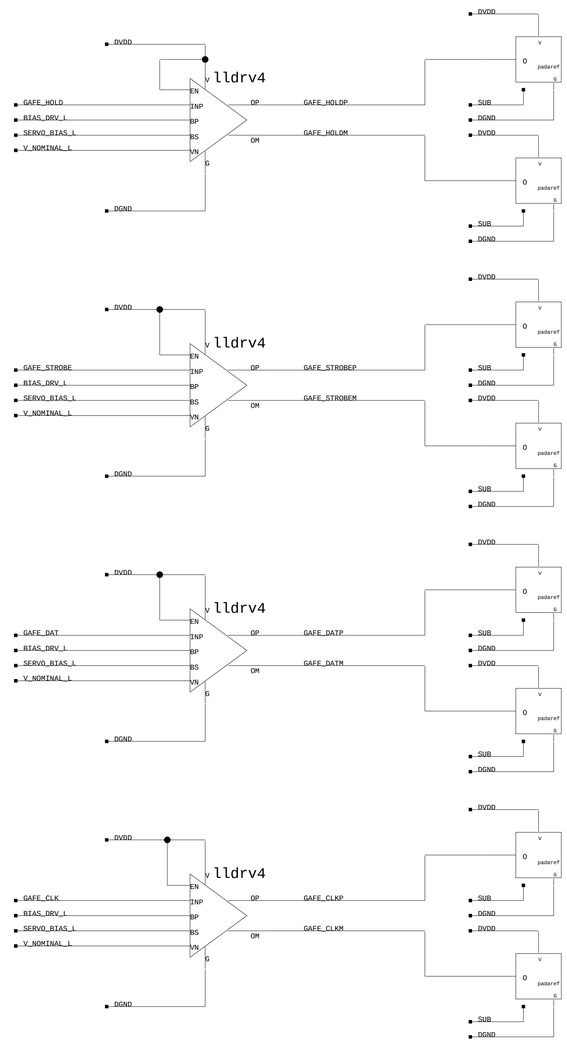
GARC AEM A OUTPUTS AND

LVDS DRIVERS (7 mA)

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE





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CHKR		

SHEET OF

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GARC_VER2
GAFE OUTPUT PADS
AND LVDS DRIVERS

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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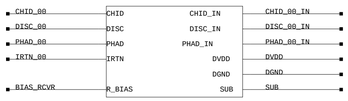

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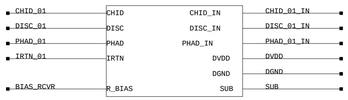

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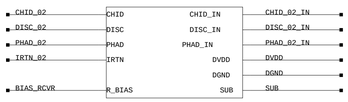

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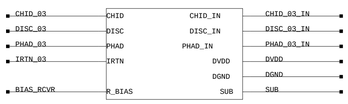

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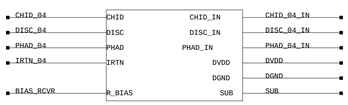

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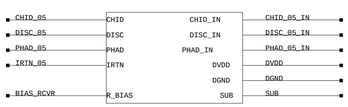

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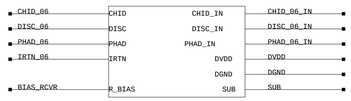

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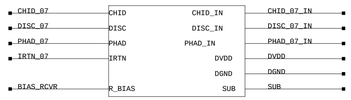

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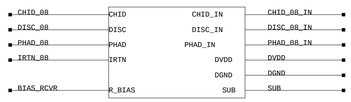

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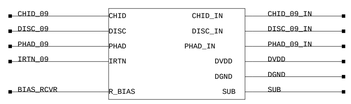

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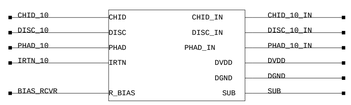

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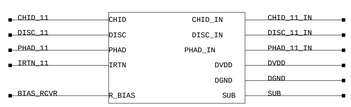

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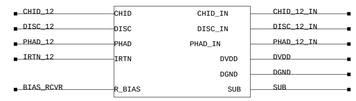

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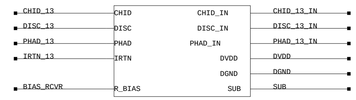
3 LVDS RCVR'S



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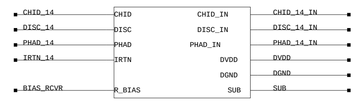

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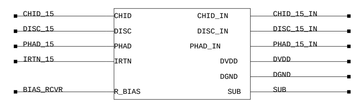

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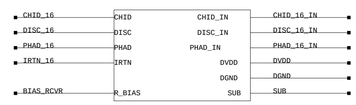

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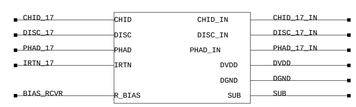

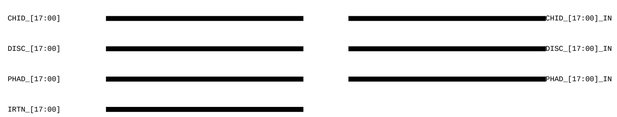

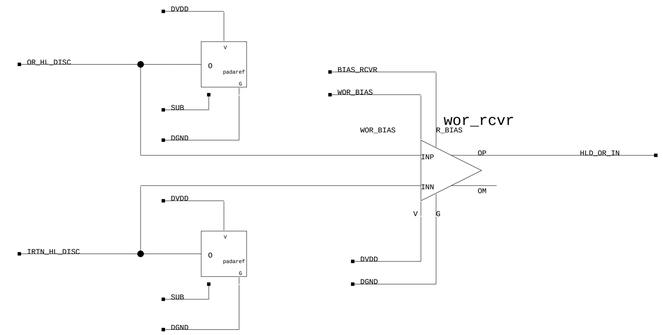

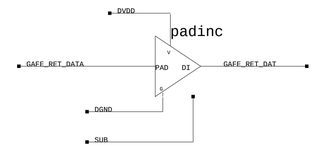

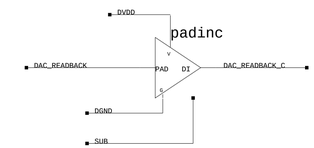

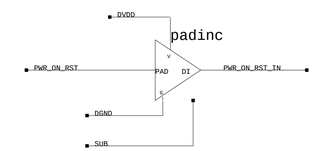

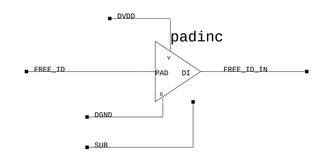

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	DATE	APPROVALS
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SHEET OF

SLAC GLAST
GARC_VER2
GAFE INPUT PADS AND
LVDS RECEIVERS

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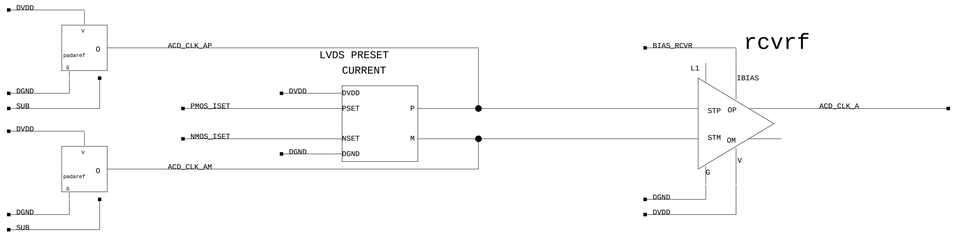
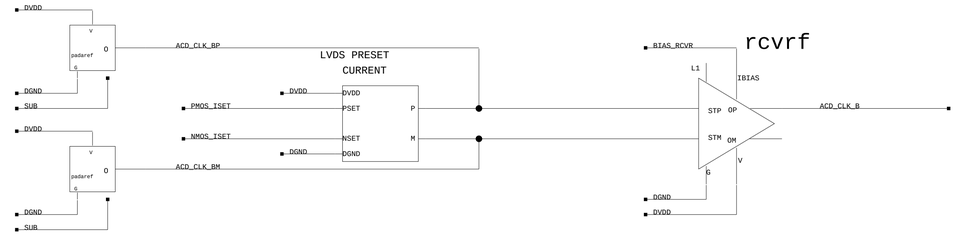
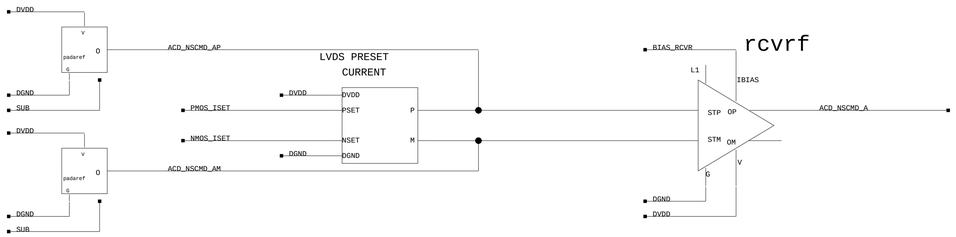
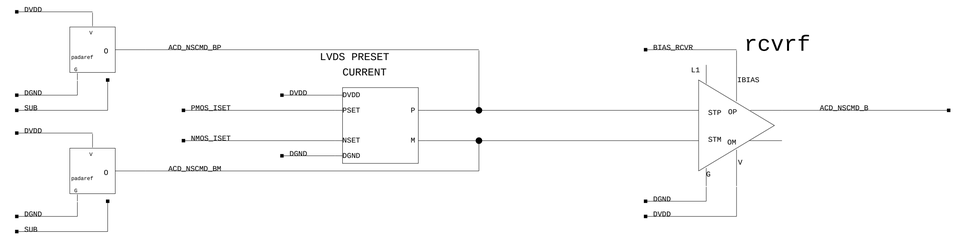
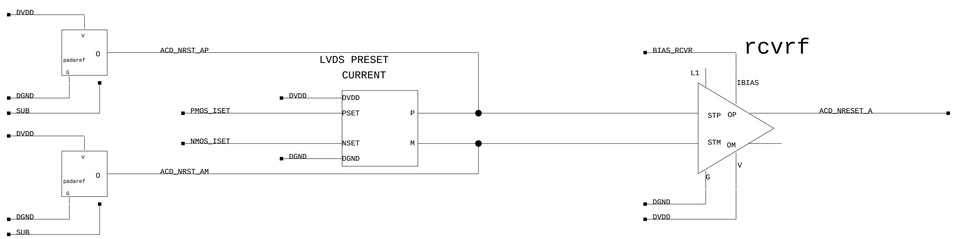
REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

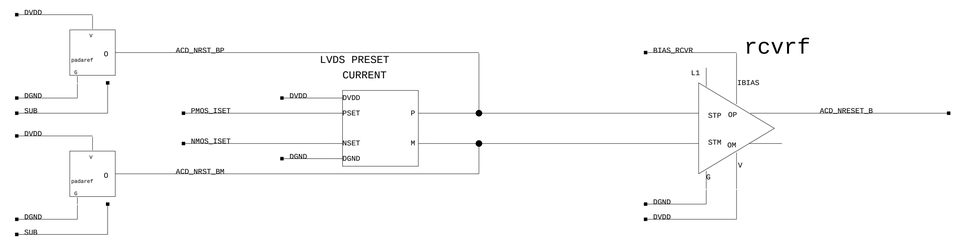












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SHEET OF

SLAC GLAST

GARC_VER2

GARC AEM INPUTS

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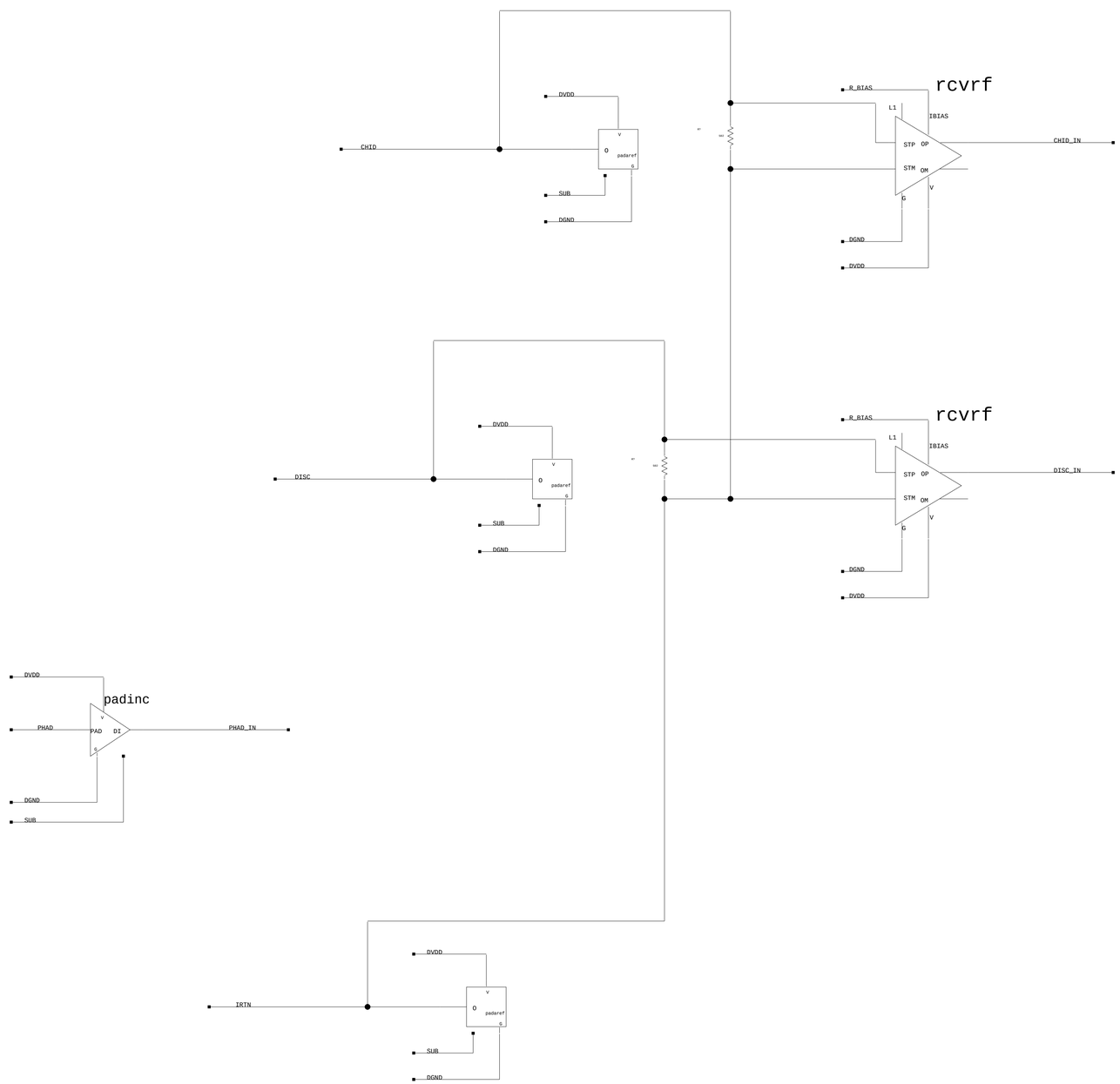

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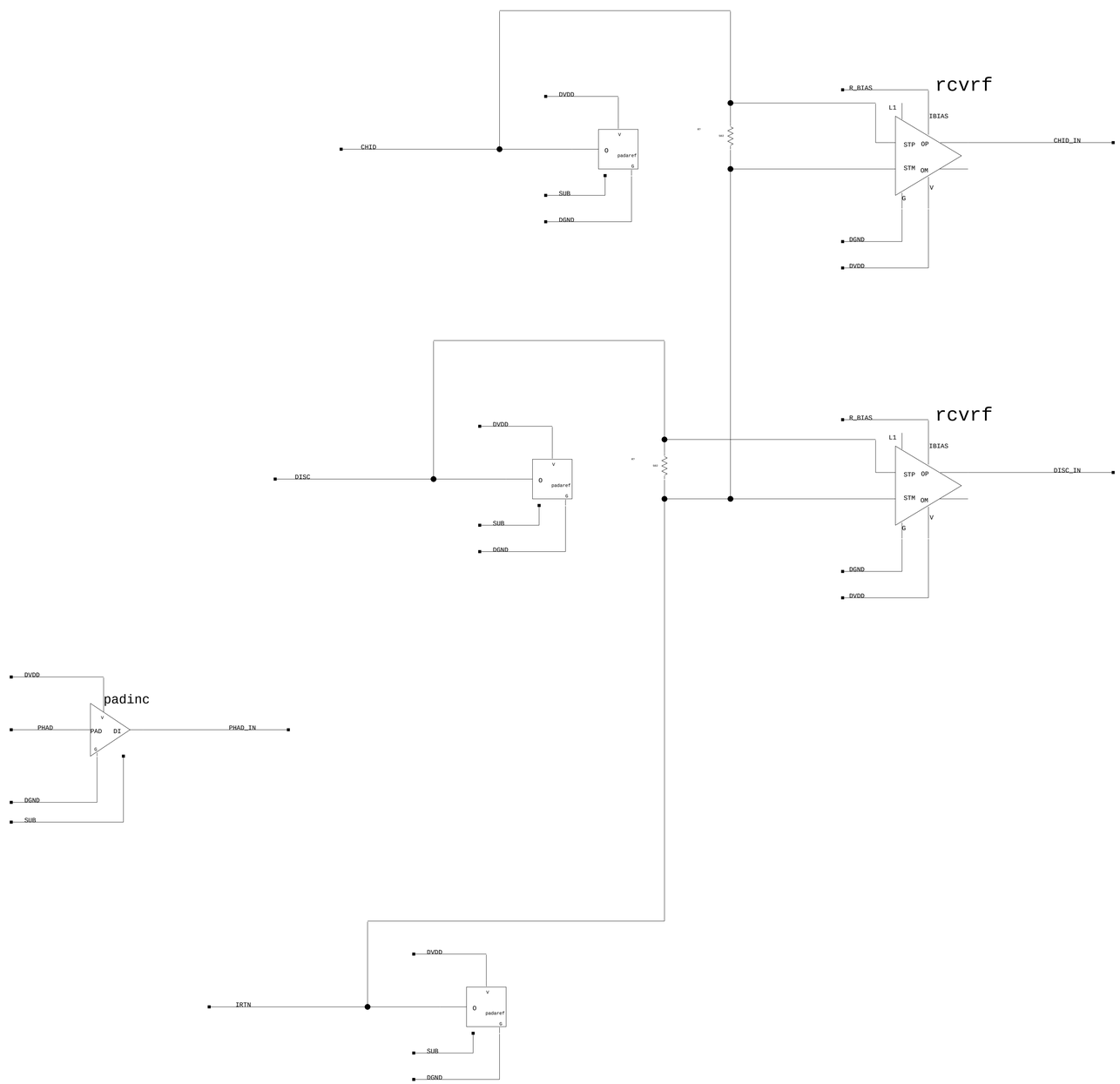

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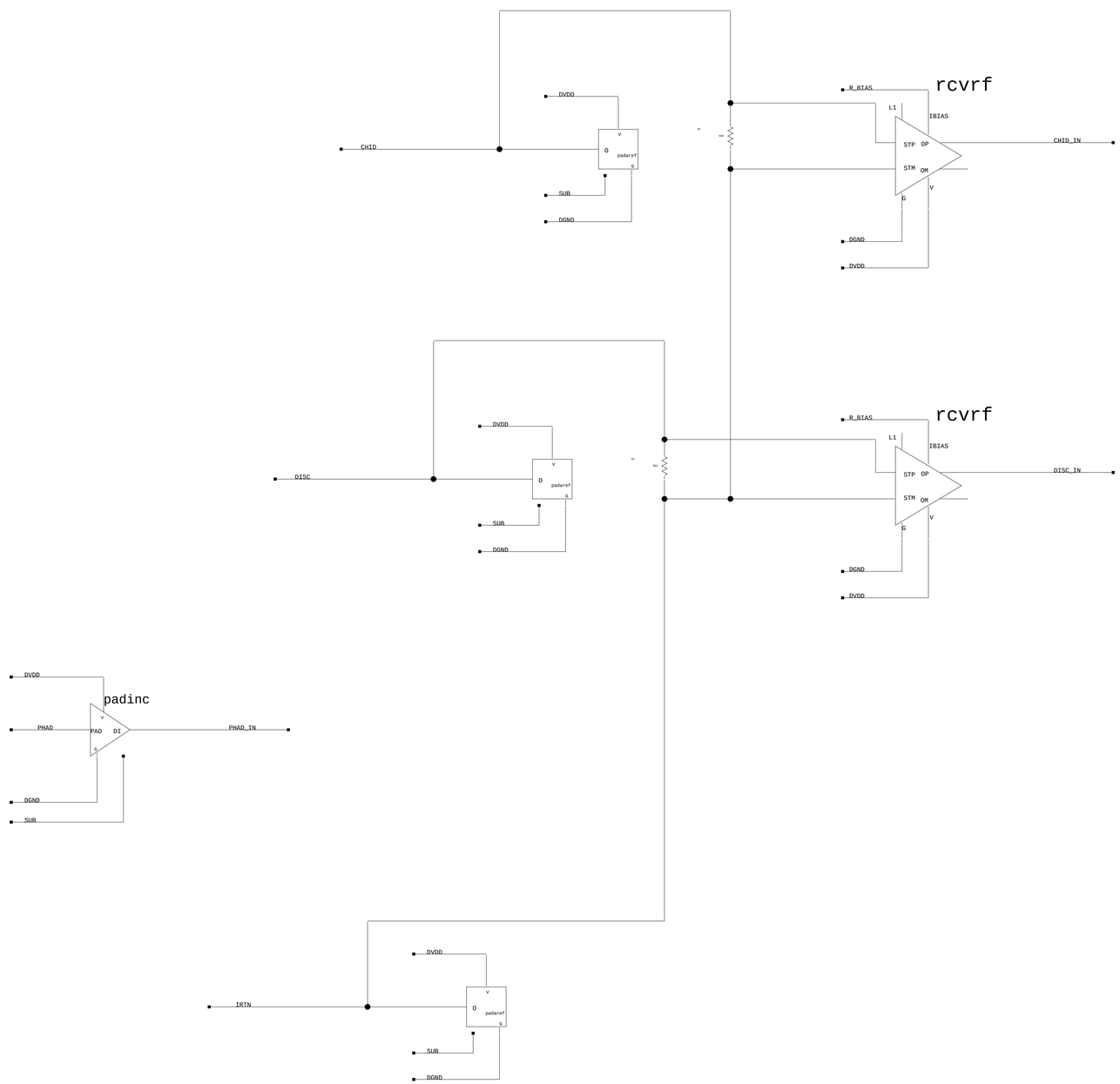

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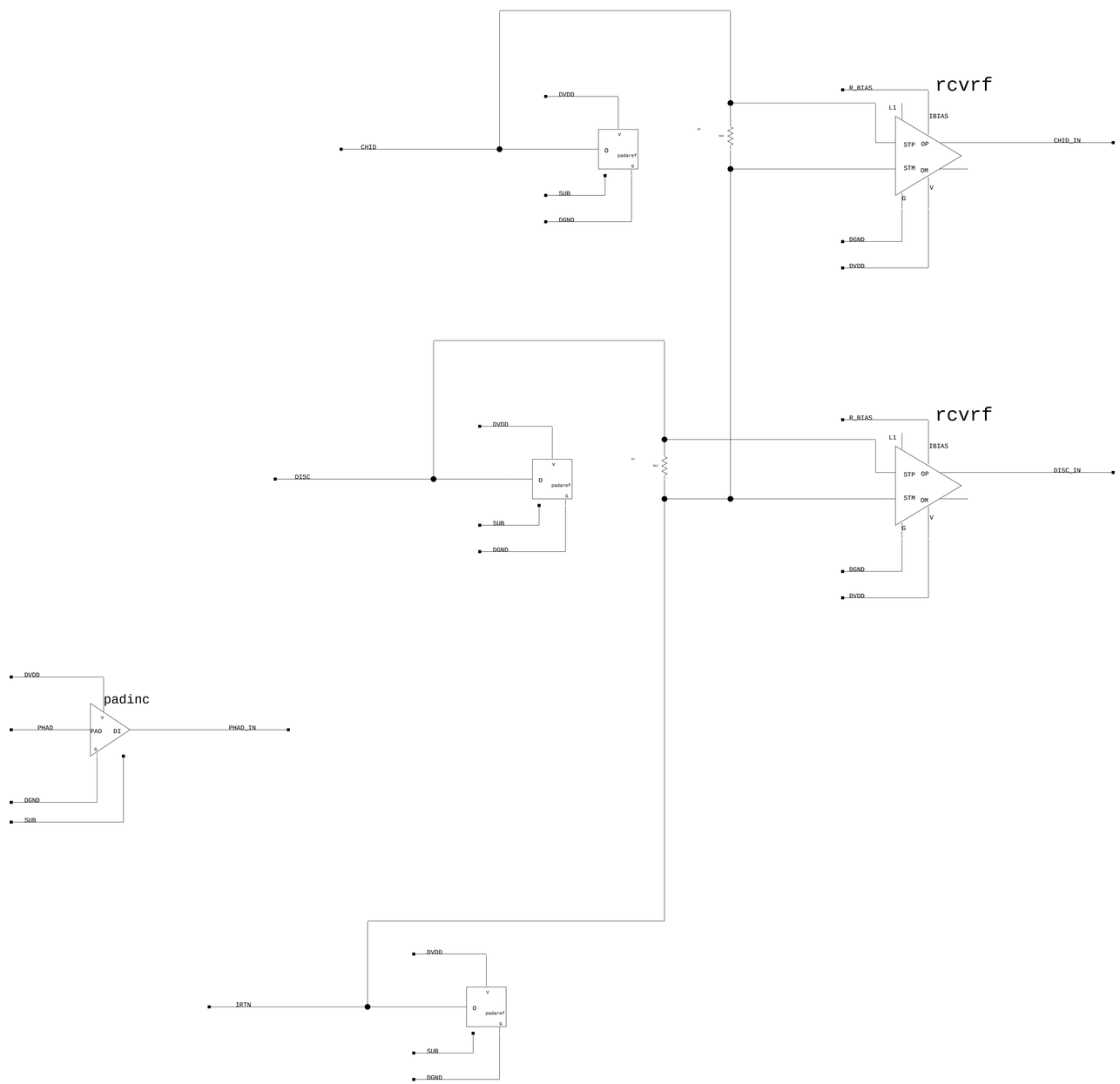

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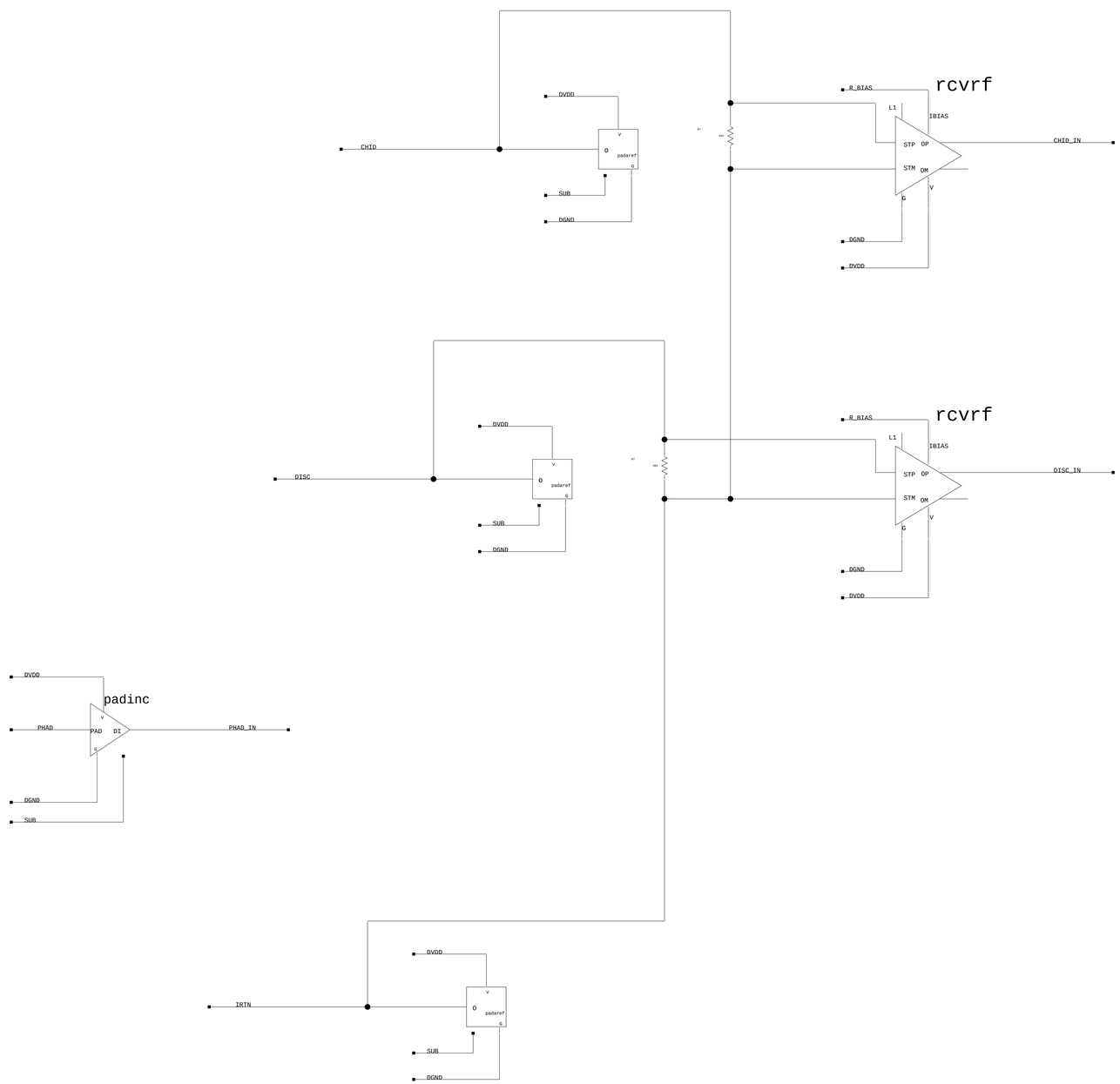

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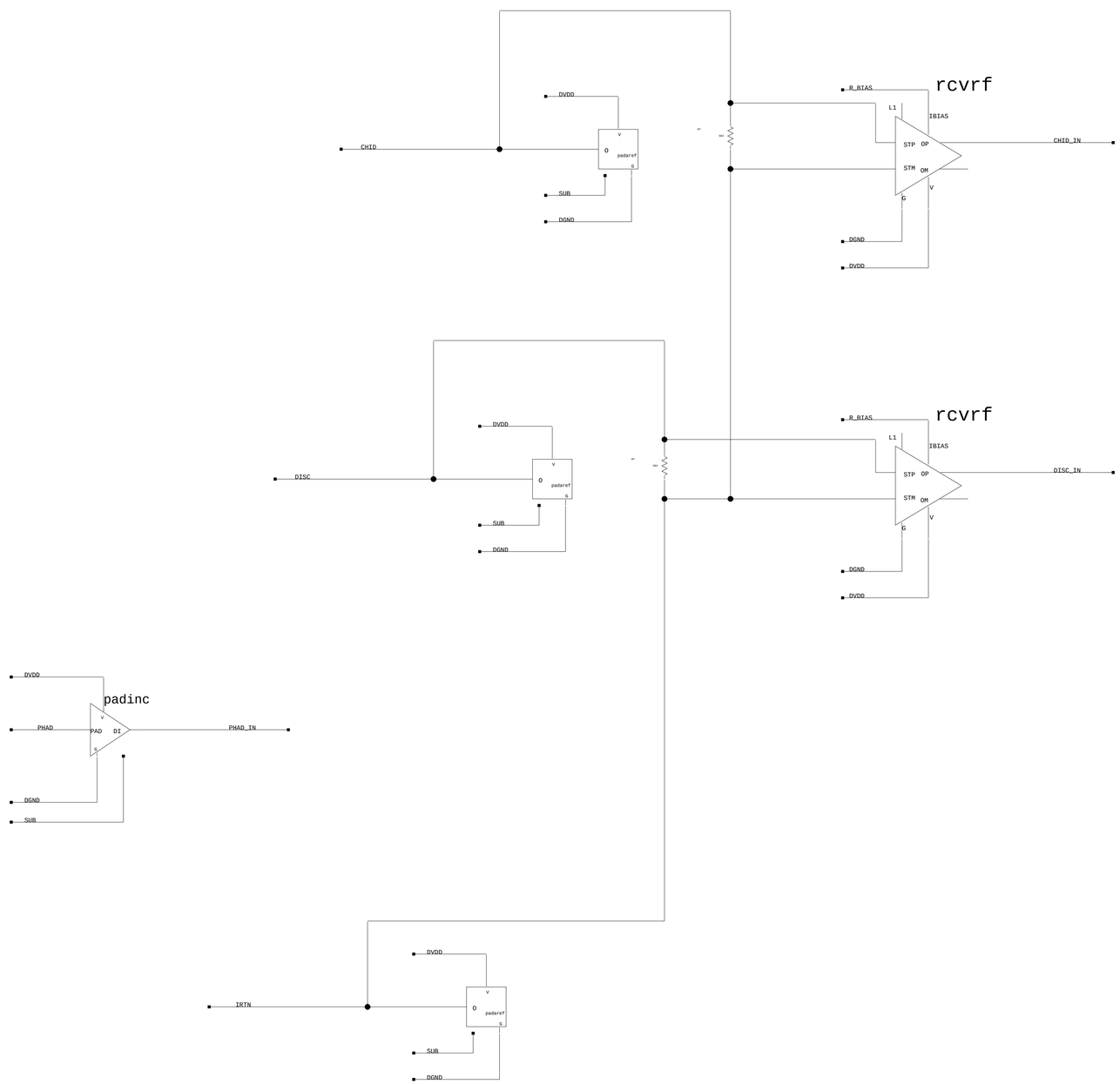

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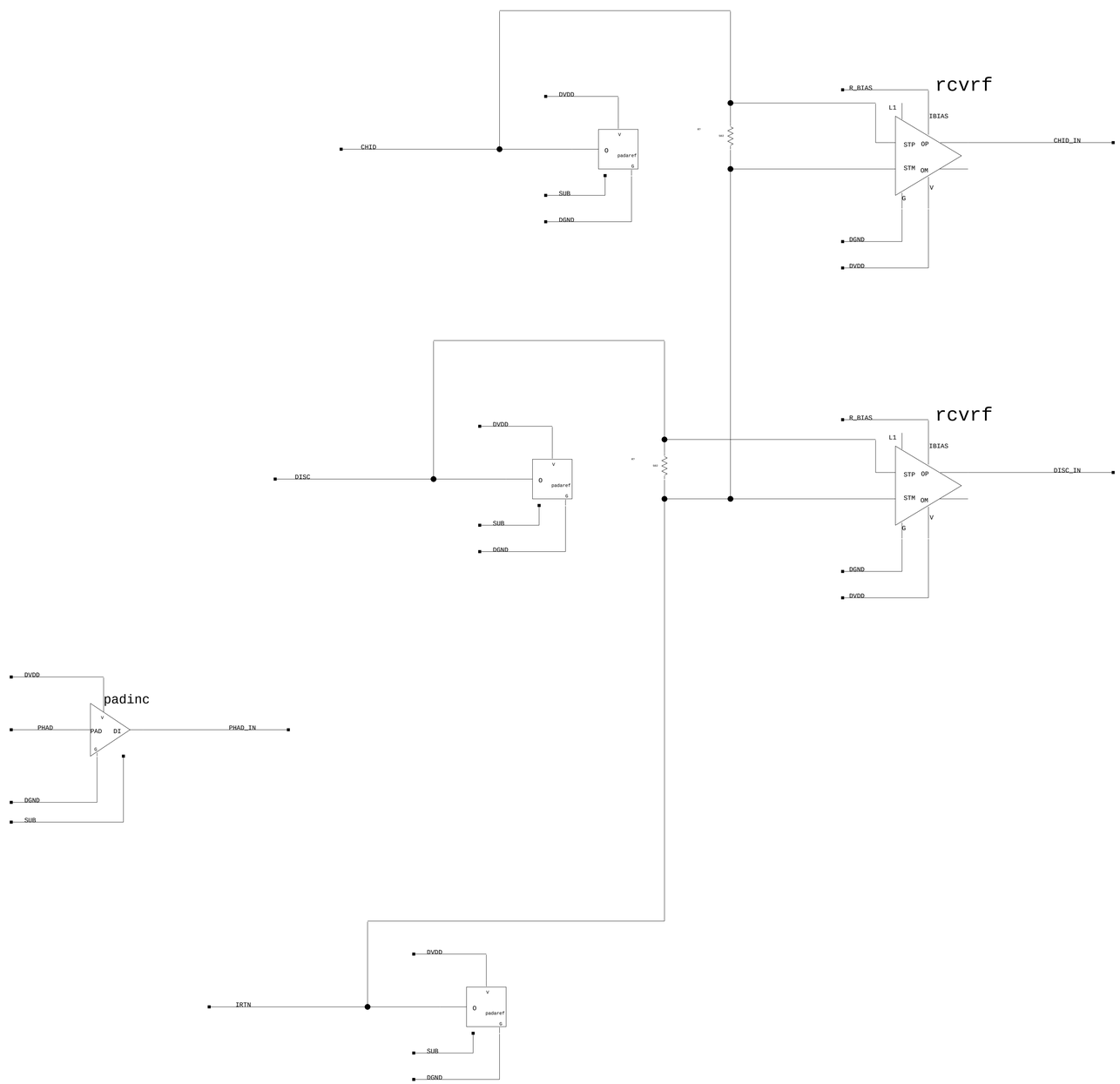

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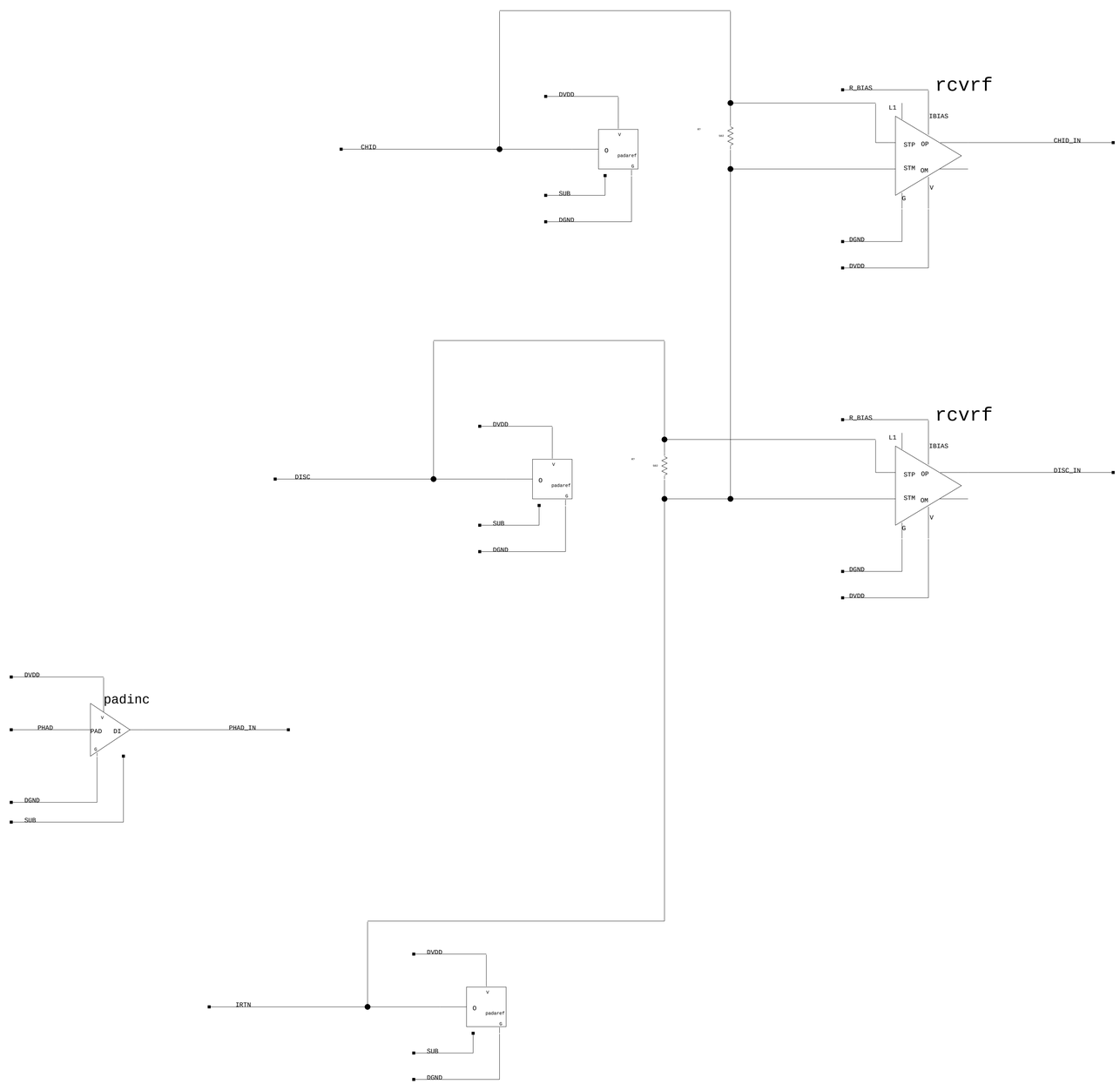

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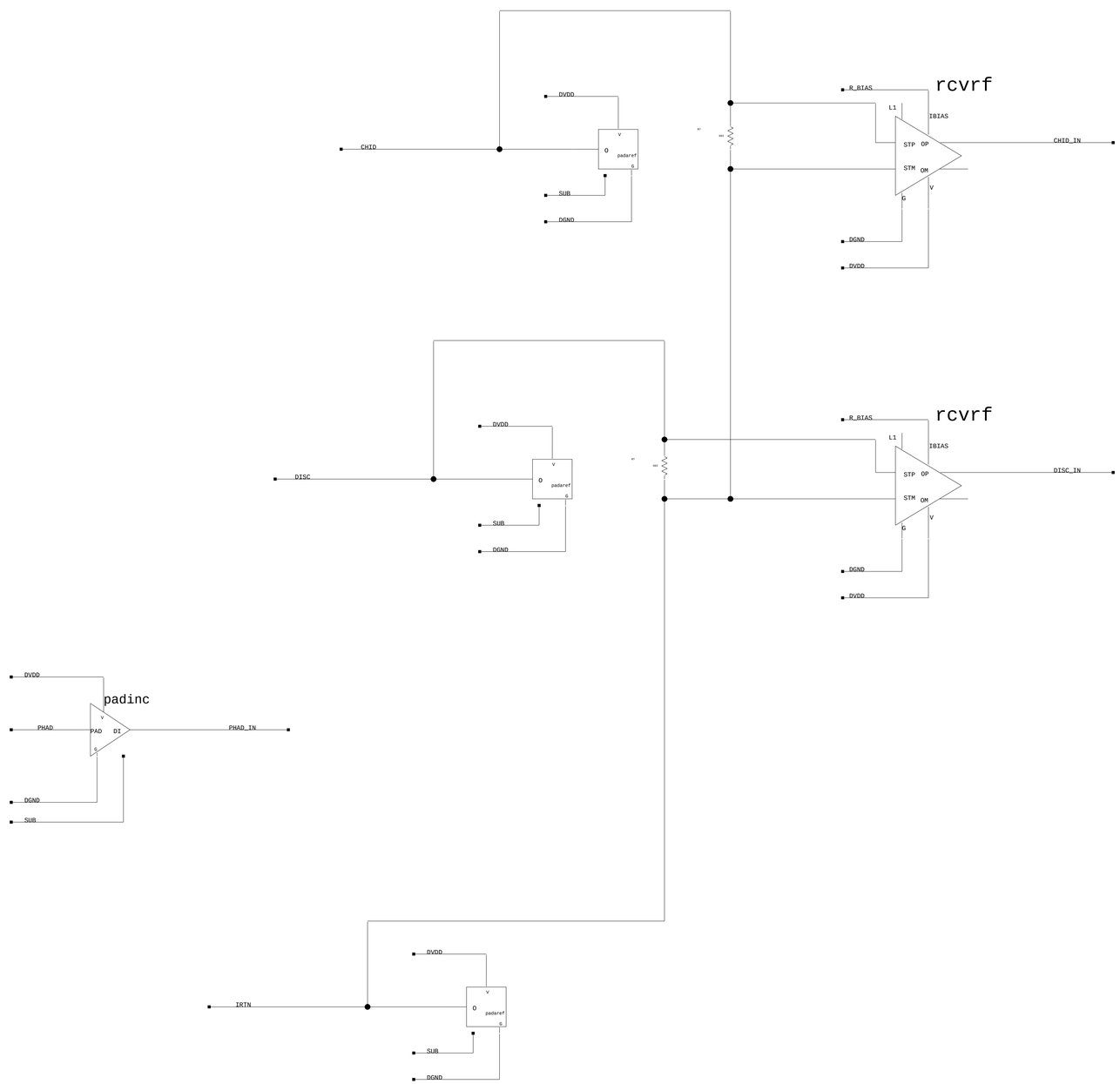

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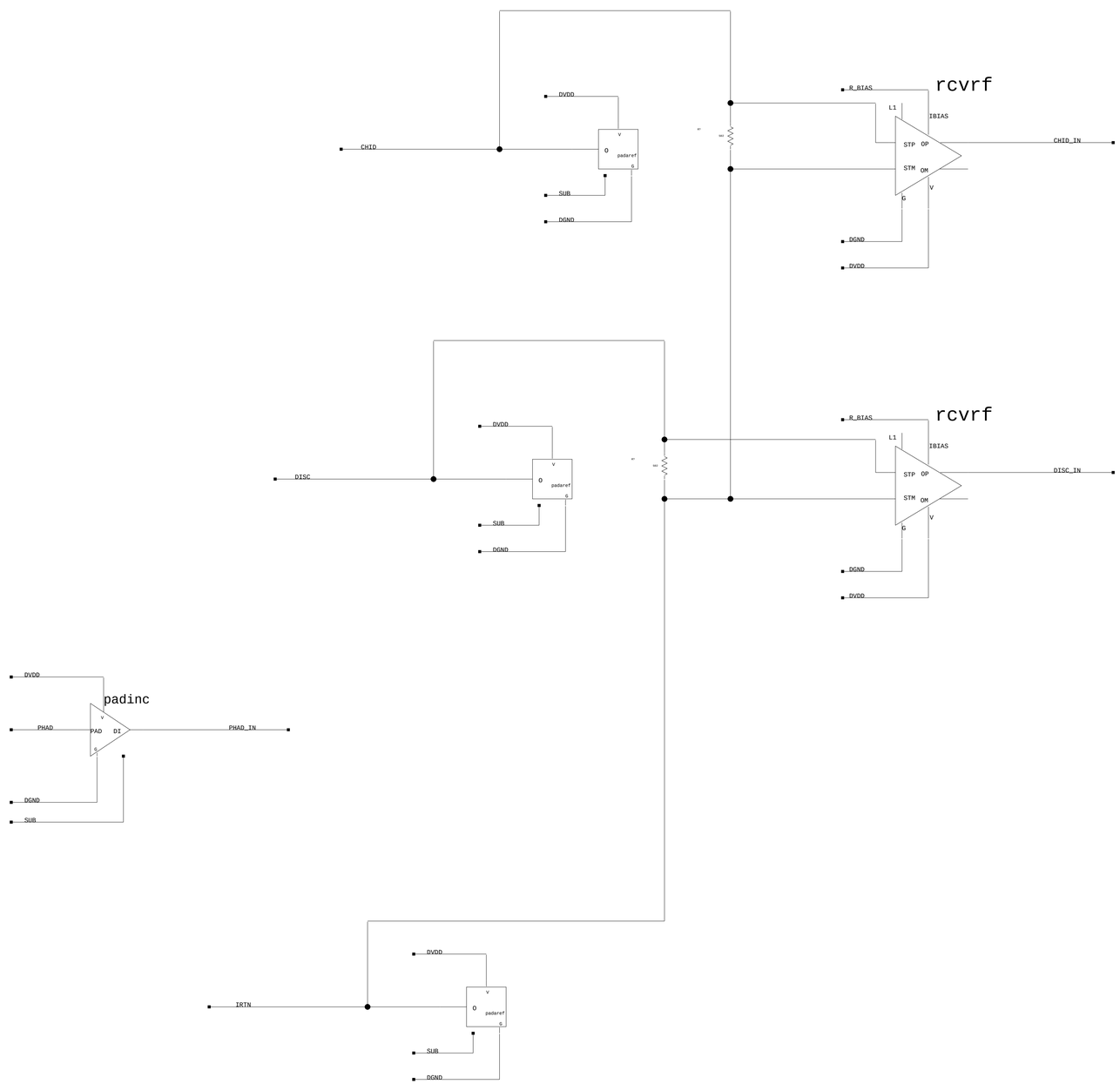

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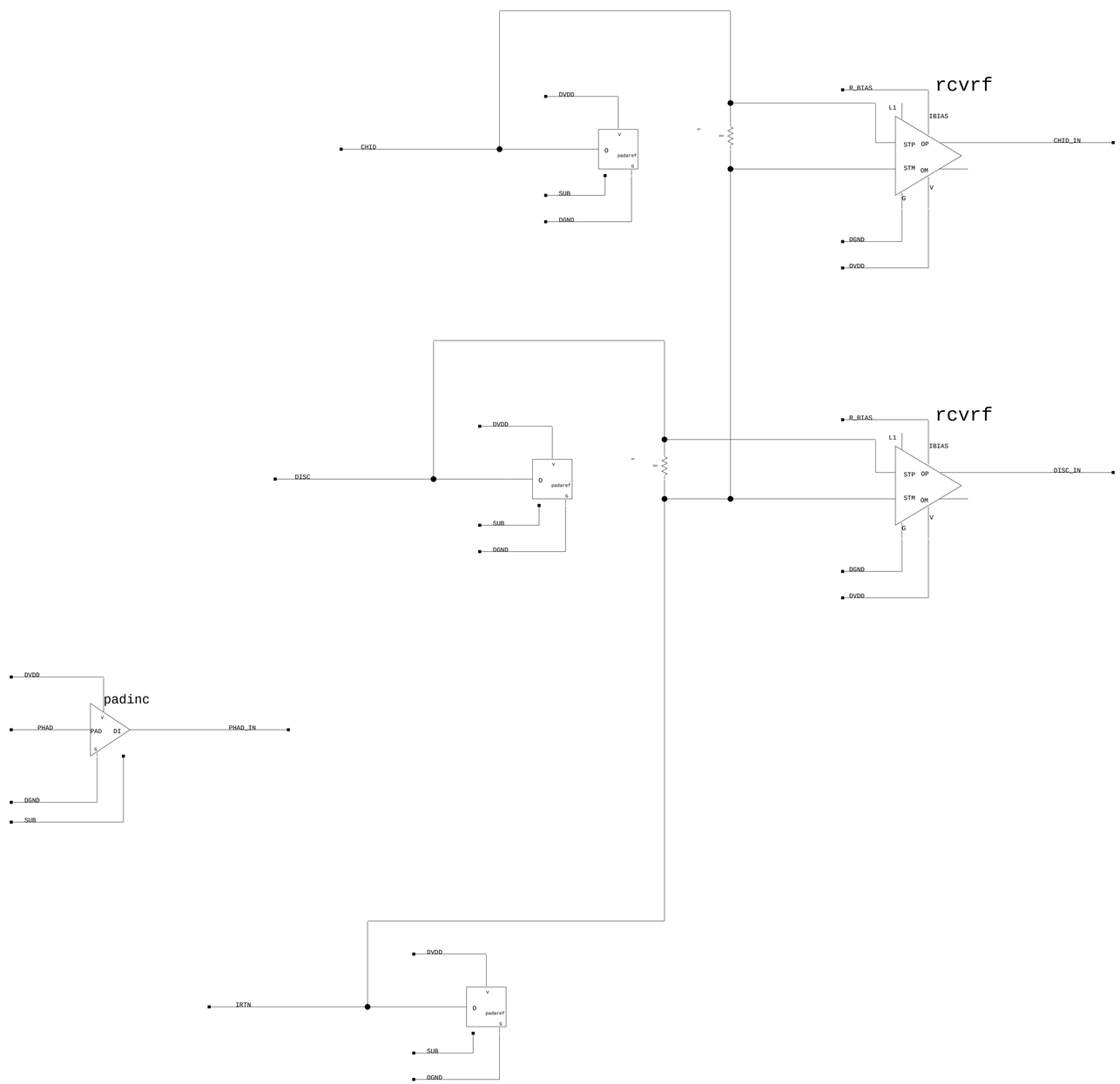

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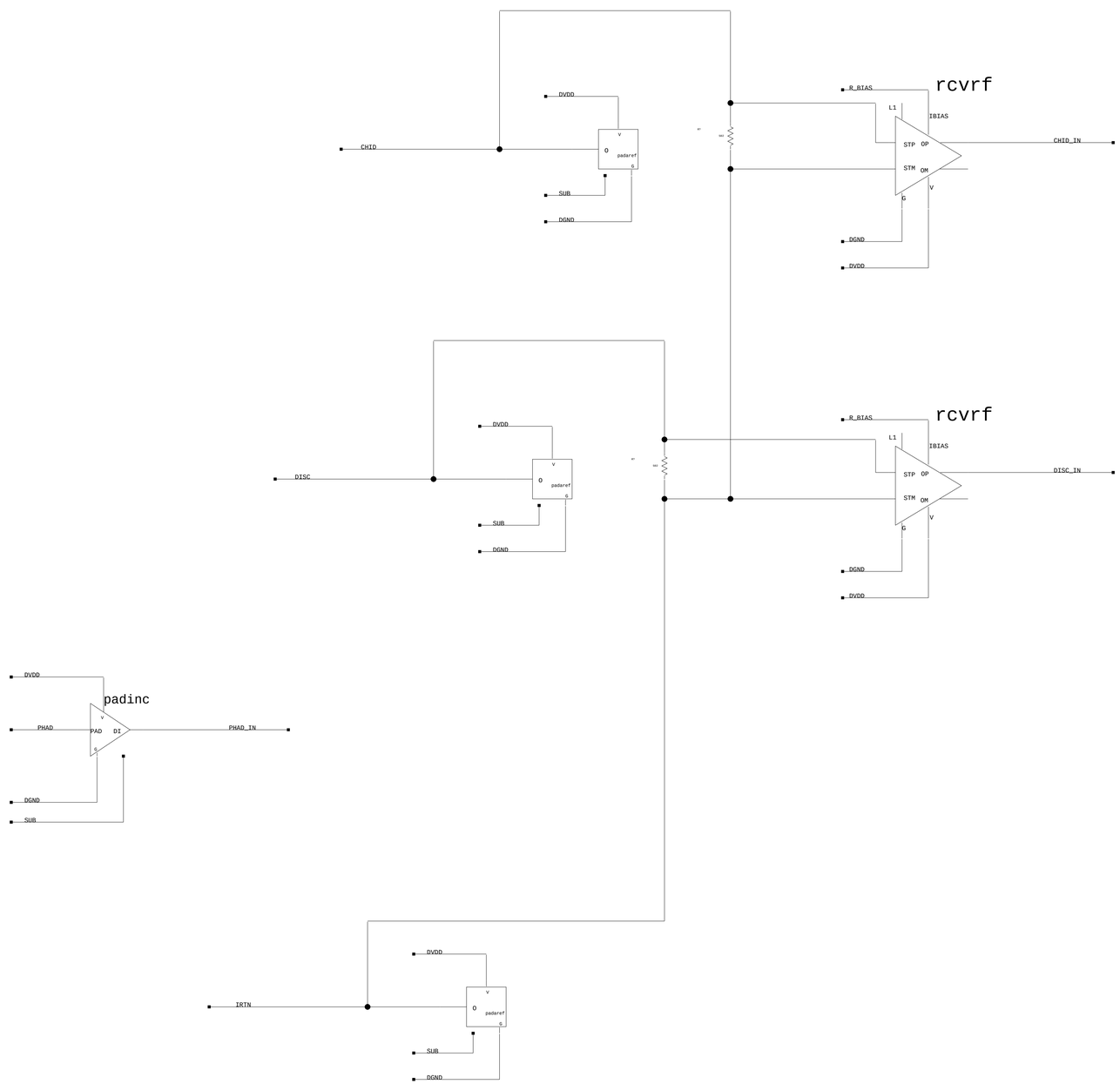

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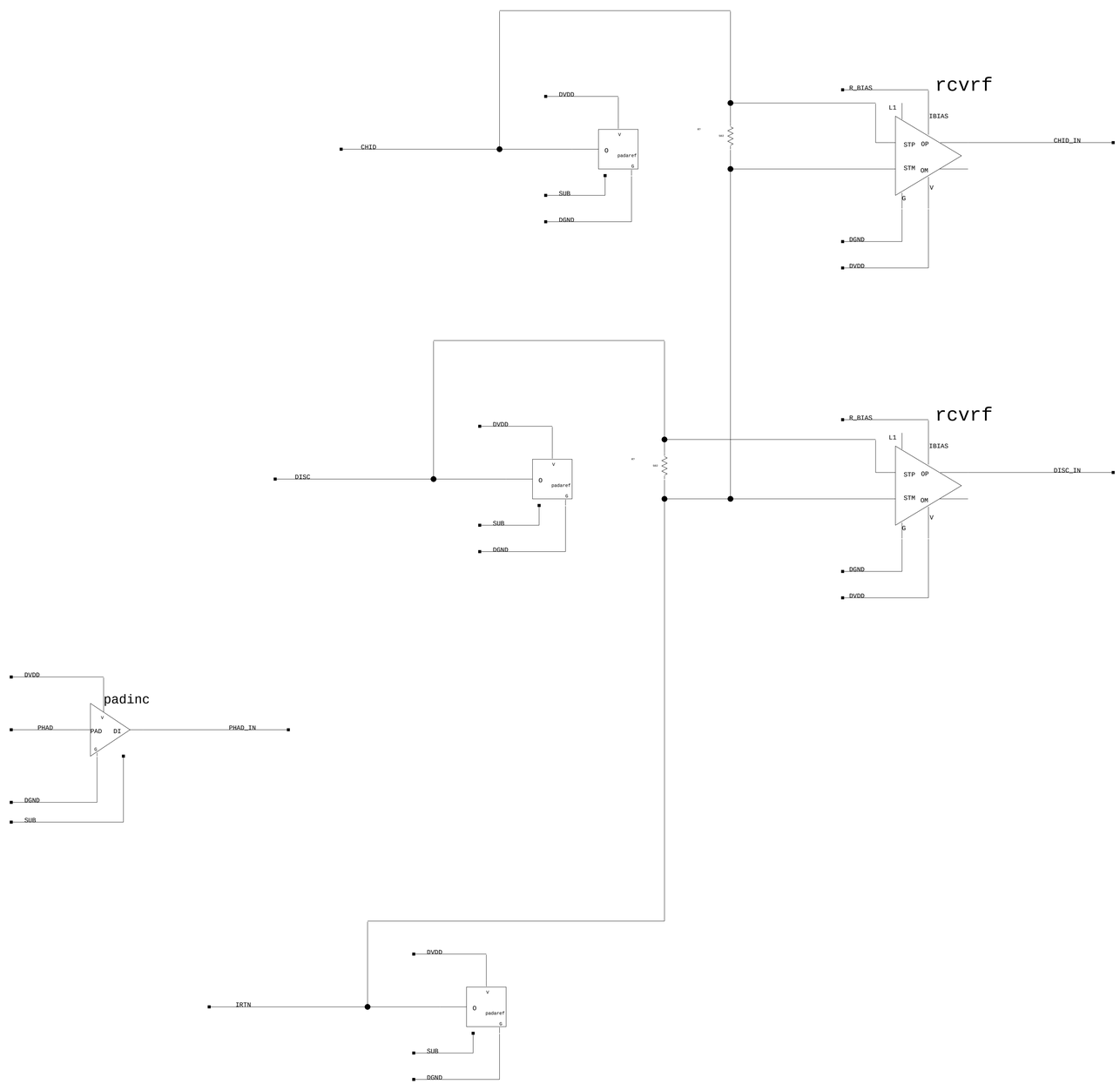

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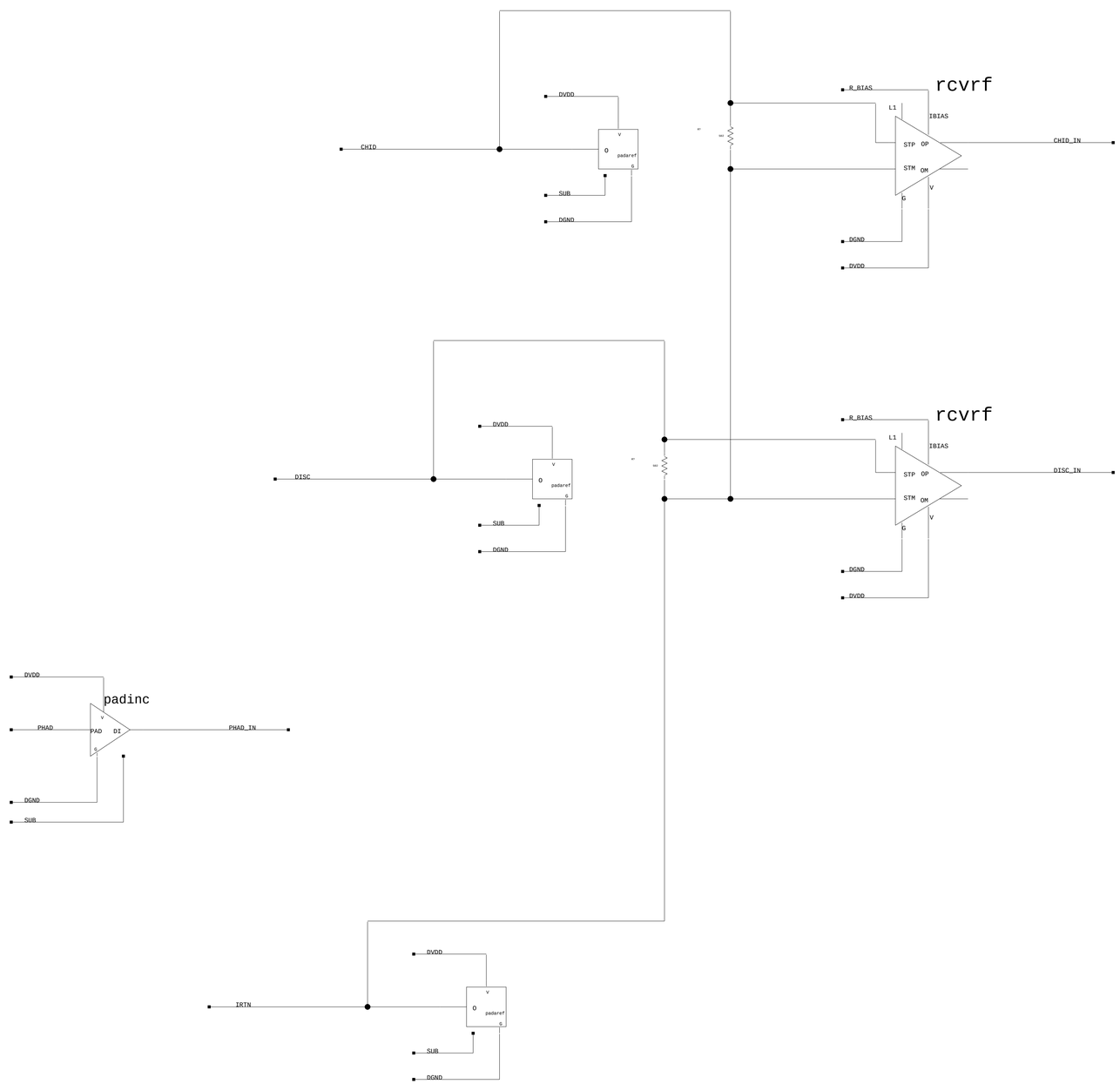

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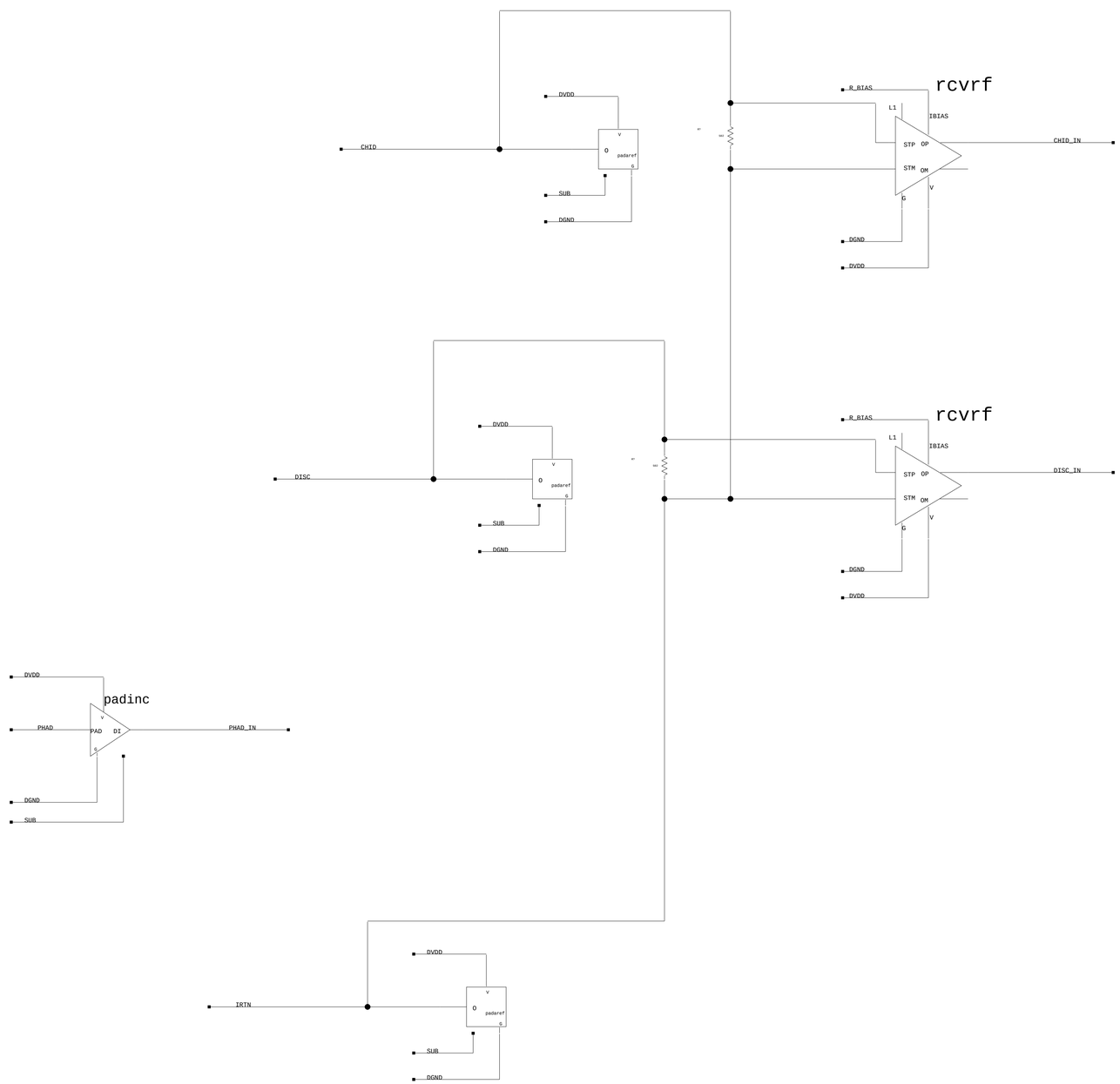

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SHEET OF

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AND LVDS RECEIVERS

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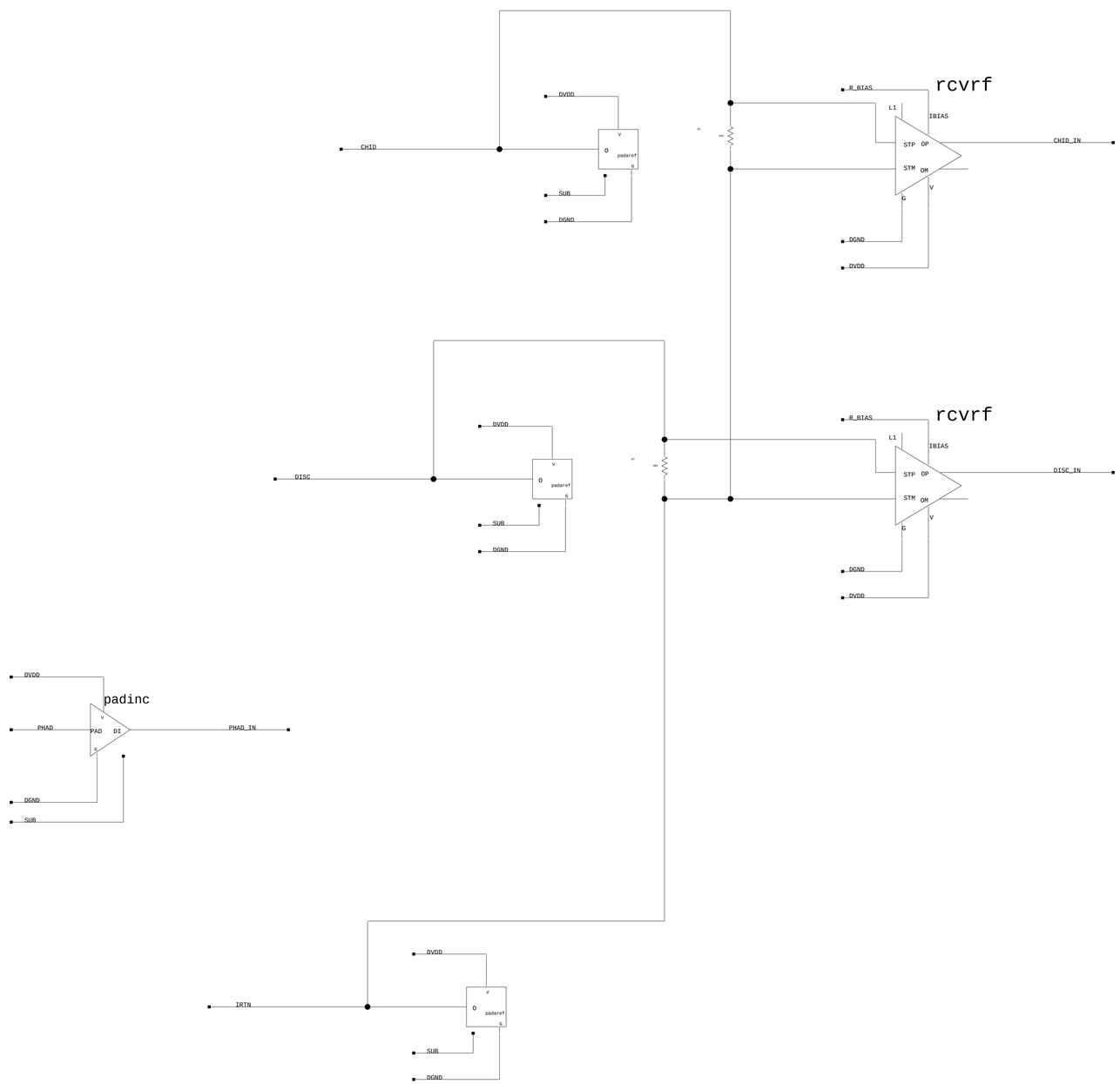
REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE



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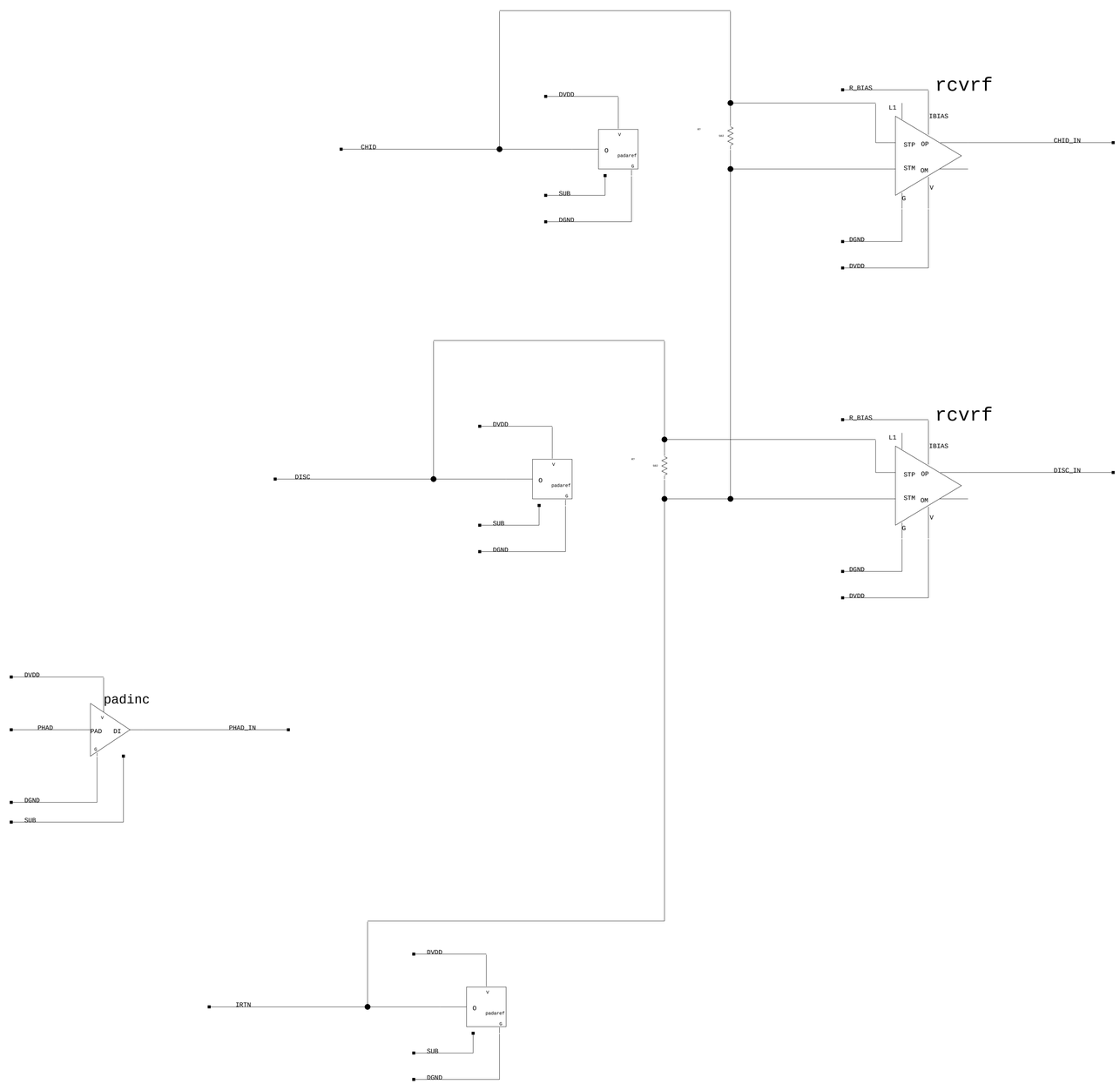

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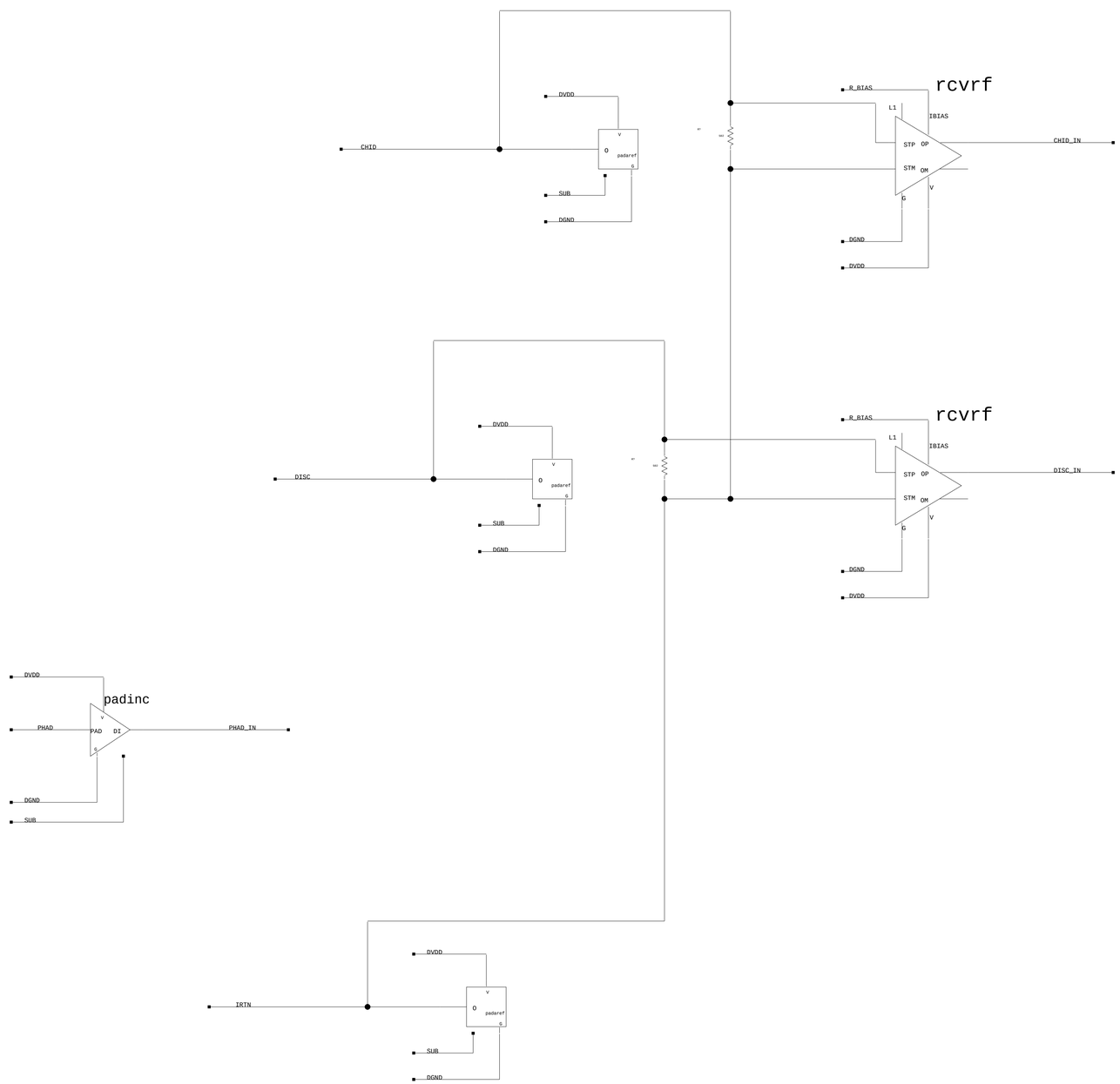

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