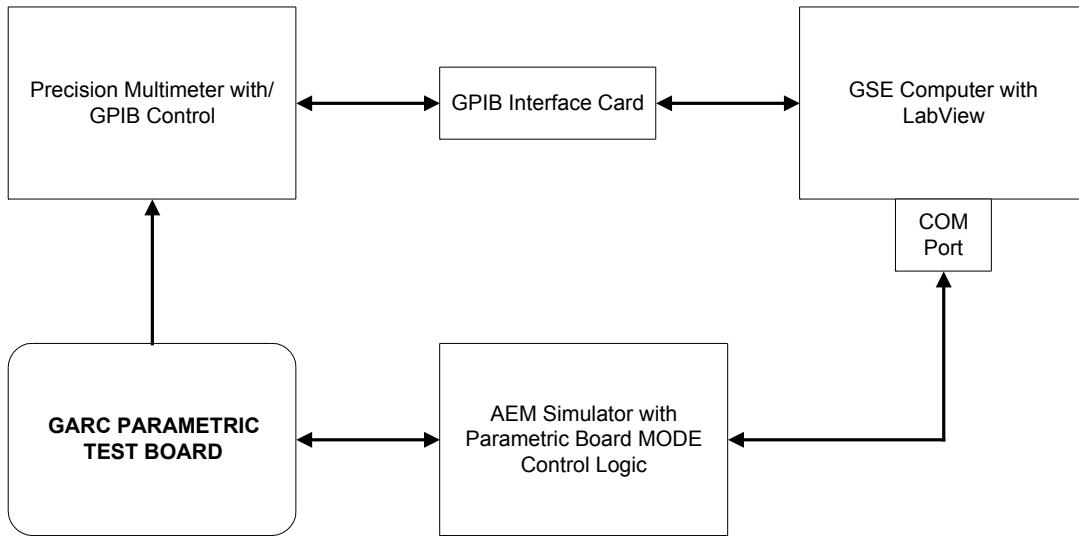


GARC Parametric Test Board Signal Map

The GARC Parametric Test Board performs a total of 320 measurements per board mode times 3 modes for a total of 960 measurements per voltage. The parametric test is performed at 3 different voltages (3.0, 3.3, 3.6), providing for a total of 2880 automated measurements per GARC V3 ASIC. A custom-designed LabView software routine is utilized to perform the measurements. A diagram of the system is shown below:



GARC V3 PARAMETRIC BOARD TEST CONFIGURATION

Each parametric test board has 20 output channels, e.g. : OUT1A - OUT1F, OUT2A - OUT2F, OUT3A - OUT3F, OUT4A, OUT4B. These channels are sampled at the output connector with a precision multimeter read out by a custom LabView program via a GPIB interface. Each of the 20 output channels on the GARC Parametric Board has a 16:1 multiplexer input to a buffer amplifier, providing a total of $20 \times 16 = 320$ measurement channels. The analog multiplexers commanding is automated via the LabView program.

Therefore, each set of parametric measurements at a given voltage consists of: $20 \times 16 \times 3 = 960$ measurements (not all of these are unique - some parameters are sampled multiple times). The parametric test is run at three voltages (min, typ, max) of 3.00, 3.30, and 3.60 volts. This gives a total data set of $960 \times 3 = 2880$ measurements for one GARC ASIC (note that not all measurements are unique).

The board has 3 valid GSE command modes (mode 1, mode 2, and mode 3). These modes are accessed via the AEM Simulator board (programmed with a special hardware version). These Parametric Board Test Modes are as follows:

- Mode 1: Selected pins are pulled to Vdd through a pullup resistor
- Mode 2: Selected pins are pulled to Gnd through a pulldown resistor
- Mode 3: Extra loading is disconnected from the GARC pins (i.e., many GARC pins are unloaded).

These modes are commanded automatically by the LabView program. Mode 0 is an invalid mode and is not utilized by the software. The loads to the GARC are switched in and out via analog multiplexers. Specific details on a pin-by-pin basis of the loading may be defined by viewing the GARC Parametric Test board schematic. This may be accessed on the ACD website at:

http://lhea-glast.gsfc.nasa.gov/acd/electronics/garc/GARC_Parametric_Test_Sch.pdf

Data is stored by the LabView program in an ASCII text file and imported manually into the "Raw Data" worksheet. The multiplexer channel to pin map is detailed below:

Signal Name	Mode	Power Supply	Mux	Channel	Parametric Bd Signal Port		Pin Type
ACD_NVETO_00_AP	1	3.30	0	0	OUT1A		LVDS Out
ACD_NVETO_00_AM	1	3.30	0	1	OUT1A		LVDS Out
ACD_NVETO_00_BP	1	3.30	0	2	OUT1A		LVDS Out
ACD_NVETO_00_BM	1	3.30	0	3	OUT1A		LVDS Out
ACD_NVETO_01_AP	1	3.30	0	4	OUT1A		LVDS Out
ACD_NVETO_01_AM	1	3.30	0	5	OUT1A		LVDS Out
ACD_NVETO_01_BP	1	3.30	0	6	OUT1A		LVDS Out
ACD_NVETO_01_BM	1	3.30	0	7	OUT1A		LVDS Out
ACD_NVETO_02_AP	1	3.30	0	8	OUT1A		LVDS Out
ACD_NVETO_02_AM	1	3.30	0	9	OUT1A		LVDS Out
ACD_NVETO_02_BP	1	3.30	0	10	OUT1A		LVDS Out
ACD_NVETO_02_BM	1	3.30	0	11	OUT1A		LVDS Out
ACD_NVETO_03_AP	1	3.30	0	12	OUT1A		LVDS Out
ACD_NVETO_03_AM	1	3.30	0	13	OUT1A		LVDS Out
VDDMID	1	3.30	0	14	OUT1A		VDD/2
GND	1	3.30	0	15	OUT1A		Ground
ACD_NVETO_03_BP	1	3.30	1	0	OUT1B		LVDS Out
ACD_NVETO_03_BM	1	3.30	1	1	OUT1B		LVDS Out
ACD_NVETO_04_AP	1	3.30	1	2	OUT1B		LVDS Out
ACD_NVETO_04_AM	1	3.30	1	3	OUT1B		LVDS Out
ACD_NVETO_04_BP	1	3.30	1	4	OUT1B		LVDS Out
ACD_NVETO_04_BM	1	3.30	1	5	OUT1B		LVDS Out
ACD_NVETO_05_AP	1	3.30	1	6	OUT1B		LVDS Out
ACD_NVETO_05_AM	1	3.30	1	7	OUT1B		LVDS Out
ACD_NVETO_05_BP	1	3.30	1	8	OUT1B		LVDS Out
ACD_NVETO_05_BM	1	3.30	1	9	OUT1B		LVDS Out
ACD_NVETO_06_AP	1	3.30	1	10	OUT1B		LVDS Out
ACD_NVETO_06_AM	1	3.30	1	11	OUT1B		LVDS Out
ACD_NVETO_06_BP	1	3.30	1	12	OUT1B		LVDS Out
ACD_NVETO_06_BM	1	3.30	1	13	OUT1B		LVDS Out
VDDMID	1	3.30	1	14	OUT1B		VDD/2
GND	1	3.30	1	15	OUT1B		Ground
ACD_NVETO_07_AP	1	3.30	2	0	OUT1C		LVDS Out
ACD_NVETO_07_AM	1	3.30	2	1	OUT1C		LVDS Out
ACD_NVETO_07_BP	1	3.30	2	2	OUT1C		LVDS Out
ACD_NVETO_07_BM	1	3.30	2	3	OUT1C		LVDS Out
ACD_NVETO_08_AP	1	3.30	2	4	OUT1C		LVDS Out
ACD_NVETO_08_AM	1	3.30	2	5	OUT1C		LVDS Out
ACD_NVETO_08_BP	1	3.30	2	6	OUT1C		LVDS Out
ACD_NVETO_08_BM	1	3.30	2	7	OUT1C		LVDS Out
ACD_NVETO_09_AP	1	3.30	2	8	OUT1C		LVDS Out
ACD_NVETO_09_AM	1	3.30	2	9	OUT1C		LVDS Out
ACD_NVETO_09_BP	1	3.30	2	10	OUT1C		LVDS Out
ACD_NVETO_09_BM	1	3.30	2	11	OUT1C		LVDS Out
ACD_NVETO_10_AP	1	3.30	2	12	OUT1C		LVDS Out

ACD_NVETO_10_AM	1	3.30	2	13	OUT1C	LVDS Out
VDDMID	1	3.30	2	14	OUT1C	VDD/2
GND	1	3.30	2	15	OUT1C	Ground
ACD_NVETO_10_BP	1	3.30	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	1	3.30	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	1	3.30	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	1	3.30	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	1	3.30	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	1	3.30	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	1	3.30	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	1	3.30	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	1	3.30	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	1	3.30	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	1	3.30	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	1	3.30	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	1	3.30	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	1	3.30	3	13	OUT1D	LVDS Out
VDDMID	1	3.30	3	14	OUT1D	VDD/2
GND	1	3.30	3	15	OUT1D	Ground
ACD_NVETO_14_AP	1	3.30	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	1	3.30	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	1	3.30	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	1	3.30	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	1	3.30	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	1	3.30	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	1	3.30	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	1	3.30	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	1	3.30	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	1	3.30	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	1	3.30	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	1	3.30	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	1	3.30	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	1	3.30	4	13	OUT1E	LVDS Out
VDDMID	1	3.30	4	14	OUT1E	VDD/2
GND	1	3.30	4	15	OUT1E	Ground
ACD_NVETO_17_BP	1	3.30	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	1	3.30	5	1	OUT1F	LVDS Out
ACD_CNO_AP	1	3.30	5	2	OUT1F	LVDS Out
ACD_CNO_AM	1	3.30	5	3	OUT1F	LVDS Out
ACD_CNO_BP	1	3.30	5	4	OUT1F	LVDS Out
ACD_CNO_BM	1	3.30	5	5	OUT1F	LVDS Out
ACD_CLK_AP	1	3.30	5	6	OUT1F	LVDS In
ACD_CLK_AM	1	3.30	5	7	OUT1F	LVDS In
ACD_CLK_BP	1	3.30	5	8	OUT1F	LVDS In
ACD_CLK_BM	1	3.30	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	1	3.30	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	1	3.30	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	1	3.30	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	1	3.30	5	13	OUT1F	LVDS In
ACD_NRST_AP	1	3.30	5	14	OUT1F	LVDS In

ACD_NRST_AM	1	3.30	5	15	OUT1F	LVDS In
ACD_NRST_BP	1	3.30	6	0	OUT2A	LVDS In
ACD_NRST_BM	1	3.30	6	1	OUT2A	LVDS In
ACD_NSADATA_AP	1	3.30	6	2	OUT2A	LVDS Out
ACD_NSADATA_AM	1	3.30	6	3	OUT2A	LVDS Out
ACD_NSADATA_BP	1	3.30	6	4	OUT2A	LVDS Out
ACD_NSADATA_BM	1	3.30	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	1	3.30	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	1	3.30	6	7	OUT2A	CMOS Input
GAFE_RST	1	3.30	6	8	OUT2A	CMOS Output
GAFE_RSTP	1	3.30	6	9	OUT2A	CMOS Output
DAC_DATA	1	3.30	6	10	OUT2A	CMOS Output
DAC_DATAP	1	3.30	6	11	OUT2A	CMOS Output
DAC_CLK	1	3.30	6	12	OUT2A	CMOS Output
DAC_CLKP	1	3.30	6	13	OUT2A	CMOS Output
VDDMID	1	3.30	6	14	OUT2A	VDD/2
GND	1	3.30	6	15	OUT2A	Ground
NDAC_CS	1	3.30	7	0	OUT2B	CMOS Output
NDAC_CSP	1	3.30	7	1	OUT2B	CMOS Output
NDAC_CLR	1	3.30	7	2	OUT2B	CMOS Output
NDAC_CLRP	1	3.30	7	3	OUT2B	CMOS Output
HV_ENABLE1	1	3.30	7	4	OUT2B	CMOS Output
HV_ENABLE1P	1	3.30	7	5	OUT2B	CMOS Output
HV_ENABLE2	1	3.30	7	6	OUT2B	CMOS Output
HV_ENABLE2P	1	3.30	7	7	OUT2B	CMOS Output
ADC_CLK	1	3.30	7	8	OUT2B	CMOS Output
ADC_CLKP	1	3.30	7	9	OUT2B	CMOS Output
NADC_CS	1	3.30	7	10	OUT2B	CMOS Output
NADC_CSP	1	3.30	7	11	OUT2B	CMOS Output
DAC_DATA	1	3.30	7	12	OUT2B	CMOS Output
DAC_DATAP	1	3.30	7	13	OUT2B	CMOS Output
VDDMID	1	3.30	7	14	OUT2B	VDD/2
GND	1	3.30	7	15	OUT2B	Ground
DAC_DATA_RET	1	3.30	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	1	3.30	8	1	OUT2C	CMOS Input
HITMAP_TEST	1	3.30	8	2	OUT2C	CMOS Output
HITMAP_TESTP	1	3.30	8	3	OUT2C	CMOS Output
TRIG_TEST	1	3.30	8	4	OUT2C	CMOS Output
TRIG_TESTP	1	3.30	8	5	OUT2C	CMOS Output
FREE_ID	1	3.30	8	6	OUT2C	CMOS Output
FREE_IDP	1	3.30	8	7	OUT2C	CMOS Output
PWR_ON_RST	1	3.30	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	1	3.30	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	1	3.30	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	1	3.30	8	11	OUT2C	CMOS Output
IRTN_17P	1	3.30	8	12	OUT2C	GAFE IF Return
IRTN_17	1	3.30	8	13	OUT2C	GAFE IF Return
VDDMID	1	3.30	8	14	OUT2C	VDD/2
GND	1	3.30	8	15	OUT2C	Ground
DISC_17P	1	3.30	9	0	OUT2D	GAFE IF Input

DISC_17	1	3.30	9	1	OUT2D	GAFE IF Input
CHID_17P	1	3.30	9	2	OUT2D	GAFE IF Input
CHID_17	1	3.30	9	3	OUT2D	GAFE IF Input
IRTN_16P	1	3.30	9	4	OUT2D	GAFE IF Return
IRTN_16	1	3.30	9	5	OUT2D	GAFE IF Return
DISC_16P	1	3.30	9	6	OUT2D	GAFE IF Input
DISC_16	1	3.30	9	7	OUT2D	GAFE IF Input
CHID_16P	1	3.30	9	8	OUT2D	GAFE IF Input
CHID_16	1	3.30	9	9	OUT2D	GAFE IF Input
IRTN_15P	1	3.30	9	10	OUT2D	GAFE IF Return
IRTN_15	1	3.30	9	11	OUT2D	GAFE IF Return
DISC_15P	1	3.30	9	12	OUT2D	GAFE IF Input
DISC_15	1	3.30	9	13	OUT2D	GAFE IF Input
VDDMID	1	3.30	9	14	OUT2D	VDD/2
GND	1	3.30	9	15	OUT2D	Ground
CHID_15P	1	3.30	10	0	OUT2E	GAFE IF Input
CHID_15	1	3.30	10	1	OUT2E	GAFE IF Input
IRTN_14P	1	3.30	10	2	OUT2E	GAFE IF Return
IRTN_14	1	3.30	10	3	OUT2E	GAFE IF Return
DISC_14P	1	3.30	10	4	OUT2E	GAFE IF Input
DISC_14	1	3.30	10	5	OUT2E	GAFE IF Input
CHID_14P	1	3.30	10	6	OUT2E	GAFE IF Input
CHID_14	1	3.30	10	7	OUT2E	GAFE IF Input
IRTN_13P	1	3.30	10	8	OUT2E	GAFE IF Return
IRTN_13	1	3.30	10	9	OUT2E	GAFE IF Return
DISC_13P	1	3.30	10	10	OUT2E	GAFE IF Input
DISC_13	1	3.30	10	11	OUT2E	GAFE IF Input
CHID_13P	1	3.30	10	12	OUT2E	GAFE IF Input
CHID_13	1	3.30	10	13	OUT2E	GAFE IF Input
IRTN_12P	1	3.30	10	14	OUT2E	GAFE IF Return
IRTN_12	1	3.30	10	15	OUT2E	GAFE IF Return
DISC_12P	1	3.30	11	0	OUT2F	GAFE IF Input
DISC_12	1	3.30	11	1	OUT2F	GAFE IF Input
CHID_12P	1	3.30	11	2	OUT2F	GAFE IF Input
CHID_12	1	3.30	11	3	OUT2F	GAFE IF Input
IRTN_11P	1	3.30	11	4	OUT2F	GAFE IF Return
IRTN_11	1	3.30	11	5	OUT2F	GAFE IF Return
DISC_11P	1	3.30	11	6	OUT2F	GAFE IF Input
DISC_11	1	3.30	11	7	OUT2F	GAFE IF Input
CHID_11P	1	3.30	11	8	OUT2F	GAFE IF Input
CHID_11	1	3.30	11	9	OUT2F	GAFE IF Input
IRTN_10P	1	3.30	11	10	OUT2F	GAFE IF Return
IRTN_10	1	3.30	11	11	OUT2F	GAFE IF Return
DISC_10P	1	3.30	11	12	OUT2F	GAFE IF Input
DISC_10	1	3.30	11	13	OUT2F	GAFE IF Input
CHID_10P	1	3.30	11	14	OUT2F	GAFE IF Input
CHID_10	1	3.30	11	15	OUT2F	GAFE IF Input
PHAD_00	1	3.30	12	0	OUT3A	CMOS Input
PHAD_00P	1	3.30	12	1	OUT3A	CMOS Input
PHAD_01	1	3.30	12	2	OUT3A	CMOS Input

PHAD_01P	1	3.30	12	3	OUT3A	CMOS Input
PHAD_02	1	3.30	12	4	OUT3A	CMOS Input
PHAD_02P	1	3.30	12	5	OUT3A	CMOS Input
PHAD_03	1	3.30	12	6	OUT3A	CMOS Input
PHAD_03P	1	3.30	12	7	OUT3A	CMOS Input
PHAD_04	1	3.30	12	8	OUT3A	CMOS Input
PHAD_04P	1	3.30	12	9	OUT3A	CMOS Input
PHAD_05	1	3.30	12	10	OUT3A	CMOS Input
PHAD_05P	1	3.30	12	11	OUT3A	CMOS Input
PHAD_06	1	3.30	12	12	OUT3A	CMOS Input
PHAD_06P	1	3.30	12	13	OUT3A	CMOS Input
PHAD_07	1	3.30	12	14	OUT3A	CMOS Input
PHAD_07P	1	3.30	12	15	OUT3A	CMOS Input
PHAD_08	1	3.30	13	0	OUT3B	CMOS Input
PHAD_08P	1	3.30	13	1	OUT3B	CMOS Input
PHAD_09	1	3.30	13	2	OUT3B	CMOS Input
PHAD_09P	1	3.30	13	3	OUT3B	CMOS Input
PHAD_10	1	3.30	13	4	OUT3B	CMOS Input
PHAD_10P	1	3.30	13	5	OUT3B	CMOS Input
PHAD_11	1	3.30	13	6	OUT3B	CMOS Input
PHAD_11P	1	3.30	13	7	OUT3B	CMOS Input
PHAD_12	1	3.30	13	8	OUT3B	CMOS Input
PHAD_12P	1	3.30	13	9	OUT3B	CMOS Input
PHAD_13	1	3.30	13	10	OUT3B	CMOS Input
PHAD_13P	1	3.30	13	11	OUT3B	CMOS Input
PHAD_14	1	3.30	13	12	OUT3B	CMOS Input
PHAD_14P	1	3.30	13	13	OUT3B	CMOS Input
PHAD_15	1	3.30	13	14	OUT3B	CMOS Input
PHAD_15P	1	3.30	13	15	OUT3B	CMOS Input
PHAD_16	1	3.30	14	0	OUT3C	CMOS Input
PHAD_16P	1	3.30	14	1	OUT3C	CMOS Input
PHAD_17	1	3.30	14	2	OUT3C	CMOS Input
PHAD_17P	1	3.30	14	3	OUT3C	CMOS Input
CHID_00	1	3.30	14	4	OUT3C	GAFE IF Input
CHID_00P	1	3.30	14	5	OUT3C	GAFE IF Input
DISC_00	1	3.30	14	6	OUT3C	GAFE IF Input
DISC_00P	1	3.30	14	7	OUT3C	GAFE IF Input
IRTN_00	1	3.30	14	8	OUT3C	GAFE IF Return
IRTN_00P	1	3.30	14	9	OUT3C	GAFE IF Return
CHID_01	1	3.30	14	10	OUT3C	GAFE IF Input
CHID_01P	1	3.30	14	11	OUT3C	GAFE IF Input
DISC_01	1	3.30	14	12	OUT3C	GAFE IF Input
DISC_01P	1	3.30	14	13	OUT3C	GAFE IF Input
IRTN_01	1	3.30	14	14	OUT3C	GAFE IF Return
IRTN_01P	1	3.30	14	15	OUT3C	GAFE IF Return
CHID_02	1	3.30	15	0	OUT3D	GAFE IF Input
CHID_02P	1	3.30	15	1	OUT3D	GAFE IF Input
DISC_02	1	3.30	15	2	OUT3D	GAFE IF Input
DISC_02P	1	3.30	15	3	OUT3D	GAFE IF Input
IRTN_02	1	3.30	15	4	OUT3D	GAFE IF Return

IRTN_02P	1	3.30	15	5	OUT3D	GAFE IF Return
CHID_03	1	3.30	15	6	OUT3D	GAFE IF Input
CHID_03P	1	3.30	15	7	OUT3D	GAFE IF Input
DISC_03	1	3.30	15	8	OUT3D	GAFE IF Input
DISC_03P	1	3.30	15	9	OUT3D	GAFE IF Input
IRTN_03	1	3.30	15	10	OUT3D	GAFE IF Return
IRTN_03P	1	3.30	15	11	OUT3D	GAFE IF Return
CHID_04	1	3.30	15	12	OUT3D	GAFE IF Input
CHID_04P	1	3.30	15	13	OUT3D	GAFE IF Input
DISC_04	1	3.30	15	14	OUT3D	GAFE IF Input
DISC_04P	1	3.30	15	15	OUT3D	GAFE IF Input
IRTN_04	1	3.30	16	0	OUT3E	GAFE IF Return
IRTN_04P	1	3.30	16	1	OUT3E	GAFE IF Return
CHID_05	1	3.30	16	2	OUT3E	GAFE IF Input
CHID_05P	1	3.30	16	3	OUT3E	GAFE IF Input
DISC_05	1	3.30	16	4	OUT3E	GAFE IF Input
DISC_05P	1	3.30	16	5	OUT3E	GAFE IF Input
IRTN_05	1	3.30	16	6	OUT3E	GAFE IF Return
IRTN_05P	1	3.30	16	7	OUT3E	GAFE IF Return
CHID_06	1	3.30	16	8	OUT3E	GAFE IF Input
CHID_06P	1	3.30	16	9	OUT3E	GAFE IF Input
DISC_06	1	3.30	16	10	OUT3E	GAFE IF Input
DISC_06P	1	3.30	16	11	OUT3E	GAFE IF Input
IRTN_06	1	3.30	16	12	OUT3E	GAFE IF Return
IRTN_06P	1	3.30	16	13	OUT3E	GAFE IF Return
CHID_07	1	3.30	16	14	OUT3E	GAFE IF Input
CHID_07P	1	3.30	16	15	OUT3E	GAFE IF Input
DISC_07	1	3.30	17	0	OUT3F	GAFE IF Input
DISC_07P	1	3.30	17	1	OUT3F	GAFE IF Input
IRTN_07	1	3.30	17	2	OUT3F	GAFE IF Return
IRTN_07P	1	3.30	17	3	OUT3F	GAFE IF Return
CHID_08	1	3.30	17	4	OUT3F	GAFE IF Input
CHID_08P	1	3.30	17	5	OUT3F	GAFE IF Input
DISC_08	1	3.30	17	6	OUT3F	GAFE IF Input
DISC_08P	1	3.30	17	7	OUT3F	GAFE IF Input
IRTN_08	1	3.30	17	8	OUT3F	GAFE IF Return
IRTN_08P	1	3.30	17	9	OUT3F	GAFE IF Return
CHID_09	1	3.30	17	10	OUT3F	GAFE IF Input
CHID_09P	1	3.30	17	11	OUT3F	GAFE IF Input
DISC_09	1	3.30	17	12	OUT3F	GAFE IF Input
DISC_09P	1	3.30	17	13	OUT3F	GAFE IF Input
IRTN_09	1	3.30	17	14	OUT3F	GAFE IF Return
IRTN_09P	1	3.30	17	15	OUT3F	GAFE IF Return
DGND001	1	3.30	18	0	OUT4A	Ground Pin
DGND027	1	3.30	18	1	OUT4A	Ground Pin
DGND053	1	3.30	18	2	OUT4A	Ground Pin
DGND080	1	3.30	18	3	OUT4A	Ground Pin
DGND136	1	3.30	18	4	OUT4A	Ground Pin
DGND157	1	3.30	18	5	OUT4A	Ground Pin
DGND187	1	3.30	18	6	OUT4A	Ground Pin

DVDD024	1	3.30	18	7	OUT4A	Power Pin
DVDD052	1	3.30	18	8	OUT4A	Power Pin
DVDD078	1	3.30	18	9	OUT4A	Power Pin
DVDD132	1	3.30	18	10	OUT4A	Power Pin
DVDD155	1	3.30	18	11	OUT4A	Power Pin
DVDD208	1	3.30	18	12	OUT4A	Power Pin
CMDCK+	1	3.30	18	13	OUT4A	GAFE Diff Output
CMDCK-	1	3.30	18	14	OUT4A	GAFE Diff Output
GND	1	3.30	18	15	OUT4A	VDD/2
VDDMID	1	3.30	19	0	OUT4B	Ground
CMDD+	1	3.30	19	1	OUT4B	GAFE Diff Output
CMDD-	1	3.30	19	2	OUT4B	GAFE Diff Output
STROB+	1	3.30	19	3	OUT4B	GAFE Diff Output
STROB-	1	3.30	19	4	OUT4B	GAFE Diff Output
HOLD+	1	3.30	19	5	OUT4B	GAFE Diff Output
HOLD-	1	3.30	19	6	OUT4B	GAFE Diff Output
HLD	1	3.30	19	7	OUT4B	GAFE HLD
IRTN_HLD	1	3.30	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	1	3.30	19	9	OUT4B	Bias Pin
BIAS_DRV_L	1	3.30	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	1	3.30	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	1	3.30	19	12	OUT4B	Bias Pin
BIAS_RCVR	1	3.30	19	13	OUT4B	Bias Pin
VDDMID	1	3.30	19	14	OUT4B	VDD/2
GND	1	3.30	19	15	OUT4B	Ground
ACD_NVETO_00_AP	2	3.30	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	2	3.30	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	2	3.30	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	2	3.30	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	2	3.30	0	4	OUT1A	LVDS Out
ACD_NVETO_01_AM	2	3.30	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	2	3.30	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	2	3.30	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	2	3.30	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	2	3.30	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	2	3.30	0	10	OUT1A	LVDS Out
ACD_NVETO_02_BM	2	3.30	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	2	3.30	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	2	3.30	0	13	OUT1A	LVDS Out
VDDMID	2	3.30	0	14	OUT1A	VDD/2
GND	2	3.30	0	15	OUT1A	Ground
ACD_NVETO_03_BP	2	3.30	1	0	OUT1B	LVDS Out
ACD_NVETO_03_BM	2	3.30	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	2	3.30	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	2	3.30	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	2	3.30	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	2	3.30	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	2	3.30	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	2	3.30	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	2	3.30	1	8	OUT1B	LVDS Out

ACD_NVETO_05_BM	2	3.30	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	2	3.30	1	10	OUT1B	LVDS Out
ACD_NVETO_06_AM	2	3.30	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	2	3.30	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	2	3.30	1	13	OUT1B	LVDS Out
VDDMID	2	3.30	1	14	OUT1B	VDD/2
GND	2	3.30	1	15	OUT1B	Ground
ACD_NVETO_07_AP	2	3.30	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	2	3.30	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	2	3.30	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	2	3.30	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	2	3.30	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	2	3.30	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	2	3.30	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	2	3.30	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	2	3.30	2	8	OUT1C	LVDS Out
ACD_NVETO_09_AM	2	3.30	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	2	3.30	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	2	3.30	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	2	3.30	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	2	3.30	2	13	OUT1C	LVDS Out
VDDMID	2	3.30	2	14	OUT1C	VDD/2
GND	2	3.30	2	15	OUT1C	Ground
ACD_NVETO_10_BP	2	3.30	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	2	3.30	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	2	3.30	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	2	3.30	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	2	3.30	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	2	3.30	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	2	3.30	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	2	3.30	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	2	3.30	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	2	3.30	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	2	3.30	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	2	3.30	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	2	3.30	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	2	3.30	3	13	OUT1D	LVDS Out
VDDMID	2	3.30	3	14	OUT1D	VDD/2
GND	2	3.30	3	15	OUT1D	Ground
ACD_NVETO_14_AP	2	3.30	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	2	3.30	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	2	3.30	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	2	3.30	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	2	3.30	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	2	3.30	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	2	3.30	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	2	3.30	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	2	3.30	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	2	3.30	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	2	3.30	4	10	OUT1E	LVDS Out

ACD_NVETO_16_BM	2	3.30	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	2	3.30	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	2	3.30	4	13	OUT1E	LVDS Out
VDDMID	2	3.30	4	14	OUT1E	VDD/2
GND	2	3.30	4	15	OUT1E	Ground
ACD_NVETO_17_BP	2	3.30	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	2	3.30	5	1	OUT1F	LVDS Out
ACD_CNO_AP	2	3.30	5	2	OUT1F	LVDS Out
ACD_CNO_AM	2	3.30	5	3	OUT1F	LVDS Out
ACD_CNO_BP	2	3.30	5	4	OUT1F	LVDS Out
ACD_CNO_BM	2	3.30	5	5	OUT1F	LVDS Out
ACD_CLK_AP	2	3.30	5	6	OUT1F	LVDS In
ACD_CLK_AM	2	3.30	5	7	OUT1F	LVDS In
ACD_CLK_BP	2	3.30	5	8	OUT1F	LVDS In
ACD_CLK_BM	2	3.30	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	2	3.30	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	2	3.30	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	2	3.30	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	2	3.30	5	13	OUT1F	LVDS In
ACD_NRST_AP	2	3.30	5	14	OUT1F	LVDS In
ACD_NRST_AM	2	3.30	5	15	OUT1F	LVDS In
ACD_NRST_BP	2	3.30	6	0	OUT2A	LVDS In
ACD_NRST_BM	2	3.30	6	1	OUT2A	LVDS In
ACD_NSADATA_AP	2	3.30	6	2	OUT2A	LVDS Out
ACD_NSADATA_AM	2	3.30	6	3	OUT2A	LVDS Out
ACD_NSADATA_BP	2	3.30	6	4	OUT2A	LVDS Out
ACD_NSADATA_BM	2	3.30	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	2	3.30	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	2	3.30	6	7	OUT2A	CMOS Input
GAFE_RST	2	3.30	6	8	OUT2A	CMOS Output
GAFE_RSTP	2	3.30	6	9	OUT2A	CMOS Output
DAC_DATA	2	3.30	6	10	OUT2A	CMOS Output
DAC_DATAP	2	3.30	6	11	OUT2A	CMOS Output
DAC_CLK	2	3.30	6	12	OUT2A	CMOS Output
DAC_CLKP	2	3.30	6	13	OUT2A	CMOS Output
VDDMID	2	3.30	6	14	OUT2A	VDD/2
GND	2	3.30	6	15	OUT2A	Ground
NDAC_CS	2	3.30	7	0	OUT2B	CMOS Output
NDAC_CSP	2	3.30	7	1	OUT2B	CMOS Output
NDAC_CLR	2	3.30	7	2	OUT2B	CMOS Output
NDAC_CLRP	2	3.30	7	3	OUT2B	CMOS Output
HV_ENABLE1	2	3.30	7	4	OUT2B	CMOS Output
HV_ENABLE1P	2	3.30	7	5	OUT2B	CMOS Output
HV_ENABLE2	2	3.30	7	6	OUT2B	CMOS Output
HV_ENABLE2P	2	3.30	7	7	OUT2B	CMOS Output
ADC_CLK	2	3.30	7	8	OUT2B	CMOS Output
ADC_CLKP	2	3.30	7	9	OUT2B	CMOS Output
NADC_CS	2	3.30	7	10	OUT2B	CMOS Output
NADC_CSP	2	3.30	7	11	OUT2B	CMOS Output
DAC_DATA	2	3.30	7	12	OUT2B	CMOS Output

DAC_DATAP	2	3.30	7	13	OUT2B	CMOS Output
VDDMID	2	3.30	7	14	OUT2B	VDD/2
GND	2	3.30	7	15	OUT2B	Ground
DAC_DATA_RET	2	3.30	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	2	3.30	8	1	OUT2C	CMOS Input
HITMAP_TEST	2	3.30	8	2	OUT2C	CMOS Output
HITMAP_TESTP	2	3.30	8	3	OUT2C	CMOS Output
TRIG_TEST	2	3.30	8	4	OUT2C	CMOS Output
TRIG_TESTP	2	3.30	8	5	OUT2C	CMOS Output
FREE_ID	2	3.30	8	6	OUT2C	CMOS Output
FREE_IDP	2	3.30	8	7	OUT2C	CMOS Output
PWR_ON_RST	2	3.30	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	2	3.30	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	2	3.30	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	2	3.30	8	11	OUT2C	CMOS Output
IRTN_17P	2	3.30	8	12	OUT2C	GAFE IF Return
IRTN_17	2	3.30	8	13	OUT2C	GAFE IF Return
VDDMID	2	3.30	8	14	OUT2C	VDD/2
GND	2	3.30	8	15	OUT2C	Ground
DISC_17P	2	3.30	9	0	OUT2D	GAFE IF Input
DISC_17	2	3.30	9	1	OUT2D	GAFE IF Input
CHID_17P	2	3.30	9	2	OUT2D	GAFE IF Input
CHID_17	2	3.30	9	3	OUT2D	GAFE IF Input
IRTN_16P	2	3.30	9	4	OUT2D	GAFE IF Return
IRTN_16	2	3.30	9	5	OUT2D	GAFE IF Return
DISC_16P	2	3.30	9	6	OUT2D	GAFE IF Input
DISC_16	2	3.30	9	7	OUT2D	GAFE IF Input
CHID_16P	2	3.30	9	8	OUT2D	GAFE IF Input
CHID_16	2	3.30	9	9	OUT2D	GAFE IF Input
IRTN_15P	2	3.30	9	10	OUT2D	GAFE IF Return
IRTN_15	2	3.30	9	11	OUT2D	GAFE IF Return
DISC_15P	2	3.30	9	12	OUT2D	GAFE IF Input
DISC_15	2	3.30	9	13	OUT2D	GAFE IF Input
VDDMID	2	3.30	9	14	OUT2D	VDD/2
GND	2	3.30	9	15	OUT2D	Ground
CHID_15P	2	3.30	10	0	OUT2E	GAFE IF Input
CHID_15	2	3.30	10	1	OUT2E	GAFE IF Input
IRTN_14P	2	3.30	10	2	OUT2E	GAFE IF Return
IRTN_14	2	3.30	10	3	OUT2E	GAFE IF Return
DISC_14P	2	3.30	10	4	OUT2E	GAFE IF Input
DISC_14	2	3.30	10	5	OUT2E	GAFE IF Input
CHID_14P	2	3.30	10	6	OUT2E	GAFE IF Input
CHID_14	2	3.30	10	7	OUT2E	GAFE IF Input
IRTN_13P	2	3.30	10	8	OUT2E	GAFE IF Return
IRTN_13	2	3.30	10	9	OUT2E	GAFE IF Return
DISC_13P	2	3.30	10	10	OUT2E	GAFE IF Input
DISC_13	2	3.30	10	11	OUT2E	GAFE IF Input
CHID_13P	2	3.30	10	12	OUT2E	GAFE IF Input
CHID_13	2	3.30	10	13	OUT2E	GAFE IF Input
IRTN_12P	2	3.30	10	14	OUT2E	GAFE IF Return

IRTN_12	2	3.30	10	15	OUT2E	GAFE IF Return
DISC_12P	2	3.30	11	0	OUT2F	GAFE IF Input
DISC_12	2	3.30	11	1	OUT2F	GAFE IF Input
CHID_12P	2	3.30	11	2	OUT2F	GAFE IF Input
CHID_12	2	3.30	11	3	OUT2F	GAFE IF Input
IRTN_11P	2	3.30	11	4	OUT2F	GAFE IF Return
IRTN_11	2	3.30	11	5	OUT2F	GAFE IF Return
DISC_11P	2	3.30	11	6	OUT2F	GAFE IF Input
DISC_11	2	3.30	11	7	OUT2F	GAFE IF Input
CHID_11P	2	3.30	11	8	OUT2F	GAFE IF Input
CHID_11	2	3.30	11	9	OUT2F	GAFE IF Input
IRTN_10P	2	3.30	11	10	OUT2F	GAFE IF Return
IRTN_10	2	3.30	11	11	OUT2F	GAFE IF Return
DISC_10P	2	3.30	11	12	OUT2F	GAFE IF Input
DISC_10	2	3.30	11	13	OUT2F	GAFE IF Input
CHID_10P	2	3.30	11	14	OUT2F	GAFE IF Input
CHID_10	2	3.30	11	15	OUT2F	GAFE IF Input
PHAD_00	2	3.30	12	0	OUT3A	CMOS Input
PHAD_00P	2	3.30	12	1	OUT3A	CMOS Input
PHAD_01	2	3.30	12	2	OUT3A	CMOS Input
PHAD_01P	2	3.30	12	3	OUT3A	CMOS Input
PHAD_02	2	3.30	12	4	OUT3A	CMOS Input
PHAD_02P	2	3.30	12	5	OUT3A	CMOS Input
PHAD_03	2	3.30	12	6	OUT3A	CMOS Input
PHAD_03P	2	3.30	12	7	OUT3A	CMOS Input
PHAD_04	2	3.30	12	8	OUT3A	CMOS Input
PHAD_04P	2	3.30	12	9	OUT3A	CMOS Input
PHAD_05	2	3.30	12	10	OUT3A	CMOS Input
PHAD_05P	2	3.30	12	11	OUT3A	CMOS Input
PHAD_06	2	3.30	12	12	OUT3A	CMOS Input
PHAD_06P	2	3.30	12	13	OUT3A	CMOS Input
PHAD_07	2	3.30	12	14	OUT3A	CMOS Input
PHAD_07P	2	3.30	12	15	OUT3A	CMOS Input
PHAD_08	2	3.30	13	0	OUT3B	CMOS Input
PHAD_08P	2	3.30	13	1	OUT3B	CMOS Input
PHAD_09	2	3.30	13	2	OUT3B	CMOS Input
PHAD_09P	2	3.30	13	3	OUT3B	CMOS Input
PHAD_10	2	3.30	13	4	OUT3B	CMOS Input
PHAD_10P	2	3.30	13	5	OUT3B	CMOS Input
PHAD_11	2	3.30	13	6	OUT3B	CMOS Input
PHAD_11P	2	3.30	13	7	OUT3B	CMOS Input
PHAD_12	2	3.30	13	8	OUT3B	CMOS Input
PHAD_12P	2	3.30	13	9	OUT3B	CMOS Input
PHAD_13	2	3.30	13	10	OUT3B	CMOS Input
PHAD_13P	2	3.30	13	11	OUT3B	CMOS Input
PHAD_14	2	3.30	13	12	OUT3B	CMOS Input
PHAD_14P	2	3.30	13	13	OUT3B	CMOS Input
PHAD_15	2	3.30	13	14	OUT3B	CMOS Input
PHAD_15P	2	3.30	13	15	OUT3B	CMOS Input
PHAD_16	2	3.30	14	0	OUT3C	CMOS Input

PHAD_16P	2	3.30	14	1	OUT3C	CMOS Input
PHAD_17	2	3.30	14	2	OUT3C	CMOS Input
PHAD_17P	2	3.30	14	3	OUT3C	CMOS Input
CHID_00	2	3.30	14	4	OUT3C	GAFE IF Input
CHID_00P	2	3.30	14	5	OUT3C	GAFE IF Input
DISC_00	2	3.30	14	6	OUT3C	GAFE IF Input
DISC_00P	2	3.30	14	7	OUT3C	GAFE IF Input
IRTN_00	2	3.30	14	8	OUT3C	GAFE IF Return
IRTN_00P	2	3.30	14	9	OUT3C	GAFE IF Return
CHID_01	2	3.30	14	10	OUT3C	GAFE IF Input
CHID_01P	2	3.30	14	11	OUT3C	GAFE IF Input
DISC_01	2	3.30	14	12	OUT3C	GAFE IF Input
DISC_01P	2	3.30	14	13	OUT3C	GAFE IF Input
IRTN_01	2	3.30	14	14	OUT3C	GAFE IF Return
IRTN_01P	2	3.30	14	15	OUT3C	GAFE IF Return
CHID_02	2	3.30	15	0	OUT3D	GAFE IF Input
CHID_02P	2	3.30	15	1	OUT3D	GAFE IF Input
DISC_02	2	3.30	15	2	OUT3D	GAFE IF Input
DISC_02P	2	3.30	15	3	OUT3D	GAFE IF Input
IRTN_02	2	3.30	15	4	OUT3D	GAFE IF Return
IRTN_02P	2	3.30	15	5	OUT3D	GAFE IF Return
CHID_03	2	3.30	15	6	OUT3D	GAFE IF Input
CHID_03P	2	3.30	15	7	OUT3D	GAFE IF Input
DISC_03	2	3.30	15	8	OUT3D	GAFE IF Input
DISC_03P	2	3.30	15	9	OUT3D	GAFE IF Input
IRTN_03	2	3.30	15	10	OUT3D	GAFE IF Return
IRTN_03P	2	3.30	15	11	OUT3D	GAFE IF Return
CHID_04	2	3.30	15	12	OUT3D	GAFE IF Input
CHID_04P	2	3.30	15	13	OUT3D	GAFE IF Input
DISC_04	2	3.30	15	14	OUT3D	GAFE IF Input
DISC_04P	2	3.30	15	15	OUT3D	GAFE IF Input
IRTN_04	2	3.30	16	0	OUT3E	GAFE IF Return
IRTN_04P	2	3.30	16	1	OUT3E	GAFE IF Return
CHID_05	2	3.30	16	2	OUT3E	GAFE IF Input
CHID_05P	2	3.30	16	3	OUT3E	GAFE IF Input
DISC_05	2	3.30	16	4	OUT3E	GAFE IF Input
DISC_05P	2	3.30	16	5	OUT3E	GAFE IF Input
IRTN_05	2	3.30	16	6	OUT3E	GAFE IF Return
IRTN_05P	2	3.30	16	7	OUT3E	GAFE IF Return
CHID_06	2	3.30	16	8	OUT3E	GAFE IF Input
CHID_06P	2	3.30	16	9	OUT3E	GAFE IF Input
DISC_06	2	3.30	16	10	OUT3E	GAFE IF Input
DISC_06P	2	3.30	16	11	OUT3E	GAFE IF Input
IRTN_06	2	3.30	16	12	OUT3E	GAFE IF Return
IRTN_06P	2	3.30	16	13	OUT3E	GAFE IF Return
CHID_07	2	3.30	16	14	OUT3E	GAFE IF Input
CHID_07P	2	3.30	16	15	OUT3E	GAFE IF Input
DISC_07	2	3.30	17	0	OUT3F	GAFE IF Input
DISC_07P	2	3.30	17	1	OUT3F	GAFE IF Input
IRTN_07	2	3.30	17	2	OUT3F	GAFE IF Return

IRTN_07P	2	3.30	17	3	OUT3F	GAFE IF Return
CHID_08	2	3.30	17	4	OUT3F	GAFE IF Input
CHID_08P	2	3.30	17	5	OUT3F	GAFE IF Input
DISC_08	2	3.30	17	6	OUT3F	GAFE IF Input
DISC_08P	2	3.30	17	7	OUT3F	GAFE IF Input
IRTN_08	2	3.30	17	8	OUT3F	GAFE IF Return
IRTN_08P	2	3.30	17	9	OUT3F	GAFE IF Return
CHID_09	2	3.30	17	10	OUT3F	GAFE IF Input
CHID_09P	2	3.30	17	11	OUT3F	GAFE IF Input
DISC_09	2	3.30	17	12	OUT3F	GAFE IF Input
DISC_09P	2	3.30	17	13	OUT3F	GAFE IF Input
IRTN_09	2	3.30	17	14	OUT3F	GAFE IF Return
IRTN_09P	2	3.30	17	15	OUT3F	GAFE IF Return
DGND001	2	3.30	18	0	OUT4A	Ground Pin
DGND027	2	3.30	18	1	OUT4A	Ground Pin
DGND053	2	3.30	18	2	OUT4A	Ground Pin
DGND080	2	3.30	18	3	OUT4A	Ground Pin
DGND136	2	3.30	18	4	OUT4A	Ground Pin
DGND157	2	3.30	18	5	OUT4A	Ground Pin
DGND187	2	3.30	18	6	OUT4A	Ground Pin
DVDD024	2	3.30	18	7	OUT4A	Power Pin
DVDD052	2	3.30	18	8	OUT4A	Power Pin
DVDD078	2	3.30	18	9	OUT4A	Power Pin
DVDD132	2	3.30	18	10	OUT4A	Power Pin
DVDD155	2	3.30	18	11	OUT4A	Power Pin
DVDD208	2	3.30	18	12	OUT4A	Power Pin
CMDCK+	2	3.30	18	13	OUT4A	GAFE Diff Output
CMDCK-	2	3.30	18	14	OUT4A	GAFE Diff Output
GND	2	3.30	18	15	OUT4A	VDD/2
VDDMID	2	3.30	19	0	OUT4B	Ground
CMDD+	2	3.30	19	1	OUT4B	GAFE Diff Output
CMDD-	2	3.30	19	2	OUT4B	GAFE Diff Output
STROB+	2	3.30	19	3	OUT4B	GAFE Diff Output
STROB-	2	3.30	19	4	OUT4B	GAFE Diff Output
HOLD+	2	3.30	19	5	OUT4B	GAFE Diff Output
HOLD-	2	3.30	19	6	OUT4B	GAFE Diff Output
HLD	2	3.30	19	7	OUT4B	GAFE HLD
IRTN_HLD	2	3.30	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	2	3.30	19	9	OUT4B	Bias Pin
BIAS_DRV_L	2	3.30	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	2	3.30	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	2	3.30	19	12	OUT4B	Bias Pin
BIAS_RCVR	2	3.30	19	13	OUT4B	Bias Pin
VDDMID	2	3.30	19	14	OUT4B	VDD/2
GND	2	3.30	19	15	OUT4B	Ground
ACD_NVETO_00_AP	3	3.30	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	3	3.30	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	3	3.30	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	3	3.30	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	3	3.30	0	4	OUT1A	LVDS Out

ACD_NVETO_01_AM	3	3.30	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	3	3.30	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	3	3.30	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	3	3.30	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	3	3.30	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	3	3.30	0	10	OUT1A	LVDS Out
ACD_NVETO_02_BM	3	3.30	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	3	3.30	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	3	3.30	0	13	OUT1A	LVDS Out
VDDMID	3	3.30	0	14	OUT1A	VDD/2
GND	3	3.30	0	15	OUT1A	Ground
ACD_NVETO_03_BP	3	3.30	1	0	OUT1B	LVDS Out
ACD_NVETO_03_BM	3	3.30	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	3	3.30	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	3	3.30	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	3	3.30	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	3	3.30	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	3	3.30	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	3	3.30	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	3	3.30	1	8	OUT1B	LVDS Out
ACD_NVETO_05_BM	3	3.30	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	3	3.30	1	10	OUT1B	LVDS Out
ACD_NVETO_06_AM	3	3.30	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	3	3.30	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	3	3.30	1	13	OUT1B	LVDS Out
VDDMID	3	3.30	1	14	OUT1B	VDD/2
GND	3	3.30	1	15	OUT1B	Ground
ACD_NVETO_07_AP	3	3.30	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	3	3.30	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	3	3.30	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	3	3.30	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	3	3.30	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	3	3.30	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	3	3.30	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	3	3.30	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	3	3.30	2	8	OUT1C	LVDS Out
ACD_NVETO_09_AM	3	3.30	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	3	3.30	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	3	3.30	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	3	3.30	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	3	3.30	2	13	OUT1C	LVDS Out
VDDMID	3	3.30	2	14	OUT1C	VDD/2
GND	3	3.30	2	15	OUT1C	Ground
ACD_NVETO_10_BP	3	3.30	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	3	3.30	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	3	3.30	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	3	3.30	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	3	3.30	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	3	3.30	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	3	3.30	3	6	OUT1D	LVDS Out

ACD_NVETO_12_AM	3	3.30	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	3	3.30	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	3	3.30	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	3	3.30	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	3	3.30	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	3	3.30	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	3	3.30	3	13	OUT1D	LVDS Out
VDDMID	3	3.30	3	14	OUT1D	VDD/2
GND	3	3.30	3	15	OUT1D	Ground
ACD_NVETO_14_AP	3	3.30	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	3	3.30	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	3	3.30	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	3	3.30	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	3	3.30	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	3	3.30	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	3	3.30	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	3	3.30	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	3	3.30	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	3	3.30	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	3	3.30	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	3	3.30	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	3	3.30	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	3	3.30	4	13	OUT1E	LVDS Out
VDDMID	3	3.30	4	14	OUT1E	VDD/2
GND	3	3.30	4	15	OUT1E	Ground
ACD_NVETO_17_BP	3	3.30	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	3	3.30	5	1	OUT1F	LVDS Out
ACD_CNO_AP	3	3.30	5	2	OUT1F	LVDS Out
ACD_CNO_AM	3	3.30	5	3	OUT1F	LVDS Out
ACD_CNO_BP	3	3.30	5	4	OUT1F	LVDS Out
ACD_CNO_BM	3	3.30	5	5	OUT1F	LVDS Out
ACD_CLK_AP	3	3.30	5	6	OUT1F	LVDS In
ACD_CLK_AM	3	3.30	5	7	OUT1F	LVDS In
ACD_CLK_BP	3	3.30	5	8	OUT1F	LVDS In
ACD_CLK_BM	3	3.30	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	3	3.30	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	3	3.30	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	3	3.30	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	3	3.30	5	13	OUT1F	LVDS In
ACD_NRST_AP	3	3.30	5	14	OUT1F	LVDS In
ACD_NRST_AM	3	3.30	5	15	OUT1F	LVDS In
ACD_NRST_BP	3	3.30	6	0	OUT2A	LVDS In
ACD_NRST_BM	3	3.30	6	1	OUT2A	LVDS In
ACD_NSDATA_AP	3	3.30	6	2	OUT2A	LVDS Out
ACD_NSDATA_AM	3	3.30	6	3	OUT2A	LVDS Out
ACD_NSDATA_BP	3	3.30	6	4	OUT2A	LVDS Out
ACD_NSDATA_BM	3	3.30	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	3	3.30	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	3	3.30	6	7	OUT2A	CMOS Input
GAFE_RST	3	3.30	6	8	OUT2A	CMOS Output

GAFE_RSTP	3	3.30	6	9	OUT2A	CMOS Output
DAC_DATA	3	3.30	6	10	OUT2A	CMOS Output
DAC_DATAP	3	3.30	6	11	OUT2A	CMOS Output
DAC_CLK	3	3.30	6	12	OUT2A	CMOS Output
DAC_CLKP	3	3.30	6	13	OUT2A	CMOS Output
VDDMID	3	3.30	6	14	OUT2A	VDD/2
GND	3	3.30	6	15	OUT2A	Ground
NDAC_CS	3	3.30	7	0	OUT2B	CMOS Output
NDAC_CSP	3	3.30	7	1	OUT2B	CMOS Output
NDAC_CLR	3	3.30	7	2	OUT2B	CMOS Output
NDAC_CLRP	3	3.30	7	3	OUT2B	CMOS Output
HV_ENABLE1	3	3.30	7	4	OUT2B	CMOS Output
HV_ENABLE1P	3	3.30	7	5	OUT2B	CMOS Output
HV_ENABLE2	3	3.30	7	6	OUT2B	CMOS Output
HV_ENABLE2P	3	3.30	7	7	OUT2B	CMOS Output
ADC_CLK	3	3.30	7	8	OUT2B	CMOS Output
ADC_CLKP	3	3.30	7	9	OUT2B	CMOS Output
NADC_CS	3	3.30	7	10	OUT2B	CMOS Output
NADC_CSP	3	3.30	7	11	OUT2B	CMOS Output
DAC_DATA	3	3.30	7	12	OUT2B	CMOS Output
DAC_DATAP	3	3.30	7	13	OUT2B	CMOS Output
VDDMID	3	3.30	7	14	OUT2B	VDD/2
GND	3	3.30	7	15	OUT2B	Ground
DAC_DATA_RET	3	3.30	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	3	3.30	8	1	OUT2C	CMOS Input
HITMAP_TEST	3	3.30	8	2	OUT2C	CMOS Output
HITMAP_TESTP	3	3.30	8	3	OUT2C	CMOS Output
TRIG_TEST	3	3.30	8	4	OUT2C	CMOS Output
TRIG_TESTP	3	3.30	8	5	OUT2C	CMOS Output
FREE_ID	3	3.30	8	6	OUT2C	CMOS Output
FREE_IDP	3	3.30	8	7	OUT2C	CMOS Output
PWR_ON_RST	3	3.30	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	3	3.30	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	3	3.30	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	3	3.30	8	11	OUT2C	CMOS Output
IRTN_17P	3	3.30	8	12	OUT2C	GAFE IF Return
IRTN_17	3	3.30	8	13	OUT2C	GAFE IF Return
VDDMID	3	3.30	8	14	OUT2C	VDD/2
GND	3	3.30	8	15	OUT2C	Ground
DISC_17P	3	3.30	9	0	OUT2D	GAFE IF Input
DISC_17	3	3.30	9	1	OUT2D	GAFE IF Input
CHID_17P	3	3.30	9	2	OUT2D	GAFE IF Input
CHID_17	3	3.30	9	3	OUT2D	GAFE IF Input
IRTN_16P	3	3.30	9	4	OUT2D	GAFE IF Return
IRTN_16	3	3.30	9	5	OUT2D	GAFE IF Return
DISC_16P	3	3.30	9	6	OUT2D	GAFE IF Input
DISC_16	3	3.30	9	7	OUT2D	GAFE IF Input
CHID_16P	3	3.30	9	8	OUT2D	GAFE IF Input
CHID_16	3	3.30	9	9	OUT2D	GAFE IF Input
IRTN_15P	3	3.30	9	10	OUT2D	GAFE IF Return

IRTN_15	3	3.30	9	11	OUT2D	GAFE IF Return
DISC_15P	3	3.30	9	12	OUT2D	GAFE IF Input
DISC_15	3	3.30	9	13	OUT2D	GAFE IF Input
VDDMID	3	3.30	9	14	OUT2D	VDD/2
GND	3	3.30	9	15	OUT2D	Ground
CHID_15P	3	3.30	10	0	OUT2E	GAFE IF Input
CHID_15	3	3.30	10	1	OUT2E	GAFE IF Input
IRTN_14P	3	3.30	10	2	OUT2E	GAFE IF Return
IRTN_14	3	3.30	10	3	OUT2E	GAFE IF Return
DISC_14P	3	3.30	10	4	OUT2E	GAFE IF Input
DISC_14	3	3.30	10	5	OUT2E	GAFE IF Input
CHID_14P	3	3.30	10	6	OUT2E	GAFE IF Input
CHID_14	3	3.30	10	7	OUT2E	GAFE IF Input
IRTN_13P	3	3.30	10	8	OUT2E	GAFE IF Return
IRTN_13	3	3.30	10	9	OUT2E	GAFE IF Return
DISC_13P	3	3.30	10	10	OUT2E	GAFE IF Input
DISC_13	3	3.30	10	11	OUT2E	GAFE IF Input
CHID_13P	3	3.30	10	12	OUT2E	GAFE IF Input
CHID_13	3	3.30	10	13	OUT2E	GAFE IF Input
IRTN_12P	3	3.30	10	14	OUT2E	GAFE IF Return
IRTN_12	3	3.30	10	15	OUT2E	GAFE IF Return
DISC_12P	3	3.30	11	0	OUT2F	GAFE IF Input
DISC_12	3	3.30	11	1	OUT2F	GAFE IF Input
CHID_12P	3	3.30	11	2	OUT2F	GAFE IF Input
CHID_12	3	3.30	11	3	OUT2F	GAFE IF Input
IRTN_11P	3	3.30	11	4	OUT2F	GAFE IF Return
IRTN_11	3	3.30	11	5	OUT2F	GAFE IF Return
DISC_11P	3	3.30	11	6	OUT2F	GAFE IF Input
DISC_11	3	3.30	11	7	OUT2F	GAFE IF Input
CHID_11P	3	3.30	11	8	OUT2F	GAFE IF Input
CHID_11	3	3.30	11	9	OUT2F	GAFE IF Input
IRTN_10P	3	3.30	11	10	OUT2F	GAFE IF Return
IRTN_10	3	3.30	11	11	OUT2F	GAFE IF Return
DISC_10P	3	3.30	11	12	OUT2F	GAFE IF Input
DISC_10	3	3.30	11	13	OUT2F	GAFE IF Input
CHID_10P	3	3.30	11	14	OUT2F	GAFE IF Input
CHID_10	3	3.30	11	15	OUT2F	GAFE IF Input
PHAD_00	3	3.30	12	0	OUT3A	CMOS Input
PHAD_00P	3	3.30	12	1	OUT3A	CMOS Input
PHAD_01	3	3.30	12	2	OUT3A	CMOS Input
PHAD_01P	3	3.30	12	3	OUT3A	CMOS Input
PHAD_02	3	3.30	12	4	OUT3A	CMOS Input
PHAD_02P	3	3.30	12	5	OUT3A	CMOS Input
PHAD_03	3	3.30	12	6	OUT3A	CMOS Input
PHAD_03P	3	3.30	12	7	OUT3A	CMOS Input
PHAD_04	3	3.30	12	8	OUT3A	CMOS Input
PHAD_04P	3	3.30	12	9	OUT3A	CMOS Input
PHAD_05	3	3.30	12	10	OUT3A	CMOS Input
PHAD_05P	3	3.30	12	11	OUT3A	CMOS Input
PHAD_06	3	3.30	12	12	OUT3A	CMOS Input

PHAD_06P	3	3.30	12	13	OUT3A	CMOS Input
PHAD_07	3	3.30	12	14	OUT3A	CMOS Input
PHAD_07P	3	3.30	12	15	OUT3A	CMOS Input
PHAD_08	3	3.30	13	0	OUT3B	CMOS Input
PHAD_08P	3	3.30	13	1	OUT3B	CMOS Input
PHAD_09	3	3.30	13	2	OUT3B	CMOS Input
PHAD_09P	3	3.30	13	3	OUT3B	CMOS Input
PHAD_10	3	3.30	13	4	OUT3B	CMOS Input
PHAD_10P	3	3.30	13	5	OUT3B	CMOS Input
PHAD_11	3	3.30	13	6	OUT3B	CMOS Input
PHAD_11P	3	3.30	13	7	OUT3B	CMOS Input
PHAD_12	3	3.30	13	8	OUT3B	CMOS Input
PHAD_12P	3	3.30	13	9	OUT3B	CMOS Input
PHAD_13	3	3.30	13	10	OUT3B	CMOS Input
PHAD_13P	3	3.30	13	11	OUT3B	CMOS Input
PHAD_14	3	3.30	13	12	OUT3B	CMOS Input
PHAD_14P	3	3.30	13	13	OUT3B	CMOS Input
PHAD_15	3	3.30	13	14	OUT3B	CMOS Input
PHAD_15P	3	3.30	13	15	OUT3B	CMOS Input
PHAD_16	3	3.30	14	0	OUT3C	CMOS Input
PHAD_16P	3	3.30	14	1	OUT3C	CMOS Input
PHAD_17	3	3.30	14	2	OUT3C	CMOS Input
PHAD_17P	3	3.30	14	3	OUT3C	CMOS Input
CHID_00	3	3.30	14	4	OUT3C	GAFE IF Input
CHID_00P	3	3.30	14	5	OUT3C	GAFE IF Input
DISC_00	3	3.30	14	6	OUT3C	GAFE IF Input
DISC_00P	3	3.30	14	7	OUT3C	GAFE IF Input
IRTN_00	3	3.30	14	8	OUT3C	GAFE IF Return
IRTN_00P	3	3.30	14	9	OUT3C	GAFE IF Return
CHID_01	3	3.30	14	10	OUT3C	GAFE IF Input
CHID_01P	3	3.30	14	11	OUT3C	GAFE IF Input
DISC_01	3	3.30	14	12	OUT3C	GAFE IF Input
DISC_01P	3	3.30	14	13	OUT3C	GAFE IF Input
IRTN_01	3	3.30	14	14	OUT3C	GAFE IF Return
IRTN_01P	3	3.30	14	15	OUT3C	GAFE IF Return
CHID_02	3	3.30	15	0	OUT3D	GAFE IF Input
CHID_02P	3	3.30	15	1	OUT3D	GAFE IF Input
DISC_02	3	3.30	15	2	OUT3D	GAFE IF Input
DISC_02P	3	3.30	15	3	OUT3D	GAFE IF Input
IRTN_02	3	3.30	15	4	OUT3D	GAFE IF Return
IRTN_02P	3	3.30	15	5	OUT3D	GAFE IF Return
CHID_03	3	3.30	15	6	OUT3D	GAFE IF Input
CHID_03P	3	3.30	15	7	OUT3D	GAFE IF Input
DISC_03	3	3.30	15	8	OUT3D	GAFE IF Input
DISC_03P	3	3.30	15	9	OUT3D	GAFE IF Input
IRTN_03	3	3.30	15	10	OUT3D	GAFE IF Return
IRTN_03P	3	3.30	15	11	OUT3D	GAFE IF Return
CHID_04	3	3.30	15	12	OUT3D	GAFE IF Input
CHID_04P	3	3.30	15	13	OUT3D	GAFE IF Input
DISC_04	3	3.30	15	14	OUT3D	GAFE IF Input

DISC_04P	3	3.30	15	15	OUT3D	GAFE IF Input
IRTN_04	3	3.30	16	0	OUT3E	GAFE IF Return
IRTN_04P	3	3.30	16	1	OUT3E	GAFE IF Return
CHID_05	3	3.30	16	2	OUT3E	GAFE IF Input
CHID_05P	3	3.30	16	3	OUT3E	GAFE IF Input
DISC_05	3	3.30	16	4	OUT3E	GAFE IF Input
DISC_05P	3	3.30	16	5	OUT3E	GAFE IF Input
IRTN_05	3	3.30	16	6	OUT3E	GAFE IF Return
IRTN_05P	3	3.30	16	7	OUT3E	GAFE IF Return
CHID_06	3	3.30	16	8	OUT3E	GAFE IF Input
CHID_06P	3	3.30	16	9	OUT3E	GAFE IF Input
DISC_06	3	3.30	16	10	OUT3E	GAFE IF Input
DISC_06P	3	3.30	16	11	OUT3E	GAFE IF Input
IRTN_06	3	3.30	16	12	OUT3E	GAFE IF Return
IRTN_06P	3	3.30	16	13	OUT3E	GAFE IF Return
CHID_07	3	3.30	16	14	OUT3E	GAFE IF Input
CHID_07P	3	3.30	16	15	OUT3E	GAFE IF Input
DISC_07	3	3.30	17	0	OUT3F	GAFE IF Input
DISC_07P	3	3.30	17	1	OUT3F	GAFE IF Input
IRTN_07	3	3.30	17	2	OUT3F	GAFE IF Return
IRTN_07P	3	3.30	17	3	OUT3F	GAFE IF Return
CHID_08	3	3.30	17	4	OUT3F	GAFE IF Input
CHID_08P	3	3.30	17	5	OUT3F	GAFE IF Input
DISC_08	3	3.30	17	6	OUT3F	GAFE IF Input
DISC_08P	3	3.30	17	7	OUT3F	GAFE IF Input
IRTN_08	3	3.30	17	8	OUT3F	GAFE IF Return
IRTN_08P	3	3.30	17	9	OUT3F	GAFE IF Return
CHID_09	3	3.30	17	10	OUT3F	GAFE IF Input
CHID_09P	3	3.30	17	11	OUT3F	GAFE IF Input
DISC_09	3	3.30	17	12	OUT3F	GAFE IF Input
DISC_09P	3	3.30	17	13	OUT3F	GAFE IF Input
IRTN_09	3	3.30	17	14	OUT3F	GAFE IF Return
IRTN_09P	3	3.30	17	15	OUT3F	GAFE IF Return
DGND001	3	3.30	18	0	OUT4A	Ground Pin
DGND027	3	3.30	18	1	OUT4A	Ground Pin
DGND053	3	3.30	18	2	OUT4A	Ground Pin
DGND080	3	3.30	18	3	OUT4A	Ground Pin
DGND136	3	3.30	18	4	OUT4A	Ground Pin
DGND157	3	3.30	18	5	OUT4A	Ground Pin
DGND187	3	3.30	18	6	OUT4A	Ground Pin
DVDD024	3	3.30	18	7	OUT4A	Power Pin
DVDD052	3	3.30	18	8	OUT4A	Power Pin
DVDD078	3	3.30	18	9	OUT4A	Power Pin
DVDD132	3	3.30	18	10	OUT4A	Power Pin
DVDD155	3	3.30	18	11	OUT4A	Power Pin
DVDD208	3	3.30	18	12	OUT4A	Power Pin
CMDCK+	3	3.30	18	13	OUT4A	GAFE Diff Output
CMDCK-	3	3.30	18	14	OUT4A	GAFE Diff Output
GND	3	3.30	18	15	OUT4A	VDD/2
VDDMID	3	3.30	19	0	OUT4B	Ground

CMDD+	3	3.30	19	1	OUT4B	GAFE Diff Output
CMDD-	3	3.30	19	2	OUT4B	GAFE Diff Output
STROB+	3	3.30	19	3	OUT4B	GAFE Diff Output
STROB-	3	3.30	19	4	OUT4B	GAFE Diff Output
HOLD+	3	3.30	19	5	OUT4B	GAFE Diff Output
HOLD-	3	3.30	19	6	OUT4B	GAFE Diff Output
HLD	3	3.30	19	7	OUT4B	GAFE HLD
IRTN_HLD	3	3.30	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	3	3.30	19	9	OUT4B	Bias Pin
BIAS_DRV_L	3	3.30	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	3	3.30	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	3	3.30	19	12	OUT4B	Bias Pin
BIAS_RCVR	3	3.30	19	13	OUT4B	Bias Pin
VDDMID	3	3.30	19	14	OUT4B	VDD/2
GND	3	3.30	19	15	OUT4B	Ground

Parameter	Mode	Power Supply	Mux	Channel	Parametric Board Signal Port	
					OUT1A	OUT1B
ACD_NVETO_00_AP	1	3.00	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	1	3.00	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	1	3.00	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	1	3.00	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	1	3.00	0	4	OUT1A	LVDS Out
ACD_NVETO_01_AM	1	3.00	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	1	3.00	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	1	3.00	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	1	3.00	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	1	3.00	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	1	3.00	0	10	OUT1A	LVDS Out
ACD_NVETO_02_BM	1	3.00	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	1	3.00	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	1	3.00	0	13	OUT1A	LVDS Out
VDDMID	1	3.00	0	14	OUT1A	VDD/2
GND	1	3.00	0	15	OUT1A	Ground
ACD_NVETO_03_BP	1	3.00	1	0	OUT1B	LVDS Out
ACD_NVETO_03_BM	1	3.00	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	1	3.00	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	1	3.00	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	1	3.00	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	1	3.00	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	1	3.00	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	1	3.00	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	1	3.00	1	8	OUT1B	LVDS Out
ACD_NVETO_05_BM	1	3.00	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	1	3.00	1	10	OUT1B	LVDS Out
ACD_NVETO_06_AM	1	3.00	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	1	3.00	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	1	3.00	1	13	OUT1B	LVDS Out
VDDMID	1	3.00	1	14	OUT1B	VDD/2

GND	1	3.00	1	15	OUT1B	Ground
ACD_NVETO_07_AP	1	3.00	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	1	3.00	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	1	3.00	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	1	3.00	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	1	3.00	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	1	3.00	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	1	3.00	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	1	3.00	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	1	3.00	2	8	OUT1C	LVDS Out
ACD_NVETO_09_AM	1	3.00	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	1	3.00	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	1	3.00	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	1	3.00	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	1	3.00	2	13	OUT1C	LVDS Out
VDDMID	1	3.00	2	14	OUT1C	VDD/2
GND	1	3.00	2	15	OUT1C	Ground
ACD_NVETO_10_BP	1	3.00	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	1	3.00	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	1	3.00	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	1	3.00	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	1	3.00	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	1	3.00	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	1	3.00	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	1	3.00	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	1	3.00	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	1	3.00	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	1	3.00	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	1	3.00	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	1	3.00	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	1	3.00	3	13	OUT1D	LVDS Out
VDDMID	1	3.00	3	14	OUT1D	VDD/2
GND	1	3.00	3	15	OUT1D	Ground
ACD_NVETO_14_AP	1	3.00	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	1	3.00	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	1	3.00	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	1	3.00	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	1	3.00	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	1	3.00	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	1	3.00	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	1	3.00	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	1	3.00	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	1	3.00	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	1	3.00	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	1	3.00	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	1	3.00	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	1	3.00	4	13	OUT1E	LVDS Out
VDDMID	1	3.00	4	14	OUT1E	VDD/2
GND	1	3.00	4	15	OUT1E	Ground
ACD_NVETO_17_BP	1	3.00	5	0	OUT1F	LVDS Out

ACD_NVETO_17_BM	1	3.00	5	1	OUT1F	LVDS Out
ACD_CNO_AP	1	3.00	5	2	OUT1F	LVDS Out
ACD_CNO_AM	1	3.00	5	3	OUT1F	LVDS Out
ACD_CNO_BP	1	3.00	5	4	OUT1F	LVDS Out
ACD_CNO_BM	1	3.00	5	5	OUT1F	LVDS Out
ACD_CLK_AP	1	3.00	5	6	OUT1F	LVDS In
ACD_CLK_AM	1	3.00	5	7	OUT1F	LVDS In
ACD_CLK_BP	1	3.00	5	8	OUT1F	LVDS In
ACD_CLK_BM	1	3.00	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	1	3.00	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	1	3.00	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	1	3.00	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	1	3.00	5	13	OUT1F	LVDS In
ACD_NRST_AP	1	3.00	5	14	OUT1F	LVDS In
ACD_NRST_AM	1	3.00	5	15	OUT1F	LVDS In
ACD_NRST_BP	1	3.00	6	0	OUT2A	LVDS In
ACD_NRST_BM	1	3.00	6	1	OUT2A	LVDS In
ACD_NSADATA_AP	1	3.00	6	2	OUT2A	LVDS Out
ACD_NSADATA_AM	1	3.00	6	3	OUT2A	LVDS Out
ACD_NSADATA_BP	1	3.00	6	4	OUT2A	LVDS Out
ACD_NSADATA_BM	1	3.00	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	1	3.00	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	1	3.00	6	7	OUT2A	CMOS Input
GAFE_RST	1	3.00	6	8	OUT2A	CMOS Output
GAFE_RSTP	1	3.00	6	9	OUT2A	CMOS Output
DAC_DATA	1	3.00	6	10	OUT2A	CMOS Output
DAC_DATAP	1	3.00	6	11	OUT2A	CMOS Output
DAC_CLK	1	3.00	6	12	OUT2A	CMOS Output
DAC_CLKP	1	3.00	6	13	OUT2A	CMOS Output
VDDMID	1	3.00	6	14	OUT2A	VDD/2
GND	1	3.00	6	15	OUT2A	Ground
NDAC_CS	1	3.00	7	0	OUT2B	CMOS Output
NDAC_CSP	1	3.00	7	1	OUT2B	CMOS Output
NDAC_CLR	1	3.00	7	2	OUT2B	CMOS Output
NDAC_CLRP	1	3.00	7	3	OUT2B	CMOS Output
HV_ENABLE1	1	3.00	7	4	OUT2B	CMOS Output
HV_ENABLE1P	1	3.00	7	5	OUT2B	CMOS Output
HV_ENABLE2	1	3.00	7	6	OUT2B	CMOS Output
HV_ENABLE2P	1	3.00	7	7	OUT2B	CMOS Output
ADC_CLK	1	3.00	7	8	OUT2B	CMOS Output
ADC_CLKP	1	3.00	7	9	OUT2B	CMOS Output
NADC_CS	1	3.00	7	10	OUT2B	CMOS Output
NADC_CSP	1	3.00	7	11	OUT2B	CMOS Output
DAC_DATA	1	3.00	7	12	OUT2B	CMOS Output
DAC_DATAP	1	3.00	7	13	OUT2B	CMOS Output
VDDMID	1	3.00	7	14	OUT2B	VDD/2
GND	1	3.00	7	15	OUT2B	Ground
DAC_DATA_RET	1	3.00	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	1	3.00	8	1	OUT2C	CMOS Input
HITMAP_TEST	1	3.00	8	2	OUT2C	CMOS Output

HITMAP_TESTP	1	3.00	8	3	OUT2C	CMOS Output
TRIG_TEST	1	3.00	8	4	OUT2C	CMOS Output
TRIG_TESTP	1	3.00	8	5	OUT2C	CMOS Output
FREE_ID	1	3.00	8	6	OUT2C	CMOS Output
FREE_IDP	1	3.00	8	7	OUT2C	CMOS Output
PWR_ON_RST	1	3.00	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	1	3.00	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	1	3.00	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	1	3.00	8	11	OUT2C	CMOS Output
IRTN_17P	1	3.00	8	12	OUT2C	GAFE IF Return
IRTN_17	1	3.00	8	13	OUT2C	GAFE IF Return
VDDMID	1	3.00	8	14	OUT2C	VDD/2
GND	1	3.00	8	15	OUT2C	Ground
DISC_17P	1	3.00	9	0	OUT2D	GAFE IF Input
DISC_17	1	3.00	9	1	OUT2D	GAFE IF Input
CHID_17P	1	3.00	9	2	OUT2D	GAFE IF Input
CHID_17	1	3.00	9	3	OUT2D	GAFE IF Input
IRTN_16P	1	3.00	9	4	OUT2D	GAFE IF Return
IRTN_16	1	3.00	9	5	OUT2D	GAFE IF Return
DISC_16P	1	3.00	9	6	OUT2D	GAFE IF Input
DISC_16	1	3.00	9	7	OUT2D	GAFE IF Input
CHID_16P	1	3.00	9	8	OUT2D	GAFE IF Input
CHID_16	1	3.00	9	9	OUT2D	GAFE IF Input
IRTN_15P	1	3.00	9	10	OUT2D	GAFE IF Return
IRTN_15	1	3.00	9	11	OUT2D	GAFE IF Return
DISC_15P	1	3.00	9	12	OUT2D	GAFE IF Input
DISC_15	1	3.00	9	13	OUT2D	GAFE IF Input
VDDMID	1	3.00	9	14	OUT2D	VDD/2
GND	1	3.00	9	15	OUT2D	Ground
CHID_15P	1	3.00	10	0	OUT2E	GAFE IF Input
CHID_15	1	3.00	10	1	OUT2E	GAFE IF Input
IRTN_14P	1	3.00	10	2	OUT2E	GAFE IF Return
IRTN_14	1	3.00	10	3	OUT2E	GAFE IF Return
DISC_14P	1	3.00	10	4	OUT2E	GAFE IF Input
DISC_14	1	3.00	10	5	OUT2E	GAFE IF Input
CHID_14P	1	3.00	10	6	OUT2E	GAFE IF Input
CHID_14	1	3.00	10	7	OUT2E	GAFE IF Input
IRTN_13P	1	3.00	10	8	OUT2E	GAFE IF Return
IRTN_13	1	3.00	10	9	OUT2E	GAFE IF Return
DISC_13P	1	3.00	10	10	OUT2E	GAFE IF Input
DISC_13	1	3.00	10	11	OUT2E	GAFE IF Input
CHID_13P	1	3.00	10	12	OUT2E	GAFE IF Input
CHID_13	1	3.00	10	13	OUT2E	GAFE IF Input
IRTN_12P	1	3.00	10	14	OUT2E	GAFE IF Return
IRTN_12	1	3.00	10	15	OUT2E	GAFE IF Return
DISC_12P	1	3.00	11	0	OUT2F	GAFE IF Input
DISC_12	1	3.00	11	1	OUT2F	GAFE IF Input
CHID_12P	1	3.00	11	2	OUT2F	GAFE IF Input
CHID_12	1	3.00	11	3	OUT2F	GAFE IF Input
IRTN_11P	1	3.00	11	4	OUT2F	GAFE IF Return

IRTN_11	1	3.00	11	5	OUT2F	GAFE IF Return
DISC_11P	1	3.00	11	6	OUT2F	GAFE IF Input
DISC_11	1	3.00	11	7	OUT2F	GAFE IF Input
CHID_11P	1	3.00	11	8	OUT2F	GAFE IF Input
CHID_11	1	3.00	11	9	OUT2F	GAFE IF Input
IRTN_10P	1	3.00	11	10	OUT2F	GAFE IF Return
IRTN_10	1	3.00	11	11	OUT2F	GAFE IF Return
DISC_10P	1	3.00	11	12	OUT2F	GAFE IF Input
DISC_10	1	3.00	11	13	OUT2F	GAFE IF Input
CHID_10P	1	3.00	11	14	OUT2F	GAFE IF Input
CHID_10	1	3.00	11	15	OUT2F	GAFE IF Input
PHAD_00	1	3.00	12	0	OUT3A	CMOS Input
PHAD_00P	1	3.00	12	1	OUT3A	CMOS Input
PHAD_01	1	3.00	12	2	OUT3A	CMOS Input
PHAD_01P	1	3.00	12	3	OUT3A	CMOS Input
PHAD_02	1	3.00	12	4	OUT3A	CMOS Input
PHAD_02P	1	3.00	12	5	OUT3A	CMOS Input
PHAD_03	1	3.00	12	6	OUT3A	CMOS Input
PHAD_03P	1	3.00	12	7	OUT3A	CMOS Input
PHAD_04	1	3.00	12	8	OUT3A	CMOS Input
PHAD_04P	1	3.00	12	9	OUT3A	CMOS Input
PHAD_05	1	3.00	12	10	OUT3A	CMOS Input
PHAD_05P	1	3.00	12	11	OUT3A	CMOS Input
PHAD_06	1	3.00	12	12	OUT3A	CMOS Input
PHAD_06P	1	3.00	12	13	OUT3A	CMOS Input
PHAD_07	1	3.00	12	14	OUT3A	CMOS Input
PHAD_07P	1	3.00	12	15	OUT3A	CMOS Input
PHAD_08	1	3.00	13	0	OUT3B	CMOS Input
PHAD_08P	1	3.00	13	1	OUT3B	CMOS Input
PHAD_09	1	3.00	13	2	OUT3B	CMOS Input
PHAD_09P	1	3.00	13	3	OUT3B	CMOS Input
PHAD_10	1	3.00	13	4	OUT3B	CMOS Input
PHAD_10P	1	3.00	13	5	OUT3B	CMOS Input
PHAD_11	1	3.00	13	6	OUT3B	CMOS Input
PHAD_11P	1	3.00	13	7	OUT3B	CMOS Input
PHAD_12	1	3.00	13	8	OUT3B	CMOS Input
PHAD_12P	1	3.00	13	9	OUT3B	CMOS Input
PHAD_13	1	3.00	13	10	OUT3B	CMOS Input
PHAD_13P	1	3.00	13	11	OUT3B	CMOS Input
PHAD_14	1	3.00	13	12	OUT3B	CMOS Input
PHAD_14P	1	3.00	13	13	OUT3B	CMOS Input
PHAD_15	1	3.00	13	14	OUT3B	CMOS Input
PHAD_15P	1	3.00	13	15	OUT3B	CMOS Input
PHAD_16	1	3.00	14	0	OUT3C	CMOS Input
PHAD_16P	1	3.00	14	1	OUT3C	CMOS Input
PHAD_17	1	3.00	14	2	OUT3C	CMOS Input
PHAD_17P	1	3.00	14	3	OUT3C	CMOS Input
CHID_00	1	3.00	14	4	OUT3C	GAFE IF Input
CHID_00P	1	3.00	14	5	OUT3C	GAFE IF Input
DISC_00	1	3.00	14	6	OUT3C	GAFE IF Input

DISC_00P	1	3.00	14	7	OUT3C	GAFE IF Input
IRTN_00	1	3.00	14	8	OUT3C	GAFE IF Return
IRTN_00P	1	3.00	14	9	OUT3C	GAFE IF Return
CHID_01	1	3.00	14	10	OUT3C	GAFE IF Input
CHID_01P	1	3.00	14	11	OUT3C	GAFE IF Input
DISC_01	1	3.00	14	12	OUT3C	GAFE IF Input
DISC_01P	1	3.00	14	13	OUT3C	GAFE IF Input
IRTN_01	1	3.00	14	14	OUT3C	GAFE IF Return
IRTN_01P	1	3.00	14	15	OUT3C	GAFE IF Return
CHID_02	1	3.00	15	0	OUT3D	GAFE IF Input
CHID_02P	1	3.00	15	1	OUT3D	GAFE IF Input
DISC_02	1	3.00	15	2	OUT3D	GAFE IF Input
DISC_02P	1	3.00	15	3	OUT3D	GAFE IF Input
IRTN_02	1	3.00	15	4	OUT3D	GAFE IF Return
IRTN_02P	1	3.00	15	5	OUT3D	GAFE IF Return
CHID_03	1	3.00	15	6	OUT3D	GAFE IF Input
CHID_03P	1	3.00	15	7	OUT3D	GAFE IF Input
DISC_03	1	3.00	15	8	OUT3D	GAFE IF Input
DISC_03P	1	3.00	15	9	OUT3D	GAFE IF Input
IRTN_03	1	3.00	15	10	OUT3D	GAFE IF Return
IRTN_03P	1	3.00	15	11	OUT3D	GAFE IF Return
CHID_04	1	3.00	15	12	OUT3D	GAFE IF Input
CHID_04P	1	3.00	15	13	OUT3D	GAFE IF Input
DISC_04	1	3.00	15	14	OUT3D	GAFE IF Input
DISC_04P	1	3.00	15	15	OUT3D	GAFE IF Input
IRTN_04	1	3.00	16	0	OUT3E	GAFE IF Return
IRTN_04P	1	3.00	16	1	OUT3E	GAFE IF Return
CHID_05	1	3.00	16	2	OUT3E	GAFE IF Input
CHID_05P	1	3.00	16	3	OUT3E	GAFE IF Input
DISC_05	1	3.00	16	4	OUT3E	GAFE IF Input
DISC_05P	1	3.00	16	5	OUT3E	GAFE IF Input
IRTN_05	1	3.00	16	6	OUT3E	GAFE IF Return
IRTN_05P	1	3.00	16	7	OUT3E	GAFE IF Return
CHID_06	1	3.00	16	8	OUT3E	GAFE IF Input
CHID_06P	1	3.00	16	9	OUT3E	GAFE IF Input
DISC_06	1	3.00	16	10	OUT3E	GAFE IF Input
DISC_06P	1	3.00	16	11	OUT3E	GAFE IF Input
IRTN_06	1	3.00	16	12	OUT3E	GAFE IF Return
IRTN_06P	1	3.00	16	13	OUT3E	GAFE IF Return
CHID_07	1	3.00	16	14	OUT3E	GAFE IF Input
CHID_07P	1	3.00	16	15	OUT3E	GAFE IF Input
DISC_07	1	3.00	17	0	OUT3F	GAFE IF Input
DISC_07P	1	3.00	17	1	OUT3F	GAFE IF Input
IRTN_07	1	3.00	17	2	OUT3F	GAFE IF Return
IRTN_07P	1	3.00	17	3	OUT3F	GAFE IF Return
CHID_08	1	3.00	17	4	OUT3F	GAFE IF Input
CHID_08P	1	3.00	17	5	OUT3F	GAFE IF Input
DISC_08	1	3.00	17	6	OUT3F	GAFE IF Input
DISC_08P	1	3.00	17	7	OUT3F	GAFE IF Input
IRTN_08	1	3.00	17	8	OUT3F	GAFE IF Return

IRTN_08P	1	3.00	17	9	OUT3F	GAFE IF Return
CHID_09	1	3.00	17	10	OUT3F	GAFE IF Input
CHID_09P	1	3.00	17	11	OUT3F	GAFE IF Input
DISC_09	1	3.00	17	12	OUT3F	GAFE IF Input
DISC_09P	1	3.00	17	13	OUT3F	GAFE IF Input
IRTN_09	1	3.00	17	14	OUT3F	GAFE IF Return
IRTN_09P	1	3.00	17	15	OUT3F	GAFE IF Return
DGND001	1	3.00	18	0	OUT4A	Ground Pin
DGND027	1	3.00	18	1	OUT4A	Ground Pin
DGND053	1	3.00	18	2	OUT4A	Ground Pin
DGND080	1	3.00	18	3	OUT4A	Ground Pin
DGND136	1	3.00	18	4	OUT4A	Ground Pin
DGND157	1	3.00	18	5	OUT4A	Ground Pin
DGND187	1	3.00	18	6	OUT4A	Ground Pin
DVDD024	1	3.00	18	7	OUT4A	Power Pin
DVDD052	1	3.00	18	8	OUT4A	Power Pin
DVDD078	1	3.00	18	9	OUT4A	Power Pin
DVDD132	1	3.00	18	10	OUT4A	Power Pin
DVDD155	1	3.00	18	11	OUT4A	Power Pin
DVDD208	1	3.00	18	12	OUT4A	Power Pin
CMDCK+	1	3.00	18	13	OUT4A	GAFE Diff Output
CMDCK-	1	3.00	18	14	OUT4A	GAFE Diff Output
GND	1	3.00	18	15	OUT4A	VDD/2
VDDMID	1	3.00	19	0	OUT4B	Ground
CMDD+	1	3.00	19	1	OUT4B	GAFE Diff Output
CMDD-	1	3.00	19	2	OUT4B	GAFE Diff Output
STROB+	1	3.00	19	3	OUT4B	GAFE Diff Output
STROB-	1	3.00	19	4	OUT4B	GAFE Diff Output
HOLD+	1	3.00	19	5	OUT4B	GAFE Diff Output
HOLD-	1	3.00	19	6	OUT4B	GAFE Diff Output
HLD	1	3.00	19	7	OUT4B	GAFE HLD
IRTN_HLD	1	3.00	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	1	3.00	19	9	OUT4B	Bias Pin
BIAS_DRV_L	1	3.00	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	1	3.00	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	1	3.00	19	12	OUT4B	Bias Pin
BIAS_RCVR	1	3.00	19	13	OUT4B	Bias Pin
VDDMID	1	3.00	19	14	OUT4B	VDD/2
GND	1	3.00	19	15	OUT4B	Ground
ACD_NVETO_00_AP	2	3.00	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	2	3.00	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	2	3.00	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	2	3.00	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	2	3.00	0	4	OUT1A	LVDS Out
ACD_NVETO_01_AM	2	3.00	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	2	3.00	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	2	3.00	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	2	3.00	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	2	3.00	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	2	3.00	0	10	OUT1A	LVDS Out

ACD_NVETO_02_BM	2	3.00	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	2	3.00	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	2	3.00	0	13	OUT1A	LVDS Out
VDDMID	2	3.00	0	14	OUT1A	VDD/2
GND	2	3.00	0	15	OUT1A	Ground
ACD_NVETO_03_BP	2	3.00	1	0	OUT1B	LVDS Out
ACD_NVETO_03_BM	2	3.00	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	2	3.00	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	2	3.00	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	2	3.00	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	2	3.00	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	2	3.00	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	2	3.00	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	2	3.00	1	8	OUT1B	LVDS Out
ACD_NVETO_05_BM	2	3.00	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	2	3.00	1	10	OUT1B	LVDS Out
ACD_NVETO_06_AM	2	3.00	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	2	3.00	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	2	3.00	1	13	OUT1B	LVDS Out
VDDMID	2	3.00	1	14	OUT1B	VDD/2
GND	2	3.00	1	15	OUT1B	Ground
ACD_NVETO_07_AP	2	3.00	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	2	3.00	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	2	3.00	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	2	3.00	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	2	3.00	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	2	3.00	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	2	3.00	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	2	3.00	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	2	3.00	2	8	OUT1C	LVDS Out
ACD_NVETO_09_AM	2	3.00	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	2	3.00	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	2	3.00	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	2	3.00	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	2	3.00	2	13	OUT1C	LVDS Out
VDDMID	2	3.00	2	14	OUT1C	VDD/2
GND	2	3.00	2	15	OUT1C	Ground
ACD_NVETO_10_BP	2	3.00	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	2	3.00	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	2	3.00	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	2	3.00	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	2	3.00	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	2	3.00	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	2	3.00	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	2	3.00	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	2	3.00	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	2	3.00	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	2	3.00	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	2	3.00	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	2	3.00	3	12	OUT1D	LVDS Out

ACD_NVETO_13_BM	2	3.00	3	13	OUT1D	LVDS Out
VDDMD	2	3.00	3	14	OUT1D	VDD/2
GND	2	3.00	3	15	OUT1D	Ground
ACD_NVETO_14_AP	2	3.00	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	2	3.00	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	2	3.00	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	2	3.00	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	2	3.00	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	2	3.00	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	2	3.00	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	2	3.00	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	2	3.00	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	2	3.00	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	2	3.00	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	2	3.00	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	2	3.00	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	2	3.00	4	13	OUT1E	LVDS Out
VDDMD	2	3.00	4	14	OUT1E	VDD/2
GND	2	3.00	4	15	OUT1E	Ground
ACD_NVETO_17_BP	2	3.00	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	2	3.00	5	1	OUT1F	LVDS Out
ACD_CNO_AP	2	3.00	5	2	OUT1F	LVDS Out
ACD_CNO_AM	2	3.00	5	3	OUT1F	LVDS Out
ACD_CNO_BP	2	3.00	5	4	OUT1F	LVDS Out
ACD_CNO_BM	2	3.00	5	5	OUT1F	LVDS Out
ACD_CLK_AP	2	3.00	5	6	OUT1F	LVDS In
ACD_CLK_AM	2	3.00	5	7	OUT1F	LVDS In
ACD_CLK_BP	2	3.00	5	8	OUT1F	LVDS In
ACD_CLK_BM	2	3.00	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	2	3.00	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	2	3.00	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	2	3.00	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	2	3.00	5	13	OUT1F	LVDS In
ACD_NRST_AP	2	3.00	5	14	OUT1F	LVDS In
ACD_NRST_AM	2	3.00	5	15	OUT1F	LVDS In
ACD_NRST_BP	2	3.00	6	0	OUT2A	LVDS In
ACD_NRST_BM	2	3.00	6	1	OUT2A	LVDS In
ACD_NS DATA_AP	2	3.00	6	2	OUT2A	LVDS Out
ACD_NS DATA_AM	2	3.00	6	3	OUT2A	LVDS Out
ACD_NS DATA_BP	2	3.00	6	4	OUT2A	LVDS Out
ACD_NS DATA_BM	2	3.00	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	2	3.00	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	2	3.00	6	7	OUT2A	CMOS Input
GAFE_RST	2	3.00	6	8	OUT2A	CMOS Output
GAFE_RSTP	2	3.00	6	9	OUT2A	CMOS Output
DAC_DATA	2	3.00	6	10	OUT2A	CMOS Output
DAC_DATAP	2	3.00	6	11	OUT2A	CMOS Output
DAC_CLK	2	3.00	6	12	OUT2A	CMOS Output
DAC_CLKP	2	3.00	6	13	OUT2A	CMOS Output
VDDMD	2	3.00	6	14	OUT2A	VDD/2

GND	2	3.00	6	15	OUT2A	Ground
NDAC_CS	2	3.00	7	0	OUT2B	CMOS Output
NDAC_CSP	2	3.00	7	1	OUT2B	CMOS Output
NDAC_CLR	2	3.00	7	2	OUT2B	CMOS Output
NDAC_CLRP	2	3.00	7	3	OUT2B	CMOS Output
HV_ENABLE1	2	3.00	7	4	OUT2B	CMOS Output
HV_ENABLE1P	2	3.00	7	5	OUT2B	CMOS Output
HV_ENABLE2	2	3.00	7	6	OUT2B	CMOS Output
HV_ENABLE2P	2	3.00	7	7	OUT2B	CMOS Output
ADC_CLK	2	3.00	7	8	OUT2B	CMOS Output
ADC_CLKP	2	3.00	7	9	OUT2B	CMOS Output
NADC_CS	2	3.00	7	10	OUT2B	CMOS Output
NADC_CSP	2	3.00	7	11	OUT2B	CMOS Output
DAC_DATA	2	3.00	7	12	OUT2B	CMOS Output
DAC_DATAP	2	3.00	7	13	OUT2B	CMOS Output
VDDMID	2	3.00	7	14	OUT2B	VDD/2
GND	2	3.00	7	15	OUT2B	Ground
DAC_DATA_RET	2	3.00	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	2	3.00	8	1	OUT2C	CMOS Input
HITMAP_TEST	2	3.00	8	2	OUT2C	CMOS Output
HITMAP_TESTP	2	3.00	8	3	OUT2C	CMOS Output
TRIG_TEST	2	3.00	8	4	OUT2C	CMOS Output
TRIG_TESTP	2	3.00	8	5	OUT2C	CMOS Output
FREE_ID	2	3.00	8	6	OUT2C	CMOS Output
FREE_IDP	2	3.00	8	7	OUT2C	CMOS Output
PWR_ON_RST	2	3.00	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	2	3.00	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	2	3.00	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	2	3.00	8	11	OUT2C	CMOS Output
IRTN_17P	2	3.00	8	12	OUT2C	GAFE IF Return
IRTN_17	2	3.00	8	13	OUT2C	GAFE IF Return
VDDMID	2	3.00	8	14	OUT2C	VDD/2
GND	2	3.00	8	15	OUT2C	Ground
DISC_17P	2	3.00	9	0	OUT2D	GAFE IF Input
DISC_17	2	3.00	9	1	OUT2D	GAFE IF Input
CHID_17P	2	3.00	9	2	OUT2D	GAFE IF Input
CHID_17	2	3.00	9	3	OUT2D	GAFE IF Input
IRTN_16P	2	3.00	9	4	OUT2D	GAFE IF Return
IRTN_16	2	3.00	9	5	OUT2D	GAFE IF Return
DISC_16P	2	3.00	9	6	OUT2D	GAFE IF Input
DISC_16	2	3.00	9	7	OUT2D	GAFE IF Input
CHID_16P	2	3.00	9	8	OUT2D	GAFE IF Input
CHID_16	2	3.00	9	9	OUT2D	GAFE IF Input
IRTN_15P	2	3.00	9	10	OUT2D	GAFE IF Return
IRTN_15	2	3.00	9	11	OUT2D	GAFE IF Return
DISC_15P	2	3.00	9	12	OUT2D	GAFE IF Input
DISC_15	2	3.00	9	13	OUT2D	GAFE IF Input
VDDMID	2	3.00	9	14	OUT2D	VDD/2
GND	2	3.00	9	15	OUT2D	Ground
CHID_15P	2	3.00	10	0	OUT2E	GAFE IF Input

CHID_15	2	3.00	10	1	OUT2E	GAFE IF Input
IRTN_14P	2	3.00	10	2	OUT2E	GAFE IF Return
IRTN_14	2	3.00	10	3	OUT2E	GAFE IF Return
DISC_14P	2	3.00	10	4	OUT2E	GAFE IF Input
DISC_14	2	3.00	10	5	OUT2E	GAFE IF Input
CHID_14P	2	3.00	10	6	OUT2E	GAFE IF Input
CHID_14	2	3.00	10	7	OUT2E	GAFE IF Input
IRTN_13P	2	3.00	10	8	OUT2E	GAFE IF Return
IRTN_13	2	3.00	10	9	OUT2E	GAFE IF Return
DISC_13P	2	3.00	10	10	OUT2E	GAFE IF Input
DISC_13	2	3.00	10	11	OUT2E	GAFE IF Input
CHID_13P	2	3.00	10	12	OUT2E	GAFE IF Input
CHID_13	2	3.00	10	13	OUT2E	GAFE IF Input
IRTN_12P	2	3.00	10	14	OUT2E	GAFE IF Return
IRTN_12	2	3.00	10	15	OUT2E	GAFE IF Return
DISC_12P	2	3.00	11	0	OUT2F	GAFE IF Input
DISC_12	2	3.00	11	1	OUT2F	GAFE IF Input
CHID_12P	2	3.00	11	2	OUT2F	GAFE IF Input
CHID_12	2	3.00	11	3	OUT2F	GAFE IF Input
IRTN_11P	2	3.00	11	4	OUT2F	GAFE IF Return
IRTN_11	2	3.00	11	5	OUT2F	GAFE IF Return
DISC_11P	2	3.00	11	6	OUT2F	GAFE IF Input
DISC_11	2	3.00	11	7	OUT2F	GAFE IF Input
CHID_11P	2	3.00	11	8	OUT2F	GAFE IF Input
CHID_11	2	3.00	11	9	OUT2F	GAFE IF Input
IRTN_10P	2	3.00	11	10	OUT2F	GAFE IF Return
IRTN_10	2	3.00	11	11	OUT2F	GAFE IF Return
DISC_10P	2	3.00	11	12	OUT2F	GAFE IF Input
DISC_10	2	3.00	11	13	OUT2F	GAFE IF Input
CHID_10P	2	3.00	11	14	OUT2F	GAFE IF Input
CHID_10	2	3.00	11	15	OUT2F	GAFE IF Input
PHAD_00	2	3.00	12	0	OUT3A	CMOS Input
PHAD_00P	2	3.00	12	1	OUT3A	CMOS Input
PHAD_01	2	3.00	12	2	OUT3A	CMOS Input
PHAD_01P	2	3.00	12	3	OUT3A	CMOS Input
PHAD_02	2	3.00	12	4	OUT3A	CMOS Input
PHAD_02P	2	3.00	12	5	OUT3A	CMOS Input
PHAD_03	2	3.00	12	6	OUT3A	CMOS Input
PHAD_03P	2	3.00	12	7	OUT3A	CMOS Input
PHAD_04	2	3.00	12	8	OUT3A	CMOS Input
PHAD_04P	2	3.00	12	9	OUT3A	CMOS Input
PHAD_05	2	3.00	12	10	OUT3A	CMOS Input
PHAD_05P	2	3.00	12	11	OUT3A	CMOS Input
PHAD_06	2	3.00	12	12	OUT3A	CMOS Input
PHAD_06P	2	3.00	12	13	OUT3A	CMOS Input
PHAD_07	2	3.00	12	14	OUT3A	CMOS Input
PHAD_07P	2	3.00	12	15	OUT3A	CMOS Input
PHAD_08	2	3.00	13	0	OUT3B	CMOS Input
PHAD_08P	2	3.00	13	1	OUT3B	CMOS Input
PHAD_09	2	3.00	13	2	OUT3B	CMOS Input

PHAD_09P	2	3.00	13	3	OUT3B	CMOS Input
PHAD_10	2	3.00	13	4	OUT3B	CMOS Input
PHAD_10P	2	3.00	13	5	OUT3B	CMOS Input
PHAD_11	2	3.00	13	6	OUT3B	CMOS Input
PHAD_11P	2	3.00	13	7	OUT3B	CMOS Input
PHAD_12	2	3.00	13	8	OUT3B	CMOS Input
PHAD_12P	2	3.00	13	9	OUT3B	CMOS Input
PHAD_13	2	3.00	13	10	OUT3B	CMOS Input
PHAD_13P	2	3.00	13	11	OUT3B	CMOS Input
PHAD_14	2	3.00	13	12	OUT3B	CMOS Input
PHAD_14P	2	3.00	13	13	OUT3B	CMOS Input
PHAD_15	2	3.00	13	14	OUT3B	CMOS Input
PHAD_15P	2	3.00	13	15	OUT3B	CMOS Input
PHAD_16	2	3.00	14	0	OUT3C	CMOS Input
PHAD_16P	2	3.00	14	1	OUT3C	CMOS Input
PHAD_17	2	3.00	14	2	OUT3C	CMOS Input
PHAD_17P	2	3.00	14	3	OUT3C	CMOS Input
CHID_00	2	3.00	14	4	OUT3C	GAFE IF Input
CHID_00P	2	3.00	14	5	OUT3C	GAFE IF Input
DISC_00	2	3.00	14	6	OUT3C	GAFE IF Input
DISC_00P	2	3.00	14	7	OUT3C	GAFE IF Input
IRTN_00	2	3.00	14	8	OUT3C	GAFE IF Return
IRTN_00P	2	3.00	14	9	OUT3C	GAFE IF Return
CHID_01	2	3.00	14	10	OUT3C	GAFE IF Input
CHID_01P	2	3.00	14	11	OUT3C	GAFE IF Input
DISC_01	2	3.00	14	12	OUT3C	GAFE IF Input
DISC_01P	2	3.00	14	13	OUT3C	GAFE IF Input
IRTN_01	2	3.00	14	14	OUT3C	GAFE IF Return
IRTN_01P	2	3.00	14	15	OUT3C	GAFE IF Return
CHID_02	2	3.00	15	0	OUT3D	GAFE IF Input
CHID_02P	2	3.00	15	1	OUT3D	GAFE IF Input
DISC_02	2	3.00	15	2	OUT3D	GAFE IF Input
DISC_02P	2	3.00	15	3	OUT3D	GAFE IF Input
IRTN_02	2	3.00	15	4	OUT3D	GAFE IF Return
IRTN_02P	2	3.00	15	5	OUT3D	GAFE IF Return
CHID_03	2	3.00	15	6	OUT3D	GAFE IF Input
CHID_03P	2	3.00	15	7	OUT3D	GAFE IF Input
DISC_03	2	3.00	15	8	OUT3D	GAFE IF Input
DISC_03P	2	3.00	15	9	OUT3D	GAFE IF Input
IRTN_03	2	3.00	15	10	OUT3D	GAFE IF Return
IRTN_03P	2	3.00	15	11	OUT3D	GAFE IF Return
CHID_04	2	3.00	15	12	OUT3D	GAFE IF Input
CHID_04P	2	3.00	15	13	OUT3D	GAFE IF Input
DISC_04	2	3.00	15	14	OUT3D	GAFE IF Input
DISC_04P	2	3.00	15	15	OUT3D	GAFE IF Input
IRTN_04	2	3.00	16	0	OUT3E	GAFE IF Return
IRTN_04P	2	3.00	16	1	OUT3E	GAFE IF Return
CHID_05	2	3.00	16	2	OUT3E	GAFE IF Input
CHID_05P	2	3.00	16	3	OUT3E	GAFE IF Input
DISC_05	2	3.00	16	4	OUT3E	GAFE IF Input

DISC_05P	2	3.00	16	5	OUT3E	GAFE IF Input
IRTN_05	2	3.00	16	6	OUT3E	GAFE IF Return
IRTN_05P	2	3.00	16	7	OUT3E	GAFE IF Return
CHID_06	2	3.00	16	8	OUT3E	GAFE IF Input
CHID_06P	2	3.00	16	9	OUT3E	GAFE IF Input
DISC_06	2	3.00	16	10	OUT3E	GAFE IF Input
DISC_06P	2	3.00	16	11	OUT3E	GAFE IF Input
IRTN_06	2	3.00	16	12	OUT3E	GAFE IF Return
IRTN_06P	2	3.00	16	13	OUT3E	GAFE IF Return
CHID_07	2	3.00	16	14	OUT3E	GAFE IF Input
CHID_07P	2	3.00	16	15	OUT3E	GAFE IF Input
DISC_07	2	3.00	17	0	OUT3F	GAFE IF Input
DISC_07P	2	3.00	17	1	OUT3F	GAFE IF Input
IRTN_07	2	3.00	17	2	OUT3F	GAFE IF Return
IRTN_07P	2	3.00	17	3	OUT3F	GAFE IF Return
CHID_08	2	3.00	17	4	OUT3F	GAFE IF Input
CHID_08P	2	3.00	17	5	OUT3F	GAFE IF Input
DISC_08	2	3.00	17	6	OUT3F	GAFE IF Input
DISC_08P	2	3.00	17	7	OUT3F	GAFE IF Input
IRTN_08	2	3.00	17	8	OUT3F	GAFE IF Return
IRTN_08P	2	3.00	17	9	OUT3F	GAFE IF Return
CHID_09	2	3.00	17	10	OUT3F	GAFE IF Input
CHID_09P	2	3.00	17	11	OUT3F	GAFE IF Input
DISC_09	2	3.00	17	12	OUT3F	GAFE IF Input
DISC_09P	2	3.00	17	13	OUT3F	GAFE IF Input
IRTN_09	2	3.00	17	14	OUT3F	GAFE IF Return
IRTN_09P	2	3.00	17	15	OUT3F	GAFE IF Return
DGND001	2	3.00	18	0	OUT4A	Ground Pin
DGND027	2	3.00	18	1	OUT4A	Ground Pin
DGND053	2	3.00	18	2	OUT4A	Ground Pin
DGND080	2	3.00	18	3	OUT4A	Ground Pin
DGND136	2	3.00	18	4	OUT4A	Ground Pin
DGND157	2	3.00	18	5	OUT4A	Ground Pin
DGND187	2	3.00	18	6	OUT4A	Ground Pin
DVDD024	2	3.00	18	7	OUT4A	Power Pin
DVDD052	2	3.00	18	8	OUT4A	Power Pin
DVDD078	2	3.00	18	9	OUT4A	Power Pin
DVDD132	2	3.00	18	10	OUT4A	Power Pin
DVDD155	2	3.00	18	11	OUT4A	Power Pin
DVDD208	2	3.00	18	12	OUT4A	Power Pin
CMDCK+	2	3.00	18	13	OUT4A	GAFE Diff Output
CMDCK-	2	3.00	18	14	OUT4A	GAFE Diff Output
GND	2	3.00	18	15	OUT4A	VDD/2
VDDMID	2	3.00	19	0	OUT4B	Ground
CMDD+	2	3.00	19	1	OUT4B	GAFE Diff Output
CMDD-	2	3.00	19	2	OUT4B	GAFE Diff Output
STROB+	2	3.00	19	3	OUT4B	GAFE Diff Output
STROB-	2	3.00	19	4	OUT4B	GAFE Diff Output
HOLD+	2	3.00	19	5	OUT4B	GAFE Diff Output
HOLD-	2	3.00	19	6	OUT4B	GAFE Diff Output

HLD	2	3.00	19	7	OUT4B	GAFE HLD
IRTN_HLD	2	3.00	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	2	3.00	19	9	OUT4B	Bias Pin
BIAS_DRV_L	2	3.00	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	2	3.00	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	2	3.00	19	12	OUT4B	Bias Pin
BIAS_RCVR	2	3.00	19	13	OUT4B	Bias Pin
VDDMID	2	3.00	19	14	OUT4B	VDD/2
GND	2	3.00	19	15	OUT4B	Ground
ACD_NVETO_00_AP	3	3.00	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	3	3.00	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	3	3.00	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	3	3.00	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	3	3.00	0	4	OUT1A	LVDS Out
ACD_NVETO_01_AM	3	3.00	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	3	3.00	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	3	3.00	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	3	3.00	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	3	3.00	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	3	3.00	0	10	OUT1A	LVDS Out
ACD_NVETO_02_BM	3	3.00	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	3	3.00	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	3	3.00	0	13	OUT1A	LVDS Out
VDDMID	3	3.00	0	14	OUT1A	VDD/2
GND	3	3.00	0	15	OUT1A	Ground
ACD_NVETO_03_BP	3	3.00	1	0	OUT1B	LVDS Out
ACD_NVETO_03_BM	3	3.00	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	3	3.00	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	3	3.00	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	3	3.00	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	3	3.00	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	3	3.00	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	3	3.00	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	3	3.00	1	8	OUT1B	LVDS Out
ACD_NVETO_05_BM	3	3.00	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	3	3.00	1	10	OUT1B	LVDS Out
ACD_NVETO_06_AM	3	3.00	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	3	3.00	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	3	3.00	1	13	OUT1B	LVDS Out
VDDMID	3	3.00	1	14	OUT1B	VDD/2
GND	3	3.00	1	15	OUT1B	Ground
ACD_NVETO_07_AP	3	3.00	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	3	3.00	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	3	3.00	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	3	3.00	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	3	3.00	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	3	3.00	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	3	3.00	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	3	3.00	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	3	3.00	2	8	OUT1C	LVDS Out

ACD_NVETO_09_AM	3	3.00	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	3	3.00	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	3	3.00	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	3	3.00	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	3	3.00	2	13	OUT1C	LVDS Out
VDDMID	3	3.00	2	14	OUT1C	VDD/2
GND	3	3.00	2	15	OUT1C	Ground
ACD_NVETO_10_BP	3	3.00	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	3	3.00	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	3	3.00	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	3	3.00	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	3	3.00	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	3	3.00	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	3	3.00	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	3	3.00	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	3	3.00	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	3	3.00	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	3	3.00	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	3	3.00	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	3	3.00	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	3	3.00	3	13	OUT1D	LVDS Out
VDDMID	3	3.00	3	14	OUT1D	VDD/2
GND	3	3.00	3	15	OUT1D	Ground
ACD_NVETO_14_AP	3	3.00	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	3	3.00	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	3	3.00	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	3	3.00	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	3	3.00	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	3	3.00	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	3	3.00	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	3	3.00	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	3	3.00	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	3	3.00	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	3	3.00	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	3	3.00	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	3	3.00	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	3	3.00	4	13	OUT1E	LVDS Out
VDDMID	3	3.00	4	14	OUT1E	VDD/2
GND	3	3.00	4	15	OUT1E	Ground
ACD_NVETO_17_BP	3	3.00	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	3	3.00	5	1	OUT1F	LVDS Out
ACD_CNO_AP	3	3.00	5	2	OUT1F	LVDS Out
ACD_CNO_AM	3	3.00	5	3	OUT1F	LVDS Out
ACD_CNO_BP	3	3.00	5	4	OUT1F	LVDS Out
ACD_CNO_BM	3	3.00	5	5	OUT1F	LVDS Out
ACD_CLK_AP	3	3.00	5	6	OUT1F	LVDS In
ACD_CLK_AM	3	3.00	5	7	OUT1F	LVDS In
ACD_CLK_BP	3	3.00	5	8	OUT1F	LVDS In
ACD_CLK_BM	3	3.00	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	3	3.00	5	10	OUT1F	LVDS In

ACD_NSCMD_AM	3	3.00	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	3	3.00	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	3	3.00	5	13	OUT1F	LVDS In
ACD_NRST_AP	3	3.00	5	14	OUT1F	LVDS In
ACD_NRST_AM	3	3.00	5	15	OUT1F	LVDS In
ACD_NRST_BP	3	3.00	6	0	OUT2A	LVDS In
ACD_NRST_BM	3	3.00	6	1	OUT2A	LVDS In
ACD_NSADATA_AP	3	3.00	6	2	OUT2A	LVDS Out
ACD_NSADATA_AM	3	3.00	6	3	OUT2A	LVDS Out
ACD_NSADATA_BP	3	3.00	6	4	OUT2A	LVDS Out
ACD_NSADATA_BM	3	3.00	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	3	3.00	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	3	3.00	6	7	OUT2A	CMOS Input
GAFE_RST	3	3.00	6	8	OUT2A	CMOS Output
GAFE_RSTP	3	3.00	6	9	OUT2A	CMOS Output
DAC_DATA	3	3.00	6	10	OUT2A	CMOS Output
DAC_DATAP	3	3.00	6	11	OUT2A	CMOS Output
DAC_CLK	3	3.00	6	12	OUT2A	CMOS Output
DAC_CLKP	3	3.00	6	13	OUT2A	CMOS Output
VDDMID	3	3.00	6	14	OUT2A	VDD/2
GND	3	3.00	6	15	OUT2A	Ground
NDAC_CS	3	3.00	7	0	OUT2B	CMOS Output
NDAC_CSP	3	3.00	7	1	OUT2B	CMOS Output
NDAC_CLR	3	3.00	7	2	OUT2B	CMOS Output
NDAC_CLRP	3	3.00	7	3	OUT2B	CMOS Output
HV_ENABLE1	3	3.00	7	4	OUT2B	CMOS Output
HV_ENABLE1P	3	3.00	7	5	OUT2B	CMOS Output
HV_ENABLE2	3	3.00	7	6	OUT2B	CMOS Output
HV_ENABLE2P	3	3.00	7	7	OUT2B	CMOS Output
ADC_CLK	3	3.00	7	8	OUT2B	CMOS Output
ADC_CLKP	3	3.00	7	9	OUT2B	CMOS Output
NADC_CS	3	3.00	7	10	OUT2B	CMOS Output
NADC_CSP	3	3.00	7	11	OUT2B	CMOS Output
DAC_DATA	3	3.00	7	12	OUT2B	CMOS Output
DAC_DATAP	3	3.00	7	13	OUT2B	CMOS Output
VDDMID	3	3.00	7	14	OUT2B	VDD/2
GND	3	3.00	7	15	OUT2B	Ground
DAC_DATA_RET	3	3.00	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	3	3.00	8	1	OUT2C	CMOS Input
HITMAP_TEST	3	3.00	8	2	OUT2C	CMOS Output
HITMAP_TESTP	3	3.00	8	3	OUT2C	CMOS Output
TRIG_TEST	3	3.00	8	4	OUT2C	CMOS Output
TRIG_TESTP	3	3.00	8	5	OUT2C	CMOS Output
FREE_ID	3	3.00	8	6	OUT2C	CMOS Output
FREE_IDP	3	3.00	8	7	OUT2C	CMOS Output
PWR_ON_RST	3	3.00	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	3	3.00	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	3	3.00	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	3	3.00	8	11	OUT2C	CMOS Output
IRTN_17P	3	3.00	8	12	OUT2C	GAFE IF Return

IRTN_17	3	3.00	8	13	OUT2C	GAFE IF Return
VDDMID	3	3.00	8	14	OUT2C	VDD/2
GND	3	3.00	8	15	OUT2C	Ground
DISC_17P	3	3.00	9	0	OUT2D	GAFE IF Input
DISC_17	3	3.00	9	1	OUT2D	GAFE IF Input
CHID_17P	3	3.00	9	2	OUT2D	GAFE IF Input
CHID_17	3	3.00	9	3	OUT2D	GAFE IF Input
IRTN_16P	3	3.00	9	4	OUT2D	GAFE IF Return
IRTN_16	3	3.00	9	5	OUT2D	GAFE IF Return
DISC_16P	3	3.00	9	6	OUT2D	GAFE IF Input
DISC_16	3	3.00	9	7	OUT2D	GAFE IF Input
CHID_16P	3	3.00	9	8	OUT2D	GAFE IF Input
CHID_16	3	3.00	9	9	OUT2D	GAFE IF Input
IRTN_15P	3	3.00	9	10	OUT2D	GAFE IF Return
IRTN_15	3	3.00	9	11	OUT2D	GAFE IF Return
DISC_15P	3	3.00	9	12	OUT2D	GAFE IF Input
DISC_15	3	3.00	9	13	OUT2D	GAFE IF Input
VDDMID	3	3.00	9	14	OUT2D	VDD/2
GND	3	3.00	9	15	OUT2D	Ground
CHID_15P	3	3.00	10	0	OUT2E	GAFE IF Input
CHID_15	3	3.00	10	1	OUT2E	GAFE IF Input
IRTN_14P	3	3.00	10	2	OUT2E	GAFE IF Return
IRTN_14	3	3.00	10	3	OUT2E	GAFE IF Return
DISC_14P	3	3.00	10	4	OUT2E	GAFE IF Input
DISC_14	3	3.00	10	5	OUT2E	GAFE IF Input
CHID_14P	3	3.00	10	6	OUT2E	GAFE IF Input
CHID_14	3	3.00	10	7	OUT2E	GAFE IF Input
IRTN_13P	3	3.00	10	8	OUT2E	GAFE IF Return
IRTN_13	3	3.00	10	9	OUT2E	GAFE IF Return
DISC_13P	3	3.00	10	10	OUT2E	GAFE IF Input
DISC_13	3	3.00	10	11	OUT2E	GAFE IF Input
CHID_13P	3	3.00	10	12	OUT2E	GAFE IF Input
CHID_13	3	3.00	10	13	OUT2E	GAFE IF Input
IRTN_12P	3	3.00	10	14	OUT2E	GAFE IF Return
IRTN_12	3	3.00	10	15	OUT2E	GAFE IF Return
DISC_12P	3	3.00	11	0	OUT2F	GAFE IF Input
DISC_12	3	3.00	11	1	OUT2F	GAFE IF Input
CHID_12P	3	3.00	11	2	OUT2F	GAFE IF Input
CHID_12	3	3.00	11	3	OUT2F	GAFE IF Input
IRTN_11P	3	3.00	11	4	OUT2F	GAFE IF Return
IRTN_11	3	3.00	11	5	OUT2F	GAFE IF Return
DISC_11P	3	3.00	11	6	OUT2F	GAFE IF Input
DISC_11	3	3.00	11	7	OUT2F	GAFE IF Input
CHID_11P	3	3.00	11	8	OUT2F	GAFE IF Input
CHID_11	3	3.00	11	9	OUT2F	GAFE IF Input
IRTN_10P	3	3.00	11	10	OUT2F	GAFE IF Return
IRTN_10	3	3.00	11	11	OUT2F	GAFE IF Return
DISC_10P	3	3.00	11	12	OUT2F	GAFE IF Input
DISC_10	3	3.00	11	13	OUT2F	GAFE IF Input
CHID_10P	3	3.00	11	14	OUT2F	GAFE IF Input

CHID_10	3	3.00	11	15	OUT2F	GAFE IF Input
PHAD_00	3	3.00	12	0	OUT3A	CMOS Input
PHAD_00P	3	3.00	12	1	OUT3A	CMOS Input
PHAD_01	3	3.00	12	2	OUT3A	CMOS Input
PHAD_01P	3	3.00	12	3	OUT3A	CMOS Input
PHAD_02	3	3.00	12	4	OUT3A	CMOS Input
PHAD_02P	3	3.00	12	5	OUT3A	CMOS Input
PHAD_03	3	3.00	12	6	OUT3A	CMOS Input
PHAD_03P	3	3.00	12	7	OUT3A	CMOS Input
PHAD_04	3	3.00	12	8	OUT3A	CMOS Input
PHAD_04P	3	3.00	12	9	OUT3A	CMOS Input
PHAD_05	3	3.00	12	10	OUT3A	CMOS Input
PHAD_05P	3	3.00	12	11	OUT3A	CMOS Input
PHAD_06	3	3.00	12	12	OUT3A	CMOS Input
PHAD_06P	3	3.00	12	13	OUT3A	CMOS Input
PHAD_07	3	3.00	12	14	OUT3A	CMOS Input
PHAD_07P	3	3.00	12	15	OUT3A	CMOS Input
PHAD_08	3	3.00	13	0	OUT3B	CMOS Input
PHAD_08P	3	3.00	13	1	OUT3B	CMOS Input
PHAD_09	3	3.00	13	2	OUT3B	CMOS Input
PHAD_09P	3	3.00	13	3	OUT3B	CMOS Input
PHAD_10	3	3.00	13	4	OUT3B	CMOS Input
PHAD_10P	3	3.00	13	5	OUT3B	CMOS Input
PHAD_11	3	3.00	13	6	OUT3B	CMOS Input
PHAD_11P	3	3.00	13	7	OUT3B	CMOS Input
PHAD_12	3	3.00	13	8	OUT3B	CMOS Input
PHAD_12P	3	3.00	13	9	OUT3B	CMOS Input
PHAD_13	3	3.00	13	10	OUT3B	CMOS Input
PHAD_13P	3	3.00	13	11	OUT3B	CMOS Input
PHAD_14	3	3.00	13	12	OUT3B	CMOS Input
PHAD_14P	3	3.00	13	13	OUT3B	CMOS Input
PHAD_15	3	3.00	13	14	OUT3B	CMOS Input
PHAD_15P	3	3.00	13	15	OUT3B	CMOS Input
PHAD_16	3	3.00	14	0	OUT3C	CMOS Input
PHAD_16P	3	3.00	14	1	OUT3C	CMOS Input
PHAD_17	3	3.00	14	2	OUT3C	CMOS Input
PHAD_17P	3	3.00	14	3	OUT3C	CMOS Input
CHID_00	3	3.00	14	4	OUT3C	GAFE IF Input
CHID_00P	3	3.00	14	5	OUT3C	GAFE IF Input
DISC_00	3	3.00	14	6	OUT3C	GAFE IF Input
DISC_00P	3	3.00	14	7	OUT3C	GAFE IF Input
IRTN_00	3	3.00	14	8	OUT3C	GAFE IF Return
IRTN_00P	3	3.00	14	9	OUT3C	GAFE IF Return
CHID_01	3	3.00	14	10	OUT3C	GAFE IF Input
CHID_01P	3	3.00	14	11	OUT3C	GAFE IF Input
DISC_01	3	3.00	14	12	OUT3C	GAFE IF Input
DISC_01P	3	3.00	14	13	OUT3C	GAFE IF Input
IRTN_01	3	3.00	14	14	OUT3C	GAFE IF Return
IRTN_01P	3	3.00	14	15	OUT3C	GAFE IF Return
CHID_02	3	3.00	15	0	OUT3D	GAFE IF Input

CHID_02P	3	3.00	15	1	OUT3D	GAFE IF Input
DISC_02	3	3.00	15	2	OUT3D	GAFE IF Input
DISC_02P	3	3.00	15	3	OUT3D	GAFE IF Input
IRTN_02	3	3.00	15	4	OUT3D	GAFE IF Return
IRTN_02P	3	3.00	15	5	OUT3D	GAFE IF Return
CHID_03	3	3.00	15	6	OUT3D	GAFE IF Input
CHID_03P	3	3.00	15	7	OUT3D	GAFE IF Input
DISC_03	3	3.00	15	8	OUT3D	GAFE IF Input
DISC_03P	3	3.00	15	9	OUT3D	GAFE IF Input
IRTN_03	3	3.00	15	10	OUT3D	GAFE IF Return
IRTN_03P	3	3.00	15	11	OUT3D	GAFE IF Return
CHID_04	3	3.00	15	12	OUT3D	GAFE IF Input
CHID_04P	3	3.00	15	13	OUT3D	GAFE IF Input
DISC_04	3	3.00	15	14	OUT3D	GAFE IF Input
DISC_04P	3	3.00	15	15	OUT3D	GAFE IF Input
IRTN_04	3	3.00	16	0	OUT3E	GAFE IF Return
IRTN_04P	3	3.00	16	1	OUT3E	GAFE IF Return
CHID_05	3	3.00	16	2	OUT3E	GAFE IF Input
CHID_05P	3	3.00	16	3	OUT3E	GAFE IF Input
DISC_05	3	3.00	16	4	OUT3E	GAFE IF Input
DISC_05P	3	3.00	16	5	OUT3E	GAFE IF Input
IRTN_05	3	3.00	16	6	OUT3E	GAFE IF Return
IRTN_05P	3	3.00	16	7	OUT3E	GAFE IF Return
CHID_06	3	3.00	16	8	OUT3E	GAFE IF Input
CHID_06P	3	3.00	16	9	OUT3E	GAFE IF Input
DISC_06	3	3.00	16	10	OUT3E	GAFE IF Input
DISC_06P	3	3.00	16	11	OUT3E	GAFE IF Input
IRTN_06	3	3.00	16	12	OUT3E	GAFE IF Return
IRTN_06P	3	3.00	16	13	OUT3E	GAFE IF Return
CHID_07	3	3.00	16	14	OUT3E	GAFE IF Input
CHID_07P	3	3.00	16	15	OUT3E	GAFE IF Input
DISC_07	3	3.00	17	0	OUT3F	GAFE IF Input
DISC_07P	3	3.00	17	1	OUT3F	GAFE IF Input
IRTN_07	3	3.00	17	2	OUT3F	GAFE IF Return
IRTN_07P	3	3.00	17	3	OUT3F	GAFE IF Return
CHID_08	3	3.00	17	4	OUT3F	GAFE IF Input
CHID_08P	3	3.00	17	5	OUT3F	GAFE IF Input
DISC_08	3	3.00	17	6	OUT3F	GAFE IF Input
DISC_08P	3	3.00	17	7	OUT3F	GAFE IF Input
IRTN_08	3	3.00	17	8	OUT3F	GAFE IF Return
IRTN_08P	3	3.00	17	9	OUT3F	GAFE IF Return
CHID_09	3	3.00	17	10	OUT3F	GAFE IF Input
CHID_09P	3	3.00	17	11	OUT3F	GAFE IF Input
DISC_09	3	3.00	17	12	OUT3F	GAFE IF Input
DISC_09P	3	3.00	17	13	OUT3F	GAFE IF Input
IRTN_09	3	3.00	17	14	OUT3F	GAFE IF Return
IRTN_09P	3	3.00	17	15	OUT3F	GAFE IF Return
DGND001	3	3.00	18	0	OUT4A	Ground Pin
DGND027	3	3.00	18	1	OUT4A	Ground Pin
DGND053	3	3.00	18	2	OUT4A	Ground Pin

DGND080	3	3.00	18	3	OUT4A	Ground Pin
DGND136	3	3.00	18	4	OUT4A	Ground Pin
DGND157	3	3.00	18	5	OUT4A	Ground Pin
DGND187	3	3.00	18	6	OUT4A	Ground Pin
DVDD024	3	3.00	18	7	OUT4A	Power Pin
DVDD052	3	3.00	18	8	OUT4A	Power Pin
DVDD078	3	3.00	18	9	OUT4A	Power Pin
DVDD132	3	3.00	18	10	OUT4A	Power Pin
DVDD155	3	3.00	18	11	OUT4A	Power Pin
DVDD208	3	3.00	18	12	OUT4A	Power Pin
CMDCK+	3	3.00	18	13	OUT4A	GAFE Diff Output
CMDCK-	3	3.00	18	14	OUT4A	GAFE Diff Output
GND	3	3.00	18	15	OUT4A	VDD/2
VDDMID	3	3.00	19	0	OUT4B	Ground
CMDD+	3	3.00	19	1	OUT4B	GAFE Diff Output
CMDD-	3	3.00	19	2	OUT4B	GAFE Diff Output
STROB+	3	3.00	19	3	OUT4B	GAFE Diff Output
STROB-	3	3.00	19	4	OUT4B	GAFE Diff Output
HOLD+	3	3.00	19	5	OUT4B	GAFE Diff Output
HOLD-	3	3.00	19	6	OUT4B	GAFE Diff Output
HLD	3	3.00	19	7	OUT4B	GAFE HLD
IRTN_HLD	3	3.00	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	3	3.00	19	9	OUT4B	Bias Pin
BIAS_DRV_L	3	3.00	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	3	3.00	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	3	3.00	19	12	OUT4B	Bias Pin
BIAS_RCVR	3	3.00	19	13	OUT4B	Bias Pin
VDDMID	3	3.00	19	14	OUT4B	VDD/2
GND	3	3.00	19	15	OUT4B	Ground

Parameter	Mode	Power Supply	Mux	Channel	Parametric Board Signal Port	
					OUT1A	OUT1B
ACD_NVETO_00_AP	1	3.60	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	1	3.60	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	1	3.60	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	1	3.60	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	1	3.60	0	4	OUT1A	LVDS Out
ACD_NVETO_01_AM	1	3.60	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	1	3.60	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	1	3.60	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	1	3.60	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	1	3.60	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	1	3.60	0	10	OUT1A	LVDS Out
ACD_NVETO_02_BM	1	3.60	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	1	3.60	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	1	3.60	0	13	OUT1A	LVDS Out
VDDMID	1	3.60	0	14	OUT1A	VDD/2
GND	1	3.60	0	15	OUT1A	Ground
ACD_NVETO_03_BP	1	3.60	1	0	OUT1B	LVDS Out

ACD_NVETO_03_BM	1	3.60	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	1	3.60	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	1	3.60	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	1	3.60	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	1	3.60	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	1	3.60	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	1	3.60	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	1	3.60	1	8	OUT1B	LVDS Out
ACD_NVETO_05_BM	1	3.60	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	1	3.60	1	10	OUT1B	LVDS Out
ACD_NVETO_06_AM	1	3.60	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	1	3.60	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	1	3.60	1	13	OUT1B	LVDS Out
VDDMID	1	3.60	1	14	OUT1B	VDD/2
GND	1	3.60	1	15	OUT1B	Ground
ACD_NVETO_07_AP	1	3.60	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	1	3.60	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	1	3.60	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	1	3.60	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	1	3.60	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	1	3.60	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	1	3.60	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	1	3.60	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	1	3.60	2	8	OUT1C	LVDS Out
ACD_NVETO_09_AM	1	3.60	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	1	3.60	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	1	3.60	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	1	3.60	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	1	3.60	2	13	OUT1C	LVDS Out
VDDMID	1	3.60	2	14	OUT1C	VDD/2
GND	1	3.60	2	15	OUT1C	Ground
ACD_NVETO_10_BP	1	3.60	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	1	3.60	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	1	3.60	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	1	3.60	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	1	3.60	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	1	3.60	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	1	3.60	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	1	3.60	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	1	3.60	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	1	3.60	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	1	3.60	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	1	3.60	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	1	3.60	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	1	3.60	3	13	OUT1D	LVDS Out
VDDMID	1	3.60	3	14	OUT1D	VDD/2
GND	1	3.60	3	15	OUT1D	Ground
ACD_NVETO_14_AP	1	3.60	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	1	3.60	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	1	3.60	4	2	OUT1E	LVDS Out

ACD_NVETO_14_BM	1	3.60	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	1	3.60	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	1	3.60	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	1	3.60	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	1	3.60	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	1	3.60	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	1	3.60	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	1	3.60	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	1	3.60	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	1	3.60	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	1	3.60	4	13	OUT1E	LVDS Out
VDDMDID	1	3.60	4	14	OUT1E	VDD/2
GND	1	3.60	4	15	OUT1E	Ground
ACD_NVETO_17_BP	1	3.60	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	1	3.60	5	1	OUT1F	LVDS Out
ACD_CNO_AP	1	3.60	5	2	OUT1F	LVDS Out
ACD_CNO_AM	1	3.60	5	3	OUT1F	LVDS Out
ACD_CNO_BP	1	3.60	5	4	OUT1F	LVDS Out
ACD_CNO_BM	1	3.60	5	5	OUT1F	LVDS Out
ACD_CLK_AP	1	3.60	5	6	OUT1F	LVDS In
ACD_CLK_AM	1	3.60	5	7	OUT1F	LVDS In
ACD_CLK_BP	1	3.60	5	8	OUT1F	LVDS In
ACD_CLK_BM	1	3.60	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	1	3.60	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	1	3.60	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	1	3.60	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	1	3.60	5	13	OUT1F	LVDS In
ACD_NRST_AP	1	3.60	5	14	OUT1F	LVDS In
ACD_NRST_AM	1	3.60	5	15	OUT1F	LVDS In
ACD_NRST_BP	1	3.60	6	0	OUT2A	LVDS In
ACD_NRST_BM	1	3.60	6	1	OUT2A	LVDS In
ACD_NS DATA_AP	1	3.60	6	2	OUT2A	LVDS Out
ACD_NS DATA_AM	1	3.60	6	3	OUT2A	LVDS Out
ACD_NS DATA_BP	1	3.60	6	4	OUT2A	LVDS Out
ACD_NS DATA_BM	1	3.60	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	1	3.60	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	1	3.60	6	7	OUT2A	CMOS Input
GAFE_RST	1	3.60	6	8	OUT2A	CMOS Output
GAFE_RSTP	1	3.60	6	9	OUT2A	CMOS Output
DAC_DATA	1	3.60	6	10	OUT2A	CMOS Output
DAC_DATAP	1	3.60	6	11	OUT2A	CMOS Output
DAC_CLK	1	3.60	6	12	OUT2A	CMOS Output
DAC_CLKP	1	3.60	6	13	OUT2A	CMOS Output
VDDMDID	1	3.60	6	14	OUT2A	VDD/2
GND	1	3.60	6	15	OUT2A	Ground
NDAC_CS	1	3.60	7	0	OUT2B	CMOS Output
NDAC_CSP	1	3.60	7	1	OUT2B	CMOS Output
NDAC_CLR	1	3.60	7	2	OUT2B	CMOS Output
NDAC_CLRP	1	3.60	7	3	OUT2B	CMOS Output
HV_ENABLE1	1	3.60	7	4	OUT2B	CMOS Output

HV_ENABLE1P	1	3.60	7	5	OUT2B	CMOS Output
HV_ENABLE2	1	3.60	7	6	OUT2B	CMOS Output
HV_ENABLE2P	1	3.60	7	7	OUT2B	CMOS Output
ADC_CLK	1	3.60	7	8	OUT2B	CMOS Output
ADC_CLKP	1	3.60	7	9	OUT2B	CMOS Output
NADC_CS	1	3.60	7	10	OUT2B	CMOS Output
NADC_CSP	1	3.60	7	11	OUT2B	CMOS Output
DAC_DATA	1	3.60	7	12	OUT2B	CMOS Output
DAC_DATAP	1	3.60	7	13	OUT2B	CMOS Output
VDDMID	1	3.60	7	14	OUT2B	VDD/2
GND	1	3.60	7	15	OUT2B	Ground
DAC_DATA_RET	1	3.60	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	1	3.60	8	1	OUT2C	CMOS Input
HITMAP_TEST	1	3.60	8	2	OUT2C	CMOS Output
HITMAP_TESTP	1	3.60	8	3	OUT2C	CMOS Output
TRIG_TEST	1	3.60	8	4	OUT2C	CMOS Output
TRIG_TESTP	1	3.60	8	5	OUT2C	CMOS Output
FREE_ID	1	3.60	8	6	OUT2C	CMOS Output
FREE_IDP	1	3.60	8	7	OUT2C	CMOS Output
PWR_ON_RST	1	3.60	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	1	3.60	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	1	3.60	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	1	3.60	8	11	OUT2C	CMOS Output
IRTN_17P	1	3.60	8	12	OUT2C	GAFE IF Return
IRTN_17	1	3.60	8	13	OUT2C	GAFE IF Return
VDDMID	1	3.60	8	14	OUT2C	VDD/2
GND	1	3.60	8	15	OUT2C	Ground
DISC_17P	1	3.60	9	0	OUT2D	GAFE IF Input
DISC_17	1	3.60	9	1	OUT2D	GAFE IF Input
CHID_17P	1	3.60	9	2	OUT2D	GAFE IF Input
CHID_17	1	3.60	9	3	OUT2D	GAFE IF Input
IRTN_16P	1	3.60	9	4	OUT2D	GAFE IF Return
IRTN_16	1	3.60	9	5	OUT2D	GAFE IF Return
DISC_16P	1	3.60	9	6	OUT2D	GAFE IF Input
DISC_16	1	3.60	9	7	OUT2D	GAFE IF Input
CHID_16P	1	3.60	9	8	OUT2D	GAFE IF Input
CHID_16	1	3.60	9	9	OUT2D	GAFE IF Input
IRTN_15P	1	3.60	9	10	OUT2D	GAFE IF Return
IRTN_15	1	3.60	9	11	OUT2D	GAFE IF Return
DISC_15P	1	3.60	9	12	OUT2D	GAFE IF Input
DISC_15	1	3.60	9	13	OUT2D	GAFE IF Input
VDDMID	1	3.60	9	14	OUT2D	VDD/2
GND	1	3.60	9	15	OUT2D	Ground
CHID_15P	1	3.60	10	0	OUT2E	GAFE IF Input
CHID_15	1	3.60	10	1	OUT2E	GAFE IF Input
IRTN_14P	1	3.60	10	2	OUT2E	GAFE IF Return
IRTN_14	1	3.60	10	3	OUT2E	GAFE IF Return
DISC_14P	1	3.60	10	4	OUT2E	GAFE IF Input
DISC_14	1	3.60	10	5	OUT2E	GAFE IF Input
CHID_14P	1	3.60	10	6	OUT2E	GAFE IF Input

CHID_14	1	3.60	10	7	OUT2E	GAFE IF Input
IRTN_13P	1	3.60	10	8	OUT2E	GAFE IF Return
IRTN_13	1	3.60	10	9	OUT2E	GAFE IF Return
DISC_13P	1	3.60	10	10	OUT2E	GAFE IF Input
DISC_13	1	3.60	10	11	OUT2E	GAFE IF Input
CHID_13P	1	3.60	10	12	OUT2E	GAFE IF Input
CHID_13	1	3.60	10	13	OUT2E	GAFE IF Input
IRTN_12P	1	3.60	10	14	OUT2E	GAFE IF Return
IRTN_12	1	3.60	10	15	OUT2E	GAFE IF Return
DISC_12P	1	3.60	11	0	OUT2F	GAFE IF Input
DISC_12	1	3.60	11	1	OUT2F	GAFE IF Input
CHID_12P	1	3.60	11	2	OUT2F	GAFE IF Input
CHID_12	1	3.60	11	3	OUT2F	GAFE IF Input
IRTN_11P	1	3.60	11	4	OUT2F	GAFE IF Return
IRTN_11	1	3.60	11	5	OUT2F	GAFE IF Return
DISC_11P	1	3.60	11	6	OUT2F	GAFE IF Input
DISC_11	1	3.60	11	7	OUT2F	GAFE IF Input
CHID_11P	1	3.60	11	8	OUT2F	GAFE IF Input
CHID_11	1	3.60	11	9	OUT2F	GAFE IF Input
IRTN_10P	1	3.60	11	10	OUT2F	GAFE IF Return
IRTN_10	1	3.60	11	11	OUT2F	GAFE IF Return
DISC_10P	1	3.60	11	12	OUT2F	GAFE IF Input
DISC_10	1	3.60	11	13	OUT2F	GAFE IF Input
CHID_10P	1	3.60	11	14	OUT2F	GAFE IF Input
CHID_10	1	3.60	11	15	OUT2F	GAFE IF Input
PHAD_00	1	3.60	12	0	OUT3A	CMOS Input
PHAD_00P	1	3.60	12	1	OUT3A	CMOS Input
PHAD_01	1	3.60	12	2	OUT3A	CMOS Input
PHAD_01P	1	3.60	12	3	OUT3A	CMOS Input
PHAD_02	1	3.60	12	4	OUT3A	CMOS Input
PHAD_02P	1	3.60	12	5	OUT3A	CMOS Input
PHAD_03	1	3.60	12	6	OUT3A	CMOS Input
PHAD_03P	1	3.60	12	7	OUT3A	CMOS Input
PHAD_04	1	3.60	12	8	OUT3A	CMOS Input
PHAD_04P	1	3.60	12	9	OUT3A	CMOS Input
PHAD_05	1	3.60	12	10	OUT3A	CMOS Input
PHAD_05P	1	3.60	12	11	OUT3A	CMOS Input
PHAD_06	1	3.60	12	12	OUT3A	CMOS Input
PHAD_06P	1	3.60	12	13	OUT3A	CMOS Input
PHAD_07	1	3.60	12	14	OUT3A	CMOS Input
PHAD_07P	1	3.60	12	15	OUT3A	CMOS Input
PHAD_08	1	3.60	13	0	OUT3B	CMOS Input
PHAD_08P	1	3.60	13	1	OUT3B	CMOS Input
PHAD_09	1	3.60	13	2	OUT3B	CMOS Input
PHAD_09P	1	3.60	13	3	OUT3B	CMOS Input
PHAD_10	1	3.60	13	4	OUT3B	CMOS Input
PHAD_10P	1	3.60	13	5	OUT3B	CMOS Input
PHAD_11	1	3.60	13	6	OUT3B	CMOS Input
PHAD_11P	1	3.60	13	7	OUT3B	CMOS Input
PHAD_12	1	3.60	13	8	OUT3B	CMOS Input

PHAD_12P	1	3.60	13	9	OUT3B	CMOS Input
PHAD_13	1	3.60	13	10	OUT3B	CMOS Input
PHAD_13P	1	3.60	13	11	OUT3B	CMOS Input
PHAD_14	1	3.60	13	12	OUT3B	CMOS Input
PHAD_14P	1	3.60	13	13	OUT3B	CMOS Input
PHAD_15	1	3.60	13	14	OUT3B	CMOS Input
PHAD_15P	1	3.60	13	15	OUT3B	CMOS Input
PHAD_16	1	3.60	14	0	OUT3C	CMOS Input
PHAD_16P	1	3.60	14	1	OUT3C	CMOS Input
PHAD_17	1	3.60	14	2	OUT3C	CMOS Input
PHAD_17P	1	3.60	14	3	OUT3C	CMOS Input
CHID_00	1	3.60	14	4	OUT3C	GAFE IF Input
CHID_00P	1	3.60	14	5	OUT3C	GAFE IF Input
DISC_00	1	3.60	14	6	OUT3C	GAFE IF Input
DISC_00P	1	3.60	14	7	OUT3C	GAFE IF Input
IRTN_00	1	3.60	14	8	OUT3C	GAFE IF Return
IRTN_00P	1	3.60	14	9	OUT3C	GAFE IF Return
CHID_01	1	3.60	14	10	OUT3C	GAFE IF Input
CHID_01P	1	3.60	14	11	OUT3C	GAFE IF Input
DISC_01	1	3.60	14	12	OUT3C	GAFE IF Input
DISC_01P	1	3.60	14	13	OUT3C	GAFE IF Input
IRTN_01	1	3.60	14	14	OUT3C	GAFE IF Return
IRTN_01P	1	3.60	14	15	OUT3C	GAFE IF Return
CHID_02	1	3.60	15	0	OUT3D	GAFE IF Input
CHID_02P	1	3.60	15	1	OUT3D	GAFE IF Input
DISC_02	1	3.60	15	2	OUT3D	GAFE IF Input
DISC_02P	1	3.60	15	3	OUT3D	GAFE IF Input
IRTN_02	1	3.60	15	4	OUT3D	GAFE IF Return
IRTN_02P	1	3.60	15	5	OUT3D	GAFE IF Return
CHID_03	1	3.60	15	6	OUT3D	GAFE IF Input
CHID_03P	1	3.60	15	7	OUT3D	GAFE IF Input
DISC_03	1	3.60	15	8	OUT3D	GAFE IF Input
DISC_03P	1	3.60	15	9	OUT3D	GAFE IF Input
IRTN_03	1	3.60	15	10	OUT3D	GAFE IF Return
IRTN_03P	1	3.60	15	11	OUT3D	GAFE IF Return
CHID_04	1	3.60	15	12	OUT3D	GAFE IF Input
CHID_04P	1	3.60	15	13	OUT3D	GAFE IF Input
DISC_04	1	3.60	15	14	OUT3D	GAFE IF Input
DISC_04P	1	3.60	15	15	OUT3D	GAFE IF Input
IRTN_04	1	3.60	16	0	OUT3E	GAFE IF Return
IRTN_04P	1	3.60	16	1	OUT3E	GAFE IF Return
CHID_05	1	3.60	16	2	OUT3E	GAFE IF Input
CHID_05P	1	3.60	16	3	OUT3E	GAFE IF Input
DISC_05	1	3.60	16	4	OUT3E	GAFE IF Input
DISC_05P	1	3.60	16	5	OUT3E	GAFE IF Input
IRTN_05	1	3.60	16	6	OUT3E	GAFE IF Return
IRTN_05P	1	3.60	16	7	OUT3E	GAFE IF Return
CHID_06	1	3.60	16	8	OUT3E	GAFE IF Input
CHID_06P	1	3.60	16	9	OUT3E	GAFE IF Input
DISC_06	1	3.60	16	10	OUT3E	GAFE IF Input

DISC_06P	1	3.60	16	11	OUT3E	GAFE IF Input
IRTN_06	1	3.60	16	12	OUT3E	GAFE IF Return
IRTN_06P	1	3.60	16	13	OUT3E	GAFE IF Return
CHID_07	1	3.60	16	14	OUT3E	GAFE IF Input
CHID_07P	1	3.60	16	15	OUT3E	GAFE IF Input
DISC_07	1	3.60	17	0	OUT3F	GAFE IF Input
DISC_07P	1	3.60	17	1	OUT3F	GAFE IF Input
IRTN_07	1	3.60	17	2	OUT3F	GAFE IF Return
IRTN_07P	1	3.60	17	3	OUT3F	GAFE IF Return
CHID_08	1	3.60	17	4	OUT3F	GAFE IF Input
CHID_08P	1	3.60	17	5	OUT3F	GAFE IF Input
DISC_08	1	3.60	17	6	OUT3F	GAFE IF Input
DISC_08P	1	3.60	17	7	OUT3F	GAFE IF Input
IRTN_08	1	3.60	17	8	OUT3F	GAFE IF Return
IRTN_08P	1	3.60	17	9	OUT3F	GAFE IF Return
CHID_09	1	3.60	17	10	OUT3F	GAFE IF Input
CHID_09P	1	3.60	17	11	OUT3F	GAFE IF Input
DISC_09	1	3.60	17	12	OUT3F	GAFE IF Input
DISC_09P	1	3.60	17	13	OUT3F	GAFE IF Input
IRTN_09	1	3.60	17	14	OUT3F	GAFE IF Return
IRTN_09P	1	3.60	17	15	OUT3F	GAFE IF Return
DGND001	1	3.60	18	0	OUT4A	Ground Pin
DGND027	1	3.60	18	1	OUT4A	Ground Pin
DGND053	1	3.60	18	2	OUT4A	Ground Pin
DGND080	1	3.60	18	3	OUT4A	Ground Pin
DGND136	1	3.60	18	4	OUT4A	Ground Pin
DGND157	1	3.60	18	5	OUT4A	Ground Pin
DGND187	1	3.60	18	6	OUT4A	Ground Pin
DVDD024	1	3.60	18	7	OUT4A	Power Pin
DVDD052	1	3.60	18	8	OUT4A	Power Pin
DVDD078	1	3.60	18	9	OUT4A	Power Pin
DVDD132	1	3.60	18	10	OUT4A	Power Pin
DVDD155	1	3.60	18	11	OUT4A	Power Pin
DVDD208	1	3.60	18	12	OUT4A	Power Pin
CMDCK+	1	3.60	18	13	OUT4A	GAFE Diff Output
CMDCK-	1	3.60	18	14	OUT4A	GAFE Diff Output
GND	1	3.60	18	15	OUT4A	VDD/2
VDDMID	1	3.60	19	0	OUT4B	Ground
CMDD+	1	3.60	19	1	OUT4B	GAFE Diff Output
CMDD-	1	3.60	19	2	OUT4B	GAFE Diff Output
STROB+	1	3.60	19	3	OUT4B	GAFE Diff Output
STROB-	1	3.60	19	4	OUT4B	GAFE Diff Output
HOLD+	1	3.60	19	5	OUT4B	GAFE Diff Output
HOLD-	1	3.60	19	6	OUT4B	GAFE Diff Output
HLD	1	3.60	19	7	OUT4B	GAFE HLD
IRTN_HLD	1	3.60	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	1	3.60	19	9	OUT4B	Bias Pin
BIAS_DRV_L	1	3.60	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	1	3.60	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	1	3.60	19	12	OUT4B	Bias Pin

BIAS_RCVR	1	3.60	19	13	OUT4B	Bias Pin
VDDMID	1	3.60	19	14	OUT4B	VDD/2
GND	1	3.60	19	15	OUT4B	Ground
ACD_NVETO_00_AP	2	3.60	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	2	3.60	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	2	3.60	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	2	3.60	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	2	3.60	0	4	OUT1A	LVDS Out
ACD_NVETO_01_AM	2	3.60	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	2	3.60	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	2	3.60	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	2	3.60	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	2	3.60	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	2	3.60	0	10	OUT1A	LVDS Out
ACD_NVETO_02_BM	2	3.60	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	2	3.60	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	2	3.60	0	13	OUT1A	LVDS Out
VDDMID	2	3.60	0	14	OUT1A	VDD/2
GND	2	3.60	0	15	OUT1A	Ground
ACD_NVETO_03_BP	2	3.60	1	0	OUT1B	LVDS Out
ACD_NVETO_03_BM	2	3.60	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	2	3.60	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	2	3.60	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	2	3.60	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	2	3.60	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	2	3.60	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	2	3.60	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	2	3.60	1	8	OUT1B	LVDS Out
ACD_NVETO_05_BM	2	3.60	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	2	3.60	1	10	OUT1B	LVDS Out
ACD_NVETO_06_AM	2	3.60	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	2	3.60	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	2	3.60	1	13	OUT1B	LVDS Out
VDDMID	2	3.60	1	14	OUT1B	VDD/2
GND	2	3.60	1	15	OUT1B	Ground
ACD_NVETO_07_AP	2	3.60	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	2	3.60	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	2	3.60	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	2	3.60	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	2	3.60	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	2	3.60	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	2	3.60	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	2	3.60	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	2	3.60	2	8	OUT1C	LVDS Out
ACD_NVETO_09_AM	2	3.60	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	2	3.60	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	2	3.60	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	2	3.60	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	2	3.60	2	13	OUT1C	LVDS Out
VDDMID	2	3.60	2	14	OUT1C	VDD/2

GND	2	3.60	2	15	OUT1C	Ground
ACD_NVETO_10_BP	2	3.60	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	2	3.60	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	2	3.60	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	2	3.60	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	2	3.60	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	2	3.60	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	2	3.60	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	2	3.60	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	2	3.60	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	2	3.60	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	2	3.60	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	2	3.60	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	2	3.60	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	2	3.60	3	13	OUT1D	LVDS Out
VDDMID	2	3.60	3	14	OUT1D	VDD/2
GND	2	3.60	3	15	OUT1D	Ground
ACD_NVETO_14_AP	2	3.60	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	2	3.60	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	2	3.60	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	2	3.60	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	2	3.60	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	2	3.60	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	2	3.60	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	2	3.60	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	2	3.60	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	2	3.60	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	2	3.60	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	2	3.60	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	2	3.60	4	12	OUT1E	LVDS Out
ACD_NVETO_17_AM	2	3.60	4	13	OUT1E	LVDS Out
VDDMID	2	3.60	4	14	OUT1E	VDD/2
GND	2	3.60	4	15	OUT1E	Ground
ACD_NVETO_17_BP	2	3.60	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	2	3.60	5	1	OUT1F	LVDS Out
ACD_CNO_AP	2	3.60	5	2	OUT1F	LVDS Out
ACD_CNO_AM	2	3.60	5	3	OUT1F	LVDS Out
ACD_CNO_BP	2	3.60	5	4	OUT1F	LVDS Out
ACD_CNO_BM	2	3.60	5	5	OUT1F	LVDS Out
ACD_CLK_AP	2	3.60	5	6	OUT1F	LVDS In
ACD_CLK_AM	2	3.60	5	7	OUT1F	LVDS In
ACD_CLK_BP	2	3.60	5	8	OUT1F	LVDS In
ACD_CLK_BM	2	3.60	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	2	3.60	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	2	3.60	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	2	3.60	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	2	3.60	5	13	OUT1F	LVDS In
ACD_NRST_AP	2	3.60	5	14	OUT1F	LVDS In
ACD_NRST_AM	2	3.60	5	15	OUT1F	LVDS In
ACD_NRST_BP	2	3.60	6	0	OUT2A	LVDS In

ACD_NRST_BM	2	3.60	6	1	OUT2A	LVDS In
ACD_NSDATA_AP	2	3.60	6	2	OUT2A	LVDS Out
ACD_NSDATA_AM	2	3.60	6	3	OUT2A	LVDS Out
ACD_NSDATA_BP	2	3.60	6	4	OUT2A	LVDS Out
ACD_NSDATA_BM	2	3.60	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	2	3.60	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	2	3.60	6	7	OUT2A	CMOS Input
GAFE_RST	2	3.60	6	8	OUT2A	CMOS Output
GAFE_RSTP	2	3.60	6	9	OUT2A	CMOS Output
DAC_DATA	2	3.60	6	10	OUT2A	CMOS Output
DAC_DATAP	2	3.60	6	11	OUT2A	CMOS Output
DAC_CLK	2	3.60	6	12	OUT2A	CMOS Output
DAC_CLKP	2	3.60	6	13	OUT2A	CMOS Output
VDDMDI	2	3.60	6	14	OUT2A	VDD/2
GND	2	3.60	6	15	OUT2A	Ground
NDAC_CS	2	3.60	7	0	OUT2B	CMOS Output
NDAC_CSP	2	3.60	7	1	OUT2B	CMOS Output
NDAC_CLR	2	3.60	7	2	OUT2B	CMOS Output
NDAC_CLRP	2	3.60	7	3	OUT2B	CMOS Output
HV_ENABLE1	2	3.60	7	4	OUT2B	CMOS Output
HV_ENABLE1P	2	3.60	7	5	OUT2B	CMOS Output
HV_ENABLE2	2	3.60	7	6	OUT2B	CMOS Output
HV_ENABLE2P	2	3.60	7	7	OUT2B	CMOS Output
ADC_CLK	2	3.60	7	8	OUT2B	CMOS Output
ADC_CLKP	2	3.60	7	9	OUT2B	CMOS Output
NADC_CS	2	3.60	7	10	OUT2B	CMOS Output
NADC_CSP	2	3.60	7	11	OUT2B	CMOS Output
DAC_DATA	2	3.60	7	12	OUT2B	CMOS Output
DAC_DATAP	2	3.60	7	13	OUT2B	CMOS Output
VDDMDI	2	3.60	7	14	OUT2B	VDD/2
GND	2	3.60	7	15	OUT2B	Ground
DAC_DATA_RET	2	3.60	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	2	3.60	8	1	OUT2C	CMOS Input
HITMAP_TEST	2	3.60	8	2	OUT2C	CMOS Output
HITMAP_TESTP	2	3.60	8	3	OUT2C	CMOS Output
TRIG_TEST	2	3.60	8	4	OUT2C	CMOS Output
TRIG_TESTP	2	3.60	8	5	OUT2C	CMOS Output
FREE_ID	2	3.60	8	6	OUT2C	CMOS Output
FREE_IDP	2	3.60	8	7	OUT2C	CMOS Output
PWR_ON_RST	2	3.60	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	2	3.60	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	2	3.60	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	2	3.60	8	11	OUT2C	CMOS Output
IRTN_17P	2	3.60	8	12	OUT2C	GAFE IF Return
IRTN_17	2	3.60	8	13	OUT2C	GAFE IF Return
VDDMDI	2	3.60	8	14	OUT2C	VDD/2
GND	2	3.60	8	15	OUT2C	Ground
DISC_17P	2	3.60	9	0	OUT2D	GAFE IF Input
DISC_17	2	3.60	9	1	OUT2D	GAFE IF Input
CHID_17P	2	3.60	9	2	OUT2D	GAFE IF Input

CHID_17	2	3.60	9	3	OUT2D	GAFE IF Input
IRTN_16P	2	3.60	9	4	OUT2D	GAFE IF Return
IRTN_16	2	3.60	9	5	OUT2D	GAFE IF Return
DISC_16P	2	3.60	9	6	OUT2D	GAFE IF Input
DISC_16	2	3.60	9	7	OUT2D	GAFE IF Input
CHID_16P	2	3.60	9	8	OUT2D	GAFE IF Input
CHID_16	2	3.60	9	9	OUT2D	GAFE IF Input
IRTN_15P	2	3.60	9	10	OUT2D	GAFE IF Return
IRTN_15	2	3.60	9	11	OUT2D	GAFE IF Return
DISC_15P	2	3.60	9	12	OUT2D	GAFE IF Input
DISC_15	2	3.60	9	13	OUT2D	GAFE IF Input
VDDMDID	2	3.60	9	14	OUT2D	VDD/2
GND	2	3.60	9	15	OUT2D	Ground
CHID_15P	2	3.60	10	0	OUT2E	GAFE IF Input
CHID_15	2	3.60	10	1	OUT2E	GAFE IF Input
IRTN_14P	2	3.60	10	2	OUT2E	GAFE IF Return
IRTN_14	2	3.60	10	3	OUT2E	GAFE IF Return
DISC_14P	2	3.60	10	4	OUT2E	GAFE IF Input
DISC_14	2	3.60	10	5	OUT2E	GAFE IF Input
CHID_14P	2	3.60	10	6	OUT2E	GAFE IF Input
CHID_14	2	3.60	10	7	OUT2E	GAFE IF Input
IRTN_13P	2	3.60	10	8	OUT2E	GAFE IF Return
IRTN_13	2	3.60	10	9	OUT2E	GAFE IF Return
DISC_13P	2	3.60	10	10	OUT2E	GAFE IF Input
DISC_13	2	3.60	10	11	OUT2E	GAFE IF Input
CHID_13P	2	3.60	10	12	OUT2E	GAFE IF Input
CHID_13	2	3.60	10	13	OUT2E	GAFE IF Input
IRTN_12P	2	3.60	10	14	OUT2E	GAFE IF Return
IRTN_12	2	3.60	10	15	OUT2E	GAFE IF Return
DISC_12P	2	3.60	11	0	OUT2F	GAFE IF Input
DISC_12	2	3.60	11	1	OUT2F	GAFE IF Input
CHID_12P	2	3.60	11	2	OUT2F	GAFE IF Input
CHID_12	2	3.60	11	3	OUT2F	GAFE IF Input
IRTN_11P	2	3.60	11	4	OUT2F	GAFE IF Return
IRTN_11	2	3.60	11	5	OUT2F	GAFE IF Return
DISC_11P	2	3.60	11	6	OUT2F	GAFE IF Input
DISC_11	2	3.60	11	7	OUT2F	GAFE IF Input
CHID_11P	2	3.60	11	8	OUT2F	GAFE IF Input
CHID_11	2	3.60	11	9	OUT2F	GAFE IF Input
IRTN_10P	2	3.60	11	10	OUT2F	GAFE IF Return
IRTN_10	2	3.60	11	11	OUT2F	GAFE IF Return
DISC_10P	2	3.60	11	12	OUT2F	GAFE IF Input
DISC_10	2	3.60	11	13	OUT2F	GAFE IF Input
CHID_10P	2	3.60	11	14	OUT2F	GAFE IF Input
CHID_10	2	3.60	11	15	OUT2F	GAFE IF Input
PHAD_00	2	3.60	12	0	OUT3A	CMOS Input
PHAD_00P	2	3.60	12	1	OUT3A	CMOS Input
PHAD_01	2	3.60	12	2	OUT3A	CMOS Input
PHAD_01P	2	3.60	12	3	OUT3A	CMOS Input
PHAD_02	2	3.60	12	4	OUT3A	CMOS Input

PHAD_02P	2	3.60	12	5	OUT3A	CMOS Input
PHAD_03	2	3.60	12	6	OUT3A	CMOS Input
PHAD_03P	2	3.60	12	7	OUT3A	CMOS Input
PHAD_04	2	3.60	12	8	OUT3A	CMOS Input
PHAD_04P	2	3.60	12	9	OUT3A	CMOS Input
PHAD_05	2	3.60	12	10	OUT3A	CMOS Input
PHAD_05P	2	3.60	12	11	OUT3A	CMOS Input
PHAD_06	2	3.60	12	12	OUT3A	CMOS Input
PHAD_06P	2	3.60	12	13	OUT3A	CMOS Input
PHAD_07	2	3.60	12	14	OUT3A	CMOS Input
PHAD_07P	2	3.60	12	15	OUT3A	CMOS Input
PHAD_08	2	3.60	13	0	OUT3B	CMOS Input
PHAD_08P	2	3.60	13	1	OUT3B	CMOS Input
PHAD_09	2	3.60	13	2	OUT3B	CMOS Input
PHAD_09P	2	3.60	13	3	OUT3B	CMOS Input
PHAD_10	2	3.60	13	4	OUT3B	CMOS Input
PHAD_10P	2	3.60	13	5	OUT3B	CMOS Input
PHAD_11	2	3.60	13	6	OUT3B	CMOS Input
PHAD_11P	2	3.60	13	7	OUT3B	CMOS Input
PHAD_12	2	3.60	13	8	OUT3B	CMOS Input
PHAD_12P	2	3.60	13	9	OUT3B	CMOS Input
PHAD_13	2	3.60	13	10	OUT3B	CMOS Input
PHAD_13P	2	3.60	13	11	OUT3B	CMOS Input
PHAD_14	2	3.60	13	12	OUT3B	CMOS Input
PHAD_14P	2	3.60	13	13	OUT3B	CMOS Input
PHAD_15	2	3.60	13	14	OUT3B	CMOS Input
PHAD_15P	2	3.60	13	15	OUT3B	CMOS Input
PHAD_16	2	3.60	14	0	OUT3C	CMOS Input
PHAD_16P	2	3.60	14	1	OUT3C	CMOS Input
PHAD_17	2	3.60	14	2	OUT3C	CMOS Input
PHAD_17P	2	3.60	14	3	OUT3C	CMOS Input
CHID_00	2	3.60	14	4	OUT3C	GAFE IF Input
CHID_00P	2	3.60	14	5	OUT3C	GAFE IF Input
DISC_00	2	3.60	14	6	OUT3C	GAFE IF Input
DISC_00P	2	3.60	14	7	OUT3C	GAFE IF Input
IRTN_00	2	3.60	14	8	OUT3C	GAFE IF Return
IRTN_00P	2	3.60	14	9	OUT3C	GAFE IF Return
CHID_01	2	3.60	14	10	OUT3C	GAFE IF Input
CHID_01P	2	3.60	14	11	OUT3C	GAFE IF Input
DISC_01	2	3.60	14	12	OUT3C	GAFE IF Input
DISC_01P	2	3.60	14	13	OUT3C	GAFE IF Input
IRTN_01	2	3.60	14	14	OUT3C	GAFE IF Return
IRTN_01P	2	3.60	14	15	OUT3C	GAFE IF Return
CHID_02	2	3.60	15	0	OUT3D	GAFE IF Input
CHID_02P	2	3.60	15	1	OUT3D	GAFE IF Input
DISC_02	2	3.60	15	2	OUT3D	GAFE IF Input
DISC_02P	2	3.60	15	3	OUT3D	GAFE IF Input
IRTN_02	2	3.60	15	4	OUT3D	GAFE IF Return
IRTN_02P	2	3.60	15	5	OUT3D	GAFE IF Return
CHID_03	2	3.60	15	6	OUT3D	GAFE IF Input

CHID_03P	2	3.60	15	7	OUT3D	GAFE IF Input
DISC_03	2	3.60	15	8	OUT3D	GAFE IF Input
DISC_03P	2	3.60	15	9	OUT3D	GAFE IF Input
IRTN_03	2	3.60	15	10	OUT3D	GAFE IF Return
IRTN_03P	2	3.60	15	11	OUT3D	GAFE IF Return
CHID_04	2	3.60	15	12	OUT3D	GAFE IF Input
CHID_04P	2	3.60	15	13	OUT3D	GAFE IF Input
DISC_04	2	3.60	15	14	OUT3D	GAFE IF Input
DISC_04P	2	3.60	15	15	OUT3D	GAFE IF Input
IRTN_04	2	3.60	16	0	OUT3E	GAFE IF Return
IRTN_04P	2	3.60	16	1	OUT3E	GAFE IF Return
CHID_05	2	3.60	16	2	OUT3E	GAFE IF Input
CHID_05P	2	3.60	16	3	OUT3E	GAFE IF Input
DISC_05	2	3.60	16	4	OUT3E	GAFE IF Input
DISC_05P	2	3.60	16	5	OUT3E	GAFE IF Input
IRTN_05	2	3.60	16	6	OUT3E	GAFE IF Return
IRTN_05P	2	3.60	16	7	OUT3E	GAFE IF Return
CHID_06	2	3.60	16	8	OUT3E	GAFE IF Input
CHID_06P	2	3.60	16	9	OUT3E	GAFE IF Input
DISC_06	2	3.60	16	10	OUT3E	GAFE IF Input
DISC_06P	2	3.60	16	11	OUT3E	GAFE IF Input
IRTN_06	2	3.60	16	12	OUT3E	GAFE IF Return
IRTN_06P	2	3.60	16	13	OUT3E	GAFE IF Return
CHID_07	2	3.60	16	14	OUT3E	GAFE IF Input
CHID_07P	2	3.60	16	15	OUT3E	GAFE IF Input
DISC_07	2	3.60	17	0	OUT3F	GAFE IF Input
DISC_07P	2	3.60	17	1	OUT3F	GAFE IF Input
IRTN_07	2	3.60	17	2	OUT3F	GAFE IF Return
IRTN_07P	2	3.60	17	3	OUT3F	GAFE IF Return
CHID_08	2	3.60	17	4	OUT3F	GAFE IF Input
CHID_08P	2	3.60	17	5	OUT3F	GAFE IF Input
DISC_08	2	3.60	17	6	OUT3F	GAFE IF Input
DISC_08P	2	3.60	17	7	OUT3F	GAFE IF Input
IRTN_08	2	3.60	17	8	OUT3F	GAFE IF Return
IRTN_08P	2	3.60	17	9	OUT3F	GAFE IF Return
CHID_09	2	3.60	17	10	OUT3F	GAFE IF Input
CHID_09P	2	3.60	17	11	OUT3F	GAFE IF Input
DISC_09	2	3.60	17	12	OUT3F	GAFE IF Input
DISC_09P	2	3.60	17	13	OUT3F	GAFE IF Input
IRTN_09	2	3.60	17	14	OUT3F	GAFE IF Return
IRTN_09P	2	3.60	17	15	OUT3F	GAFE IF Return
DGND001	2	3.60	18	0	OUT4A	Ground Pin
DGND027	2	3.60	18	1	OUT4A	Ground Pin
DGND053	2	3.60	18	2	OUT4A	Ground Pin
DGND080	2	3.60	18	3	OUT4A	Ground Pin
DGND136	2	3.60	18	4	OUT4A	Ground Pin
DGND157	2	3.60	18	5	OUT4A	Ground Pin
DGND187	2	3.60	18	6	OUT4A	Ground Pin
DVDD024	2	3.60	18	7	OUT4A	Power Pin
DVDD052	2	3.60	18	8	OUT4A	Power Pin

DVDD078	2	3.60	18	9	OUT4A	Power Pin
DVDD132	2	3.60	18	10	OUT4A	Power Pin
DVDD155	2	3.60	18	11	OUT4A	Power Pin
DVDD208	2	3.60	18	12	OUT4A	Power Pin
CMDCK+	2	3.60	18	13	OUT4A	GAFE Diff Output
CMDCK-	2	3.60	18	14	OUT4A	GAFE Diff Output
GND	2	3.60	18	15	OUT4A	VDD/2
VDDMID	2	3.60	19	0	OUT4B	Ground
CMDD+	2	3.60	19	1	OUT4B	GAFE Diff Output
CMDD-	2	3.60	19	2	OUT4B	GAFE Diff Output
STROB+	2	3.60	19	3	OUT4B	GAFE Diff Output
STROB-	2	3.60	19	4	OUT4B	GAFE Diff Output
HOLD+	2	3.60	19	5	OUT4B	GAFE Diff Output
HOLD-	2	3.60	19	6	OUT4B	GAFE Diff Output
HLD	2	3.60	19	7	OUT4B	GAFE HLD
IRTN_HLD	2	3.60	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	2	3.60	19	9	OUT4B	Bias Pin
BIAS_DRV_L	2	3.60	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	2	3.60	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	2	3.60	19	12	OUT4B	Bias Pin
BIAS_RCVR	2	3.60	19	13	OUT4B	Bias Pin
VDDMID	2	3.60	19	14	OUT4B	VDD/2
GND	2	3.60	19	15	OUT4B	Ground
ACD_NVETO_00_AP	3	3.60	0	0	OUT1A	LVDS Out
ACD_NVETO_00_AM	3	3.60	0	1	OUT1A	LVDS Out
ACD_NVETO_00_BP	3	3.60	0	2	OUT1A	LVDS Out
ACD_NVETO_00_BM	3	3.60	0	3	OUT1A	LVDS Out
ACD_NVETO_01_AP	3	3.60	0	4	OUT1A	LVDS Out
ACD_NVETO_01_AM	3	3.60	0	5	OUT1A	LVDS Out
ACD_NVETO_01_BP	3	3.60	0	6	OUT1A	LVDS Out
ACD_NVETO_01_BM	3	3.60	0	7	OUT1A	LVDS Out
ACD_NVETO_02_AP	3	3.60	0	8	OUT1A	LVDS Out
ACD_NVETO_02_AM	3	3.60	0	9	OUT1A	LVDS Out
ACD_NVETO_02_BP	3	3.60	0	10	OUT1A	LVDS Out
ACD_NVETO_02_BM	3	3.60	0	11	OUT1A	LVDS Out
ACD_NVETO_03_AP	3	3.60	0	12	OUT1A	LVDS Out
ACD_NVETO_03_AM	3	3.60	0	13	OUT1A	LVDS Out
VDDMID	3	3.60	0	14	OUT1A	VDD/2
GND	3	3.60	0	15	OUT1A	Ground
ACD_NVETO_03_BP	3	3.60	1	0	OUT1B	LVDS Out
ACD_NVETO_03_BM	3	3.60	1	1	OUT1B	LVDS Out
ACD_NVETO_04_AP	3	3.60	1	2	OUT1B	LVDS Out
ACD_NVETO_04_AM	3	3.60	1	3	OUT1B	LVDS Out
ACD_NVETO_04_BP	3	3.60	1	4	OUT1B	LVDS Out
ACD_NVETO_04_BM	3	3.60	1	5	OUT1B	LVDS Out
ACD_NVETO_05_AP	3	3.60	1	6	OUT1B	LVDS Out
ACD_NVETO_05_AM	3	3.60	1	7	OUT1B	LVDS Out
ACD_NVETO_05_BP	3	3.60	1	8	OUT1B	LVDS Out
ACD_NVETO_05_BM	3	3.60	1	9	OUT1B	LVDS Out
ACD_NVETO_06_AP	3	3.60	1	10	OUT1B	LVDS Out

ACD_NVETO_06_AM	3	3.60	1	11	OUT1B	LVDS Out
ACD_NVETO_06_BP	3	3.60	1	12	OUT1B	LVDS Out
ACD_NVETO_06_BM	3	3.60	1	13	OUT1B	LVDS Out
VDDMID	3	3.60	1	14	OUT1B	VDD/2
GND	3	3.60	1	15	OUT1B	Ground
ACD_NVETO_07_AP	3	3.60	2	0	OUT1C	LVDS Out
ACD_NVETO_07_AM	3	3.60	2	1	OUT1C	LVDS Out
ACD_NVETO_07_BP	3	3.60	2	2	OUT1C	LVDS Out
ACD_NVETO_07_BM	3	3.60	2	3	OUT1C	LVDS Out
ACD_NVETO_08_AP	3	3.60	2	4	OUT1C	LVDS Out
ACD_NVETO_08_AM	3	3.60	2	5	OUT1C	LVDS Out
ACD_NVETO_08_BP	3	3.60	2	6	OUT1C	LVDS Out
ACD_NVETO_08_BM	3	3.60	2	7	OUT1C	LVDS Out
ACD_NVETO_09_AP	3	3.60	2	8	OUT1C	LVDS Out
ACD_NVETO_09_AM	3	3.60	2	9	OUT1C	LVDS Out
ACD_NVETO_09_BP	3	3.60	2	10	OUT1C	LVDS Out
ACD_NVETO_09_BM	3	3.60	2	11	OUT1C	LVDS Out
ACD_NVETO_10_AP	3	3.60	2	12	OUT1C	LVDS Out
ACD_NVETO_10_AM	3	3.60	2	13	OUT1C	LVDS Out
VDDMID	3	3.60	2	14	OUT1C	VDD/2
GND	3	3.60	2	15	OUT1C	Ground
ACD_NVETO_10_BP	3	3.60	3	0	OUT1D	LVDS Out
ACD_NVETO_10_BM	3	3.60	3	1	OUT1D	LVDS Out
ACD_NVETO_11_AP	3	3.60	3	2	OUT1D	LVDS Out
ACD_NVETO_11_AM	3	3.60	3	3	OUT1D	LVDS Out
ACD_NVETO_11_BP	3	3.60	3	4	OUT1D	LVDS Out
ACD_NVETO_11_BM	3	3.60	3	5	OUT1D	LVDS Out
ACD_NVETO_12_AP	3	3.60	3	6	OUT1D	LVDS Out
ACD_NVETO_12_AM	3	3.60	3	7	OUT1D	LVDS Out
ACD_NVETO_12_BP	3	3.60	3	8	OUT1D	LVDS Out
ACD_NVETO_12_BM	3	3.60	3	9	OUT1D	LVDS Out
ACD_NVETO_13_AP	3	3.60	3	10	OUT1D	LVDS Out
ACD_NVETO_13_AM	3	3.60	3	11	OUT1D	LVDS Out
ACD_NVETO_13_BP	3	3.60	3	12	OUT1D	LVDS Out
ACD_NVETO_13_BM	3	3.60	3	13	OUT1D	LVDS Out
VDDMID	3	3.60	3	14	OUT1D	VDD/2
GND	3	3.60	3	15	OUT1D	Ground
ACD_NVETO_14_AP	3	3.60	4	0	OUT1E	LVDS Out
ACD_NVETO_14_AM	3	3.60	4	1	OUT1E	LVDS Out
ACD_NVETO_14_BP	3	3.60	4	2	OUT1E	LVDS Out
ACD_NVETO_14_BM	3	3.60	4	3	OUT1E	LVDS Out
ACD_NVETO_15_AP	3	3.60	4	4	OUT1E	LVDS Out
ACD_NVETO_15_AM	3	3.60	4	5	OUT1E	LVDS Out
ACD_NVETO_15_BP	3	3.60	4	6	OUT1E	LVDS Out
ACD_NVETO_15_BM	3	3.60	4	7	OUT1E	LVDS Out
ACD_NVETO_16_AP	3	3.60	4	8	OUT1E	LVDS Out
ACD_NVETO_16_AM	3	3.60	4	9	OUT1E	LVDS Out
ACD_NVETO_16_BP	3	3.60	4	10	OUT1E	LVDS Out
ACD_NVETO_16_BM	3	3.60	4	11	OUT1E	LVDS Out
ACD_NVETO_17_AP	3	3.60	4	12	OUT1E	LVDS Out

ACD_NVETO_17_AM	3	3.60	4	13	OUT1E	LVDS Out
VDDMID	3	3.60	4	14	OUT1E	VDD/2
GND	3	3.60	4	15	OUT1E	Ground
ACD_NVETO_17_BP	3	3.60	5	0	OUT1F	LVDS Out
ACD_NVETO_17_BM	3	3.60	5	1	OUT1F	LVDS Out
ACD_CNO_AP	3	3.60	5	2	OUT1F	LVDS Out
ACD_CNO_AM	3	3.60	5	3	OUT1F	LVDS Out
ACD_CNO_BP	3	3.60	5	4	OUT1F	LVDS Out
ACD_CNO_BM	3	3.60	5	5	OUT1F	LVDS Out
ACD_CLK_AP	3	3.60	5	6	OUT1F	LVDS In
ACD_CLK_AM	3	3.60	5	7	OUT1F	LVDS In
ACD_CLK_BP	3	3.60	5	8	OUT1F	LVDS In
ACD_CLK_BM	3	3.60	5	9	OUT1F	LVDS In
ACD_NSCMD_AP	3	3.60	5	10	OUT1F	LVDS In
ACD_NSCMD_AM	3	3.60	5	11	OUT1F	LVDS In
ACD_NSCMD_BP	3	3.60	5	12	OUT1F	LVDS In
ACD_NSCMD_BM	3	3.60	5	13	OUT1F	LVDS In
ACD_NRST_AP	3	3.60	5	14	OUT1F	LVDS In
ACD_NRST_AM	3	3.60	5	15	OUT1F	LVDS In
ACD_NRST_BP	3	3.60	6	0	OUT2A	LVDS In
ACD_NRST_BM	3	3.60	6	1	OUT2A	LVDS In
ACD_NS DATA_AP	3	3.60	6	2	OUT2A	LVDS Out
ACD_NS DATA_AM	3	3.60	6	3	OUT2A	LVDS Out
ACD_NS DATA_BP	3	3.60	6	4	OUT2A	LVDS Out
ACD_NS DATA_BM	3	3.60	6	5	OUT2A	LVDS Out
GAFE_RET_DATA	3	3.60	6	6	OUT2A	CMOS Input
GAFE_RET_DATAP	3	3.60	6	7	OUT2A	CMOS Input
GAFE_RST	3	3.60	6	8	OUT2A	CMOS Output
GAFE_RSTP	3	3.60	6	9	OUT2A	CMOS Output
DAC_DATA	3	3.60	6	10	OUT2A	CMOS Output
DAC_DATAP	3	3.60	6	11	OUT2A	CMOS Output
DAC_CLK	3	3.60	6	12	OUT2A	CMOS Output
DAC_CLKP	3	3.60	6	13	OUT2A	CMOS Output
VDDMID	3	3.60	6	14	OUT2A	VDD/2
GND	3	3.60	6	15	OUT2A	Ground
NDAC_CS	3	3.60	7	0	OUT2B	CMOS Output
NDAC_CSP	3	3.60	7	1	OUT2B	CMOS Output
NDAC_CLR	3	3.60	7	2	OUT2B	CMOS Output
NDAC_CLRP	3	3.60	7	3	OUT2B	CMOS Output
HV_ENABLE1	3	3.60	7	4	OUT2B	CMOS Output
HV_ENABLE1P	3	3.60	7	5	OUT2B	CMOS Output
HV_ENABLE2	3	3.60	7	6	OUT2B	CMOS Output
HV_ENABLE2P	3	3.60	7	7	OUT2B	CMOS Output
ADC_CLK	3	3.60	7	8	OUT2B	CMOS Output
ADC_CLKP	3	3.60	7	9	OUT2B	CMOS Output
NADC_CS	3	3.60	7	10	OUT2B	CMOS Output
NADC_CSP	3	3.60	7	11	OUT2B	CMOS Output
DAC_DATA	3	3.60	7	12	OUT2B	CMOS Output
DAC_DATAP	3	3.60	7	13	OUT2B	CMOS Output
VDDMID	3	3.60	7	14	OUT2B	VDD/2

GND	3	3.60	7	15	OUT2B	Ground
DAC_DATA_RET	3	3.60	8	0	OUT2C	CMOS Input
DAC_DATA_RETP	3	3.60	8	1	OUT2C	CMOS Input
HITMAP_TEST	3	3.60	8	2	OUT2C	CMOS Output
HITMAP_TESTP	3	3.60	8	3	OUT2C	CMOS Output
TRIG_TEST	3	3.60	8	4	OUT2C	CMOS Output
TRIG_TESTP	3	3.60	8	5	OUT2C	CMOS Output
FREE_ID	3	3.60	8	6	OUT2C	CMOS Output
FREE_IDP	3	3.60	8	7	OUT2C	CMOS Output
PWR_ON_RST	3	3.60	8	8	OUT2C	CMOS Input
PWR_ON_RSTP	3	3.60	8	9	OUT2C	CMOS Input
VETO_ENA_TEST	3	3.60	8	10	OUT2C	CMOS Output
VETO_ENA_TESTP	3	3.60	8	11	OUT2C	CMOS Output
IRTN_17P	3	3.60	8	12	OUT2C	GAFE IF Return
IRTN_17	3	3.60	8	13	OUT2C	GAFE IF Return
VDDMID	3	3.60	8	14	OUT2C	VDD/2
GND	3	3.60	8	15	OUT2C	Ground
DISC_17P	3	3.60	9	0	OUT2D	GAFE IF Input
DISC_17	3	3.60	9	1	OUT2D	GAFE IF Input
CHID_17P	3	3.60	9	2	OUT2D	GAFE IF Input
CHID_17	3	3.60	9	3	OUT2D	GAFE IF Input
IRTN_16P	3	3.60	9	4	OUT2D	GAFE IF Return
IRTN_16	3	3.60	9	5	OUT2D	GAFE IF Return
DISC_16P	3	3.60	9	6	OUT2D	GAFE IF Input
DISC_16	3	3.60	9	7	OUT2D	GAFE IF Input
CHID_16P	3	3.60	9	8	OUT2D	GAFE IF Input
CHID_16	3	3.60	9	9	OUT2D	GAFE IF Input
IRTN_15P	3	3.60	9	10	OUT2D	GAFE IF Return
IRTN_15	3	3.60	9	11	OUT2D	GAFE IF Return
DISC_15P	3	3.60	9	12	OUT2D	GAFE IF Input
DISC_15	3	3.60	9	13	OUT2D	GAFE IF Input
VDDMID	3	3.60	9	14	OUT2D	VDD/2
GND	3	3.60	9	15	OUT2D	Ground
CHID_15P	3	3.60	10	0	OUT2E	GAFE IF Input
CHID_15	3	3.60	10	1	OUT2E	GAFE IF Input
IRTN_14P	3	3.60	10	2	OUT2E	GAFE IF Return
IRTN_14	3	3.60	10	3	OUT2E	GAFE IF Return
DISC_14P	3	3.60	10	4	OUT2E	GAFE IF Input
DISC_14	3	3.60	10	5	OUT2E	GAFE IF Input
CHID_14P	3	3.60	10	6	OUT2E	GAFE IF Input
CHID_14	3	3.60	10	7	OUT2E	GAFE IF Input
IRTN_13P	3	3.60	10	8	OUT2E	GAFE IF Return
IRTN_13	3	3.60	10	9	OUT2E	GAFE IF Return
DISC_13P	3	3.60	10	10	OUT2E	GAFE IF Input
DISC_13	3	3.60	10	11	OUT2E	GAFE IF Input
CHID_13P	3	3.60	10	12	OUT2E	GAFE IF Input
CHID_13	3	3.60	10	13	OUT2E	GAFE IF Input
IRTN_12P	3	3.60	10	14	OUT2E	GAFE IF Return
IRTN_12	3	3.60	10	15	OUT2E	GAFE IF Return
DISC_12P	3	3.60	11	0	OUT2F	GAFE IF Input

DISC_12	3	3.60	11	1	OUT2F	GAFE IF Input
CHID_12P	3	3.60	11	2	OUT2F	GAFE IF Input
CHID_12	3	3.60	11	3	OUT2F	GAFE IF Input
IRTN_11P	3	3.60	11	4	OUT2F	GAFE IF Return
IRTN_11	3	3.60	11	5	OUT2F	GAFE IF Return
DISC_11P	3	3.60	11	6	OUT2F	GAFE IF Input
DISC_11	3	3.60	11	7	OUT2F	GAFE IF Input
CHID_11P	3	3.60	11	8	OUT2F	GAFE IF Input
CHID_11	3	3.60	11	9	OUT2F	GAFE IF Input
IRTN_10P	3	3.60	11	10	OUT2F	GAFE IF Return
IRTN_10	3	3.60	11	11	OUT2F	GAFE IF Return
DISC_10P	3	3.60	11	12	OUT2F	GAFE IF Input
DISC_10	3	3.60	11	13	OUT2F	GAFE IF Input
CHID_10P	3	3.60	11	14	OUT2F	GAFE IF Input
CHID_10	3	3.60	11	15	OUT2F	GAFE IF Input
PHAD_00	3	3.60	12	0	OUT3A	CMOS Input
PHAD_00P	3	3.60	12	1	OUT3A	CMOS Input
PHAD_01	3	3.60	12	2	OUT3A	CMOS Input
PHAD_01P	3	3.60	12	3	OUT3A	CMOS Input
PHAD_02	3	3.60	12	4	OUT3A	CMOS Input
PHAD_02P	3	3.60	12	5	OUT3A	CMOS Input
PHAD_03	3	3.60	12	6	OUT3A	CMOS Input
PHAD_03P	3	3.60	12	7	OUT3A	CMOS Input
PHAD_04	3	3.60	12	8	OUT3A	CMOS Input
PHAD_04P	3	3.60	12	9	OUT3A	CMOS Input
PHAD_05	3	3.60	12	10	OUT3A	CMOS Input
PHAD_05P	3	3.60	12	11	OUT3A	CMOS Input
PHAD_06	3	3.60	12	12	OUT3A	CMOS Input
PHAD_06P	3	3.60	12	13	OUT3A	CMOS Input
PHAD_07	3	3.60	12	14	OUT3A	CMOS Input
PHAD_07P	3	3.60	12	15	OUT3A	CMOS Input
PHAD_08	3	3.60	13	0	OUT3B	CMOS Input
PHAD_08P	3	3.60	13	1	OUT3B	CMOS Input
PHAD_09	3	3.60	13	2	OUT3B	CMOS Input
PHAD_09P	3	3.60	13	3	OUT3B	CMOS Input
PHAD_10	3	3.60	13	4	OUT3B	CMOS Input
PHAD_10P	3	3.60	13	5	OUT3B	CMOS Input
PHAD_11	3	3.60	13	6	OUT3B	CMOS Input
PHAD_11P	3	3.60	13	7	OUT3B	CMOS Input
PHAD_12	3	3.60	13	8	OUT3B	CMOS Input
PHAD_12P	3	3.60	13	9	OUT3B	CMOS Input
PHAD_13	3	3.60	13	10	OUT3B	CMOS Input
PHAD_13P	3	3.60	13	11	OUT3B	CMOS Input
PHAD_14	3	3.60	13	12	OUT3B	CMOS Input
PHAD_14P	3	3.60	13	13	OUT3B	CMOS Input
PHAD_15	3	3.60	13	14	OUT3B	CMOS Input
PHAD_15P	3	3.60	13	15	OUT3B	CMOS Input
PHAD_16	3	3.60	14	0	OUT3C	CMOS Input
PHAD_16P	3	3.60	14	1	OUT3C	CMOS Input
PHAD_17	3	3.60	14	2	OUT3C	CMOS Input

PHAD_17P	3	3.60	14	3	OUT3C	CMOS Input
CHID_00	3	3.60	14	4	OUT3C	GAFE IF Input
CHID_00P	3	3.60	14	5	OUT3C	GAFE IF Input
DISC_00	3	3.60	14	6	OUT3C	GAFE IF Input
DISC_00P	3	3.60	14	7	OUT3C	GAFE IF Input
IRTN_00	3	3.60	14	8	OUT3C	GAFE IF Return
IRTN_00P	3	3.60	14	9	OUT3C	GAFE IF Return
CHID_01	3	3.60	14	10	OUT3C	GAFE IF Input
CHID_01P	3	3.60	14	11	OUT3C	GAFE IF Input
DISC_01	3	3.60	14	12	OUT3C	GAFE IF Input
DISC_01P	3	3.60	14	13	OUT3C	GAFE IF Input
IRTN_01	3	3.60	14	14	OUT3C	GAFE IF Return
IRTN_01P	3	3.60	14	15	OUT3C	GAFE IF Return
CHID_02	3	3.60	15	0	OUT3D	GAFE IF Input
CHID_02P	3	3.60	15	1	OUT3D	GAFE IF Input
DISC_02	3	3.60	15	2	OUT3D	GAFE IF Input
DISC_02P	3	3.60	15	3	OUT3D	GAFE IF Input
IRTN_02	3	3.60	15	4	OUT3D	GAFE IF Return
IRTN_02P	3	3.60	15	5	OUT3D	GAFE IF Return
CHID_03	3	3.60	15	6	OUT3D	GAFE IF Input
CHID_03P	3	3.60	15	7	OUT3D	GAFE IF Input
DISC_03	3	3.60	15	8	OUT3D	GAFE IF Input
DISC_03P	3	3.60	15	9	OUT3D	GAFE IF Input
IRTN_03	3	3.60	15	10	OUT3D	GAFE IF Return
IRTN_03P	3	3.60	15	11	OUT3D	GAFE IF Return
CHID_04	3	3.60	15	12	OUT3D	GAFE IF Input
CHID_04P	3	3.60	15	13	OUT3D	GAFE IF Input
DISC_04	3	3.60	15	14	OUT3D	GAFE IF Input
DISC_04P	3	3.60	15	15	OUT3D	GAFE IF Input
IRTN_04	3	3.60	16	0	OUT3E	GAFE IF Return
IRTN_04P	3	3.60	16	1	OUT3E	GAFE IF Return
CHID_05	3	3.60	16	2	OUT3E	GAFE IF Input
CHID_05P	3	3.60	16	3	OUT3E	GAFE IF Input
DISC_05	3	3.60	16	4	OUT3E	GAFE IF Input
DISC_05P	3	3.60	16	5	OUT3E	GAFE IF Input
IRTN_05	3	3.60	16	6	OUT3E	GAFE IF Return
IRTN_05P	3	3.60	16	7	OUT3E	GAFE IF Return
CHID_06	3	3.60	16	8	OUT3E	GAFE IF Input
CHID_06P	3	3.60	16	9	OUT3E	GAFE IF Input
DISC_06	3	3.60	16	10	OUT3E	GAFE IF Input
DISC_06P	3	3.60	16	11	OUT3E	GAFE IF Input
IRTN_06	3	3.60	16	12	OUT3E	GAFE IF Return
IRTN_06P	3	3.60	16	13	OUT3E	GAFE IF Return
CHID_07	3	3.60	16	14	OUT3E	GAFE IF Input
CHID_07P	3	3.60	16	15	OUT3E	GAFE IF Input
DISC_07	3	3.60	17	0	OUT3F	GAFE IF Input
DISC_07P	3	3.60	17	1	OUT3F	GAFE IF Input
IRTN_07	3	3.60	17	2	OUT3F	GAFE IF Return
IRTN_07P	3	3.60	17	3	OUT3F	GAFE IF Return
CHID_08	3	3.60	17	4	OUT3F	GAFE IF Input

CHID_08P	3	3.60	17	5	OUT3F	GAFE IF Input
DISC_08	3	3.60	17	6	OUT3F	GAFE IF Input
DISC_08P	3	3.60	17	7	OUT3F	GAFE IF Input
IRTN_08	3	3.60	17	8	OUT3F	GAFE IF Return
IRTN_08P	3	3.60	17	9	OUT3F	GAFE IF Return
CHID_09	3	3.60	17	10	OUT3F	GAFE IF Input
CHID_09P	3	3.60	17	11	OUT3F	GAFE IF Input
DISC_09	3	3.60	17	12	OUT3F	GAFE IF Input
DISC_09P	3	3.60	17	13	OUT3F	GAFE IF Input
IRTN_09	3	3.60	17	14	OUT3F	GAFE IF Return
IRTN_09P	3	3.60	17	15	OUT3F	GAFE IF Return
DGND001	3	3.60	18	0	OUT4A	Ground Pin
DGND027	3	3.60	18	1	OUT4A	Ground Pin
DGND053	3	3.60	18	2	OUT4A	Ground Pin
DGND080	3	3.60	18	3	OUT4A	Ground Pin
DGND136	3	3.60	18	4	OUT4A	Ground Pin
DGND157	3	3.60	18	5	OUT4A	Ground Pin
DGND187	3	3.60	18	6	OUT4A	Ground Pin
DVDD024	3	3.60	18	7	OUT4A	Power Pin
DVDD052	3	3.60	18	8	OUT4A	Power Pin
DVDD078	3	3.60	18	9	OUT4A	Power Pin
DVDD132	3	3.60	18	10	OUT4A	Power Pin
DVDD155	3	3.60	18	11	OUT4A	Power Pin
DVDD208	3	3.60	18	12	OUT4A	Power Pin
CMDCK+	3	3.60	18	13	OUT4A	GAFE Diff Output
CMDCK-	3	3.60	18	14	OUT4A	GAFE Diff Output
GND	3	3.60	18	15	OUT4A	VDD/2
VDDMID	3	3.60	19	0	OUT4B	Ground
CMDD+	3	3.60	19	1	OUT4B	GAFE Diff Output
CMDD-	3	3.60	19	2	OUT4B	GAFE Diff Output
STROB+	3	3.60	19	3	OUT4B	GAFE Diff Output
STROB-	3	3.60	19	4	OUT4B	GAFE Diff Output
HOLD+	3	3.60	19	5	OUT4B	GAFE Diff Output
HOLD-	3	3.60	19	6	OUT4B	GAFE Diff Output
HLD	3	3.60	19	7	OUT4B	GAFE HLD
IRTN_HLD	3	3.60	19	8	OUT4B	GAFE HLD Return
BIAS_DRV_H	3	3.60	19	9	OUT4B	Bias Pin
BIAS_DRV_L	3	3.60	19	10	OUT4B	Bias Pin
LVDS_PRESET_ADJ	3	3.60	19	11	OUT4B	Bias Pin
HLD_WOR_BIAS	3	3.60	19	12	OUT4B	Bias Pin
BIAS_RCVR	3	3.60	19	13	OUT4B	Bias Pin
VDDMID	3	3.60	19	14	OUT4B	VDD/2
GND	3	3.60	19	15	OUT4B	Ground