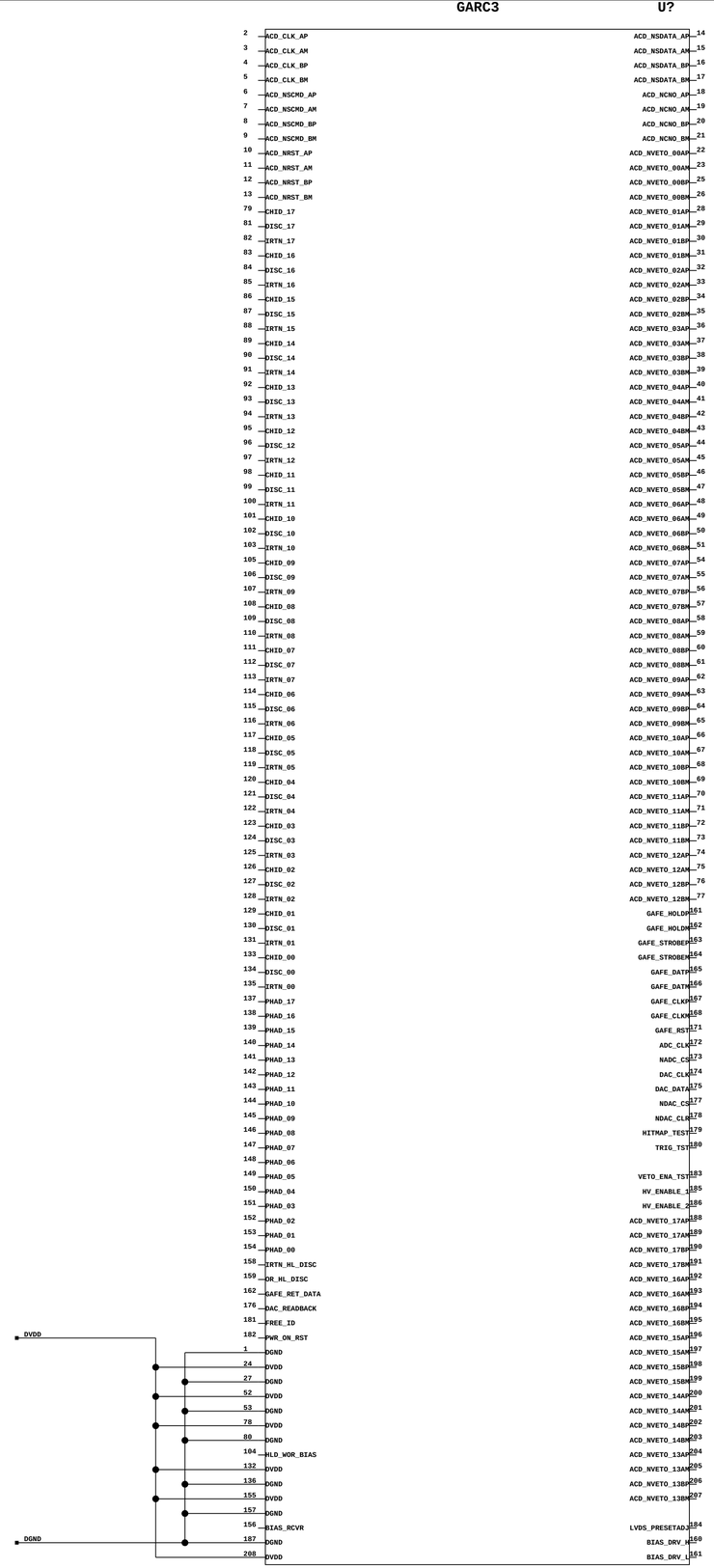
REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE



garc_top

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STANFORD LINEAR ACCELERATOR CENTER U.S. DEPARTMENT OF ENERGY STANFORD UNIVERSITY — STANFORD, CALIFORNIA		SHEET OF
PROPRIETARY DATA OF STANFORD UNIVERSITY AND/OR U.S. DEPARTMENT OF ENERGY. RECIPIENT SHALL NOT PUBLISH THE INFORMATION WITHIN UNLESS GRANTED SPECIFIC PERMISSION OF STANFORD UNIVERSITY.		SLAC GLAST GARC_VER3
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REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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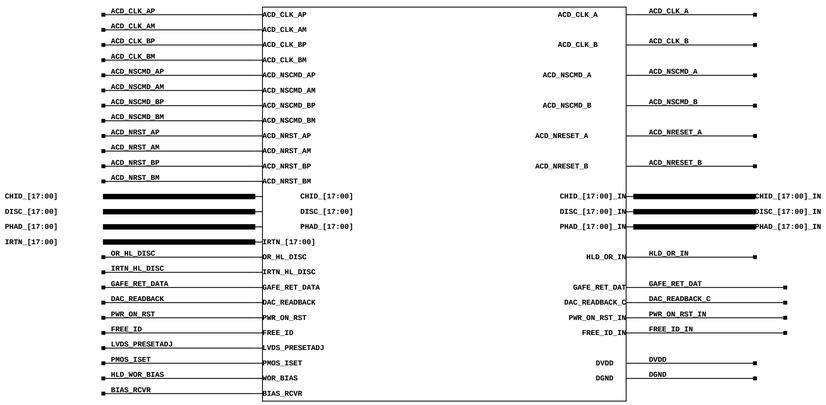

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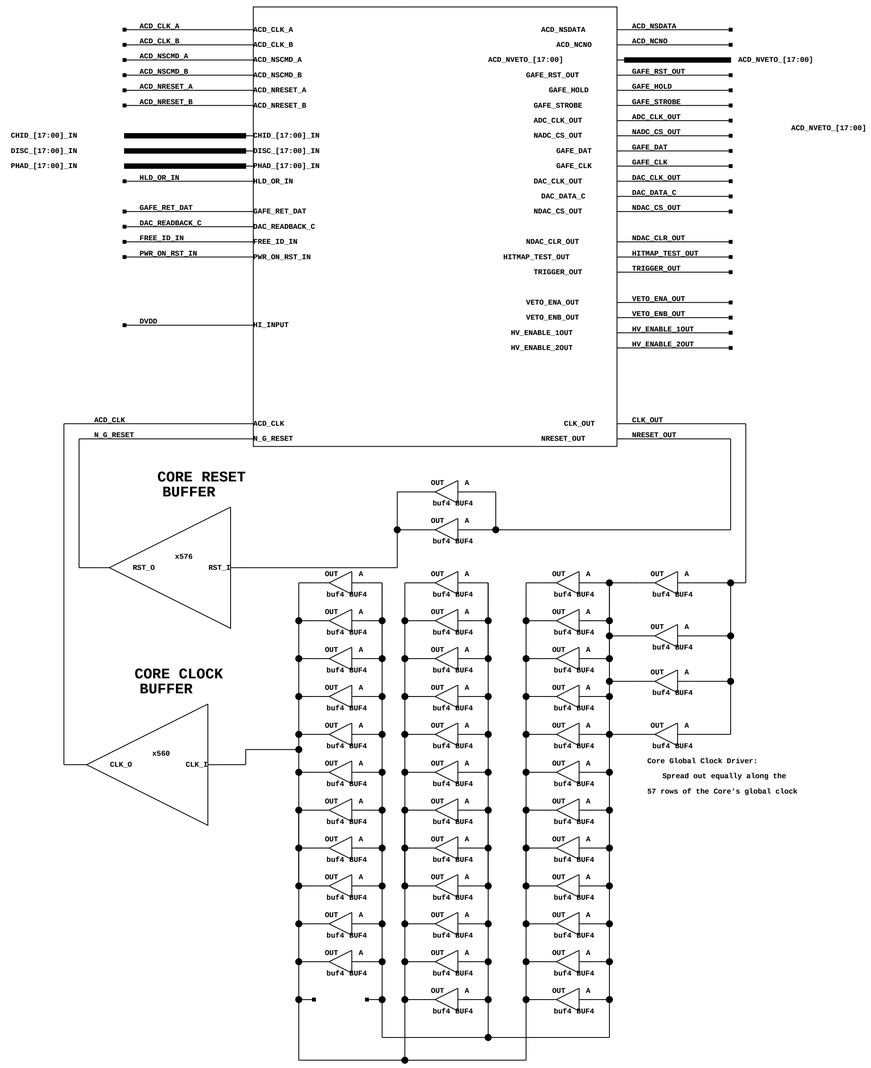

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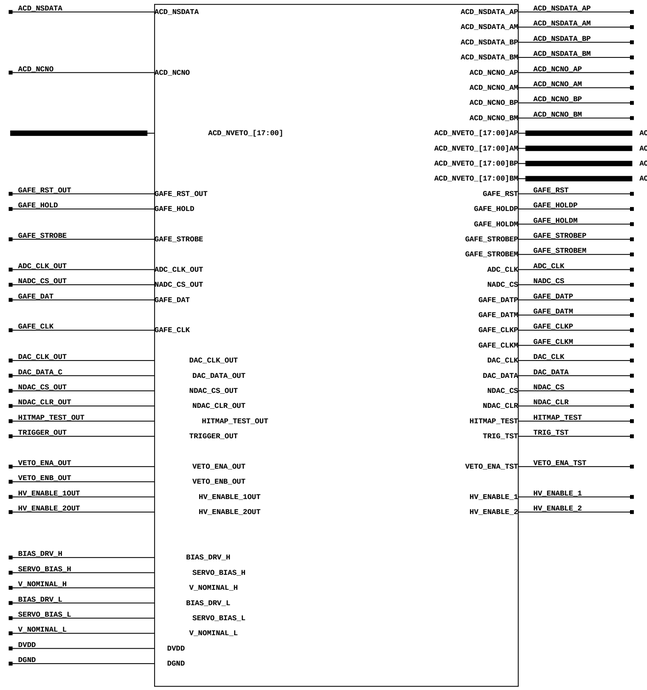
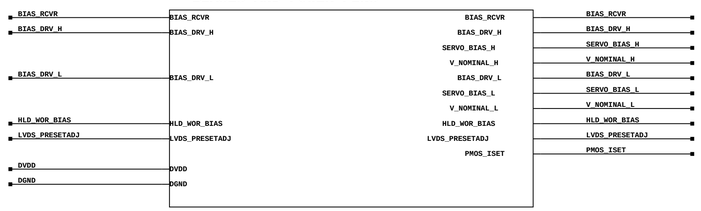
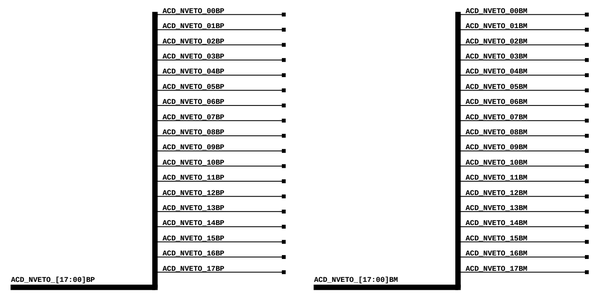
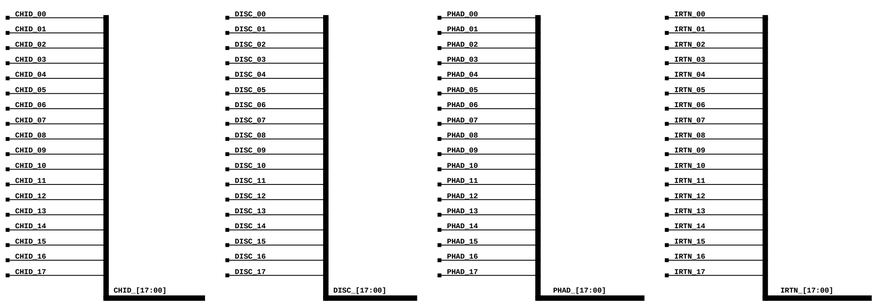
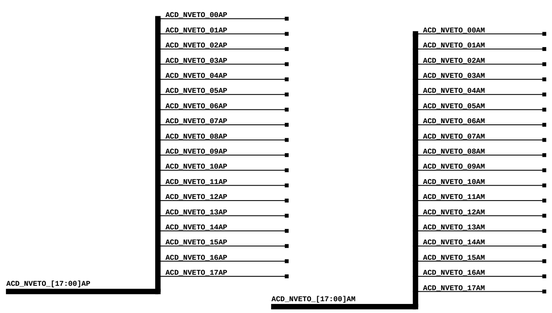


BIASES AND POWER PINS









GARC_VER3

5-9-2003_9:29

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	DATE	APPROVALS
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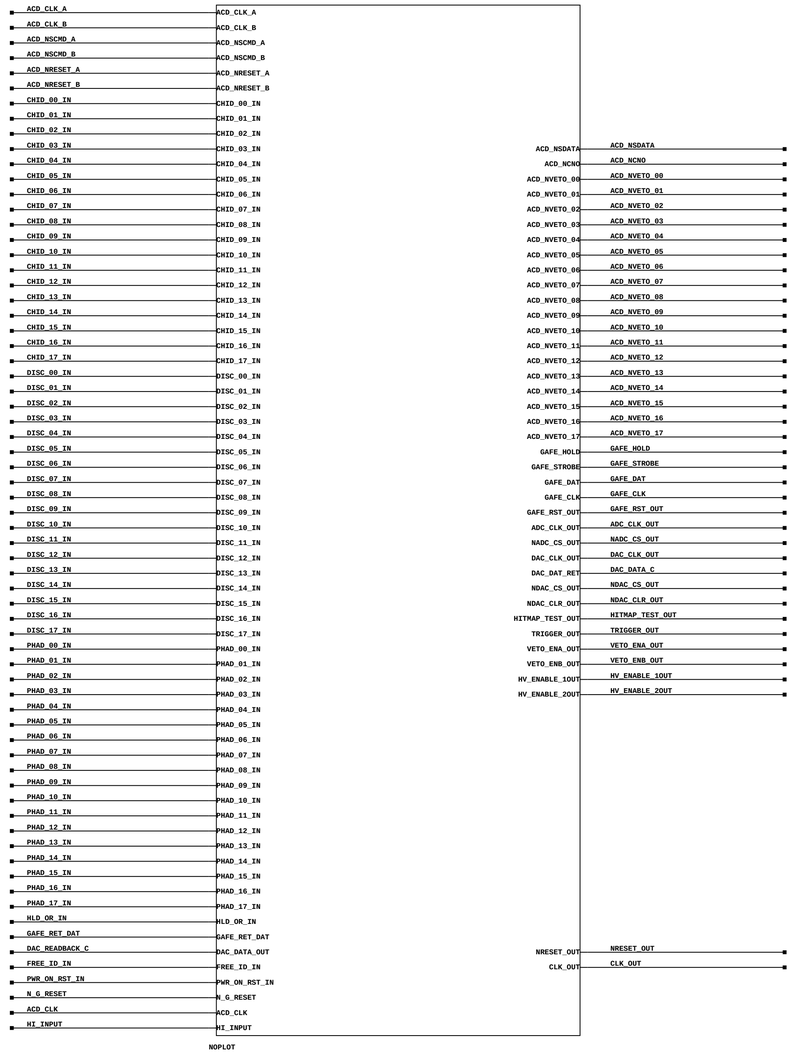
REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

GARC_CORE



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DISC_[17:00]_IN

PHAD_[17:00]_IN

ACD_NVETO_[17:00]

garc_core_intfc

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	DATE	APPROVALS
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SLAC GLAST

GARC_VER3

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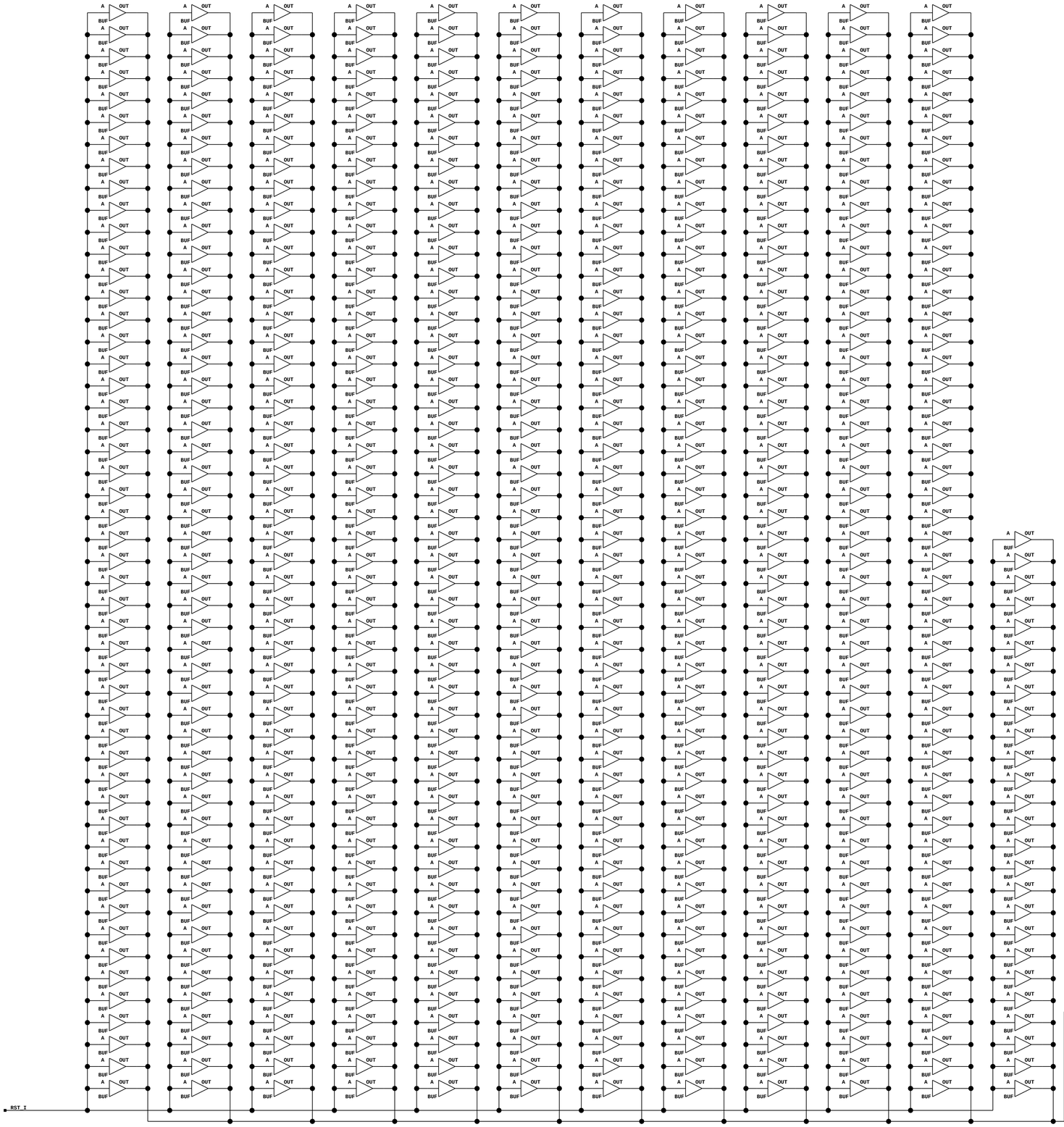
50 per column



REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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SLAC GLAST

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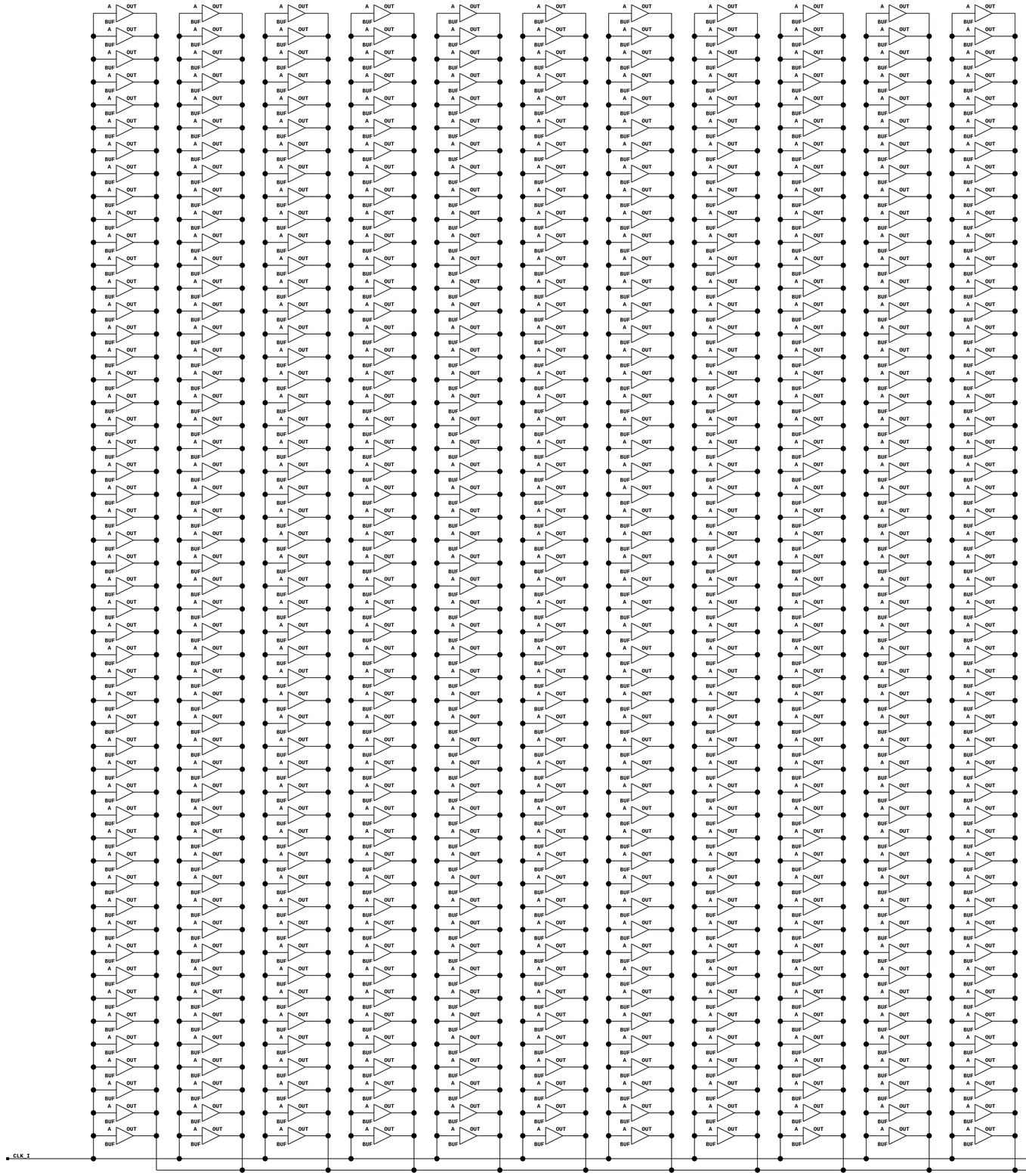
50 per column



REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

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	DATE	APPROVALS
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SLAC GLAST

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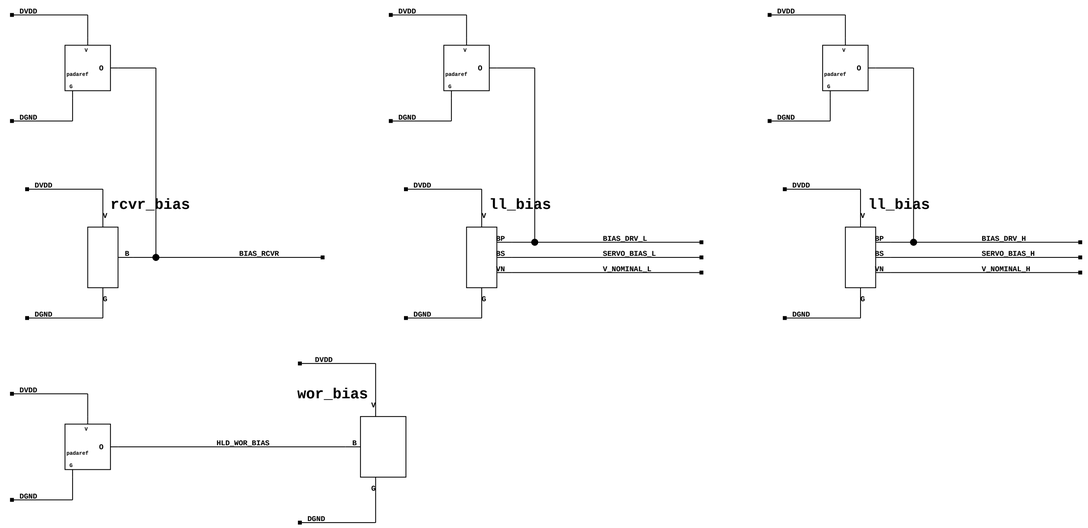
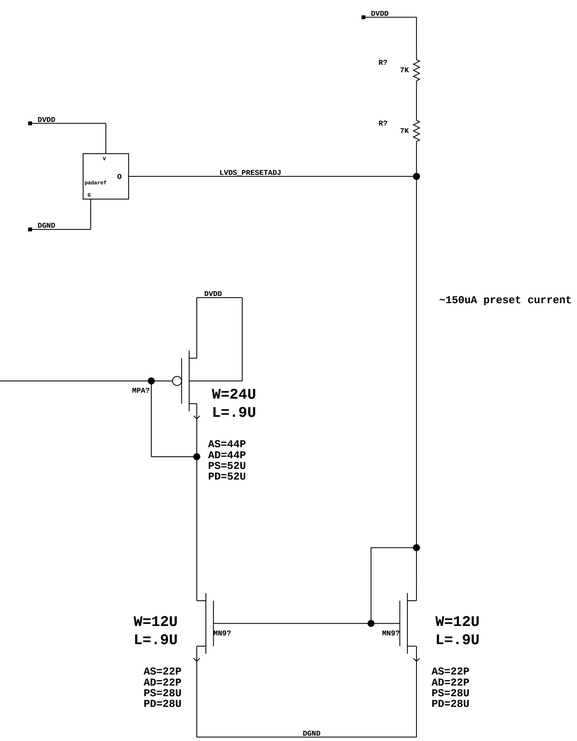
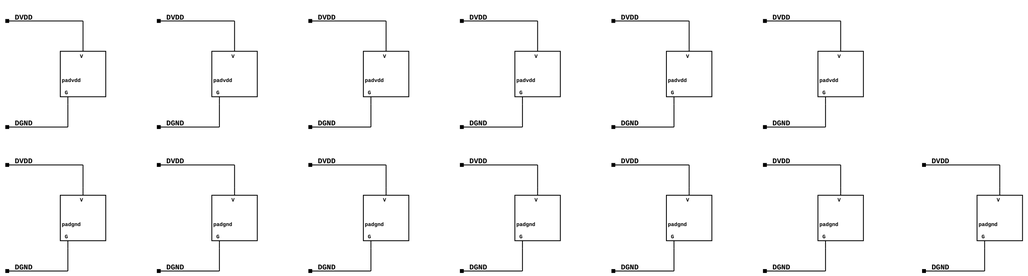
REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE







biases

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SLAC GLAST

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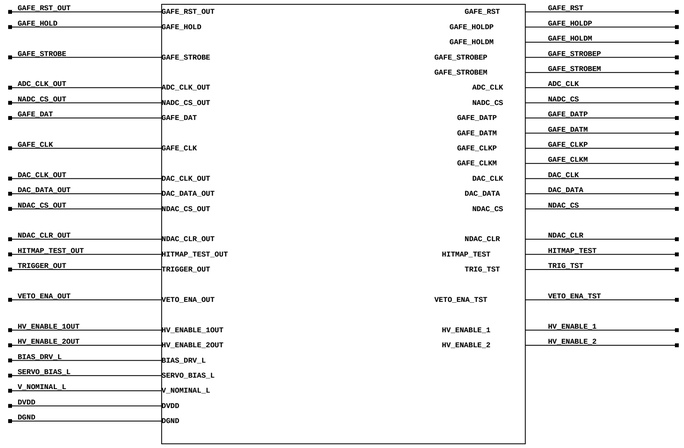
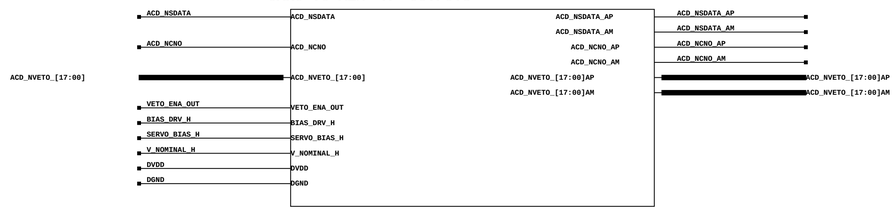
REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

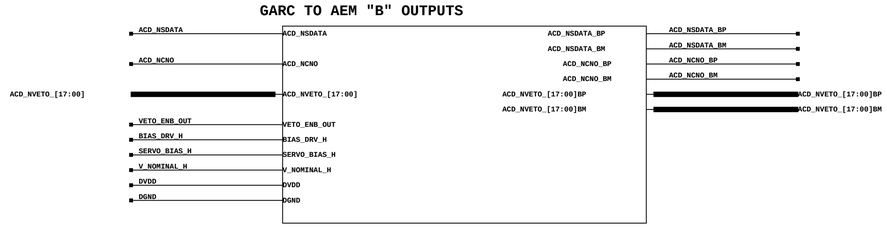
GAFE OUTPUT PADS AND LVDS DRIVERS



GARC TO AEM "A" OUTPUTS



GARC TO AEM "B" OUTPUTS



garc_outputs

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SLAC GLAST

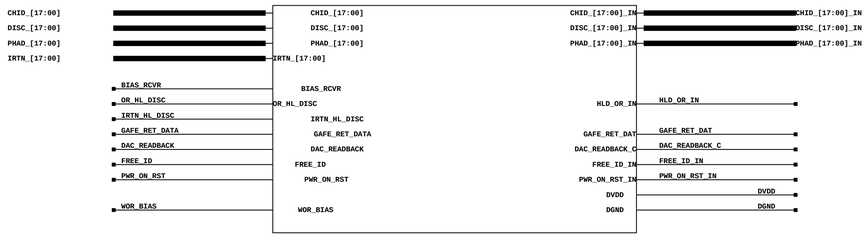
GARC_VER3

GARC OUTPUTS

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

GAFE INPUT PADS AND LVDS RECEIVERS



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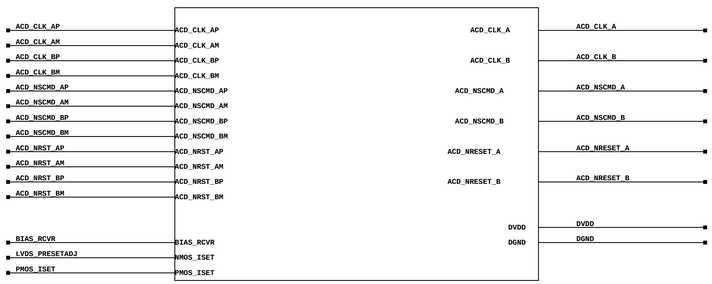

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SHEET OF

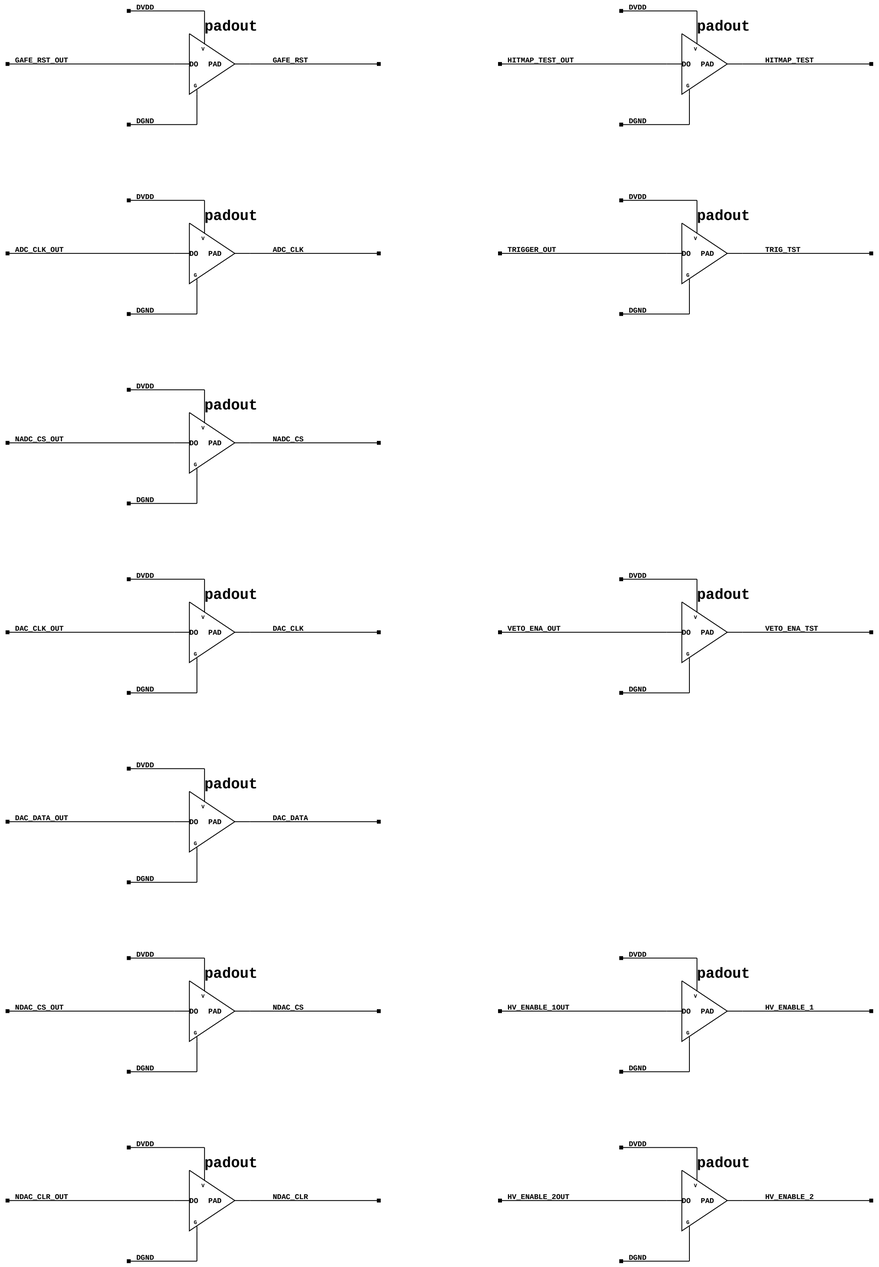
SLAC GLAST

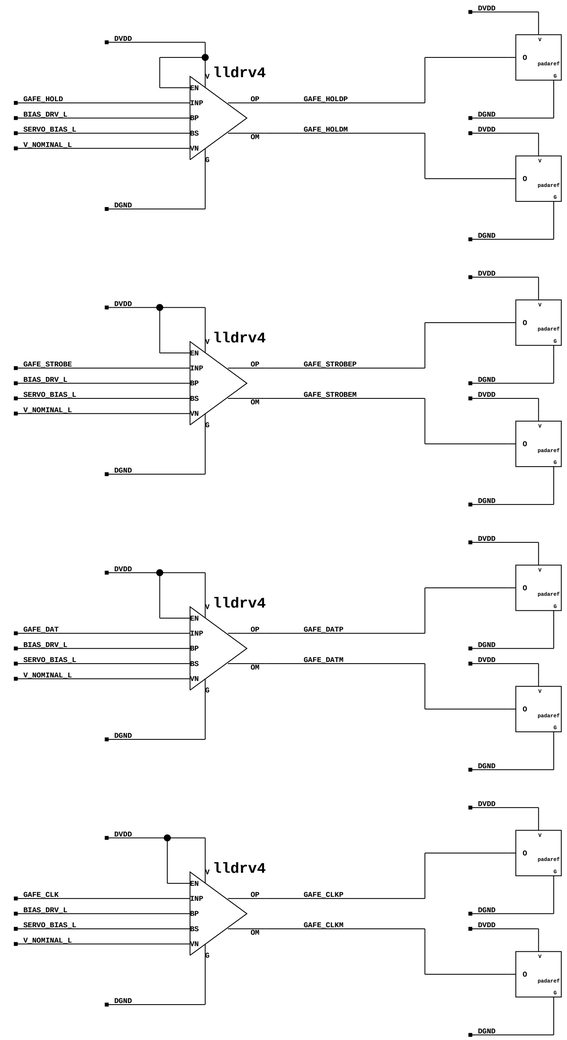
GARC_VER3

INPUT PADS AND LVDS RECEIVERS

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE





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	DATE	APPROVALS
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DFTR		
CHKR		

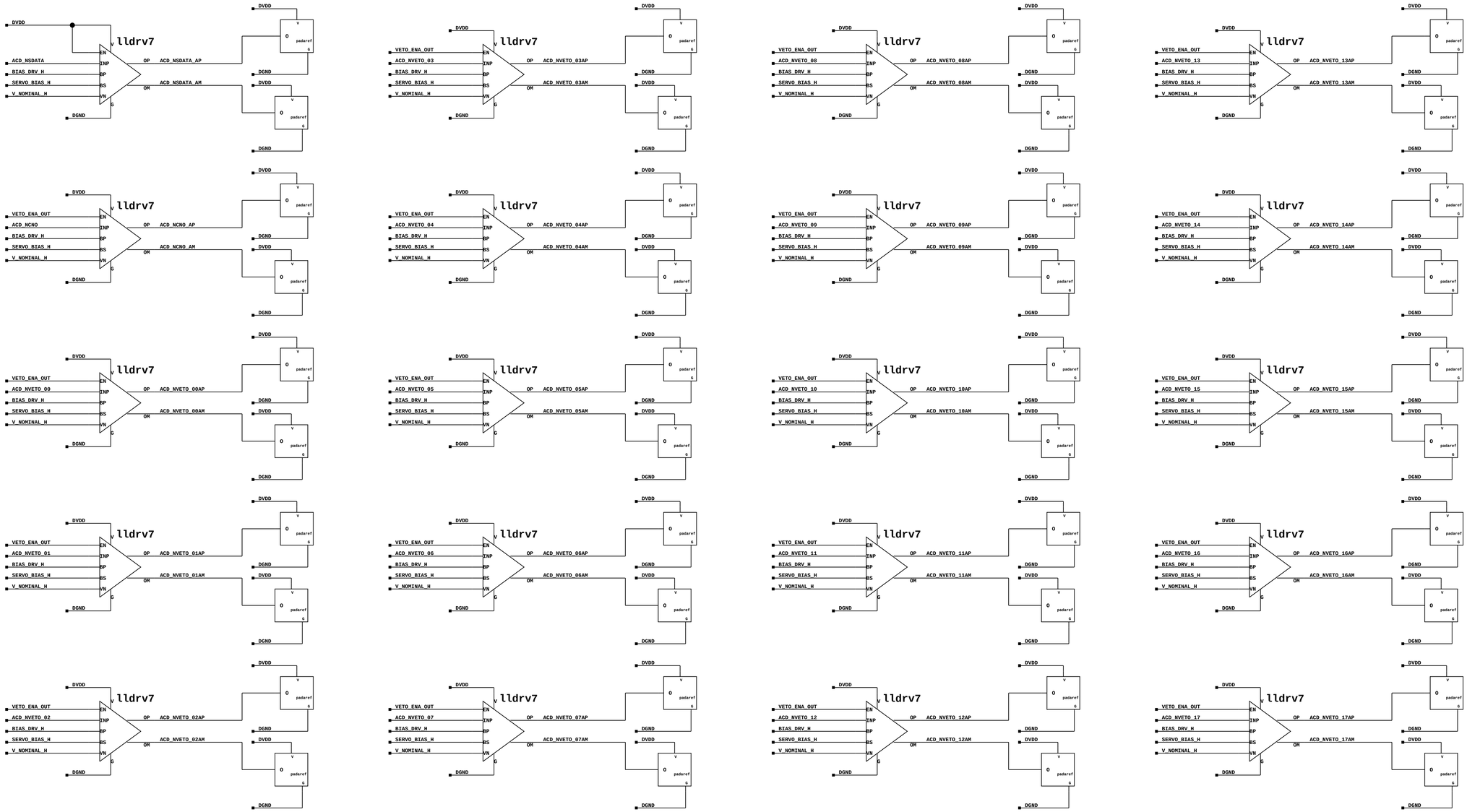
SHEET OF

SLAC GLAST

GARC_VER3
GAFE OUTPUT PADS
AND LVDS DRIVERS

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE



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	DATE	APPROVALS
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CHKR		

SHEET OF

SLAC GLAST

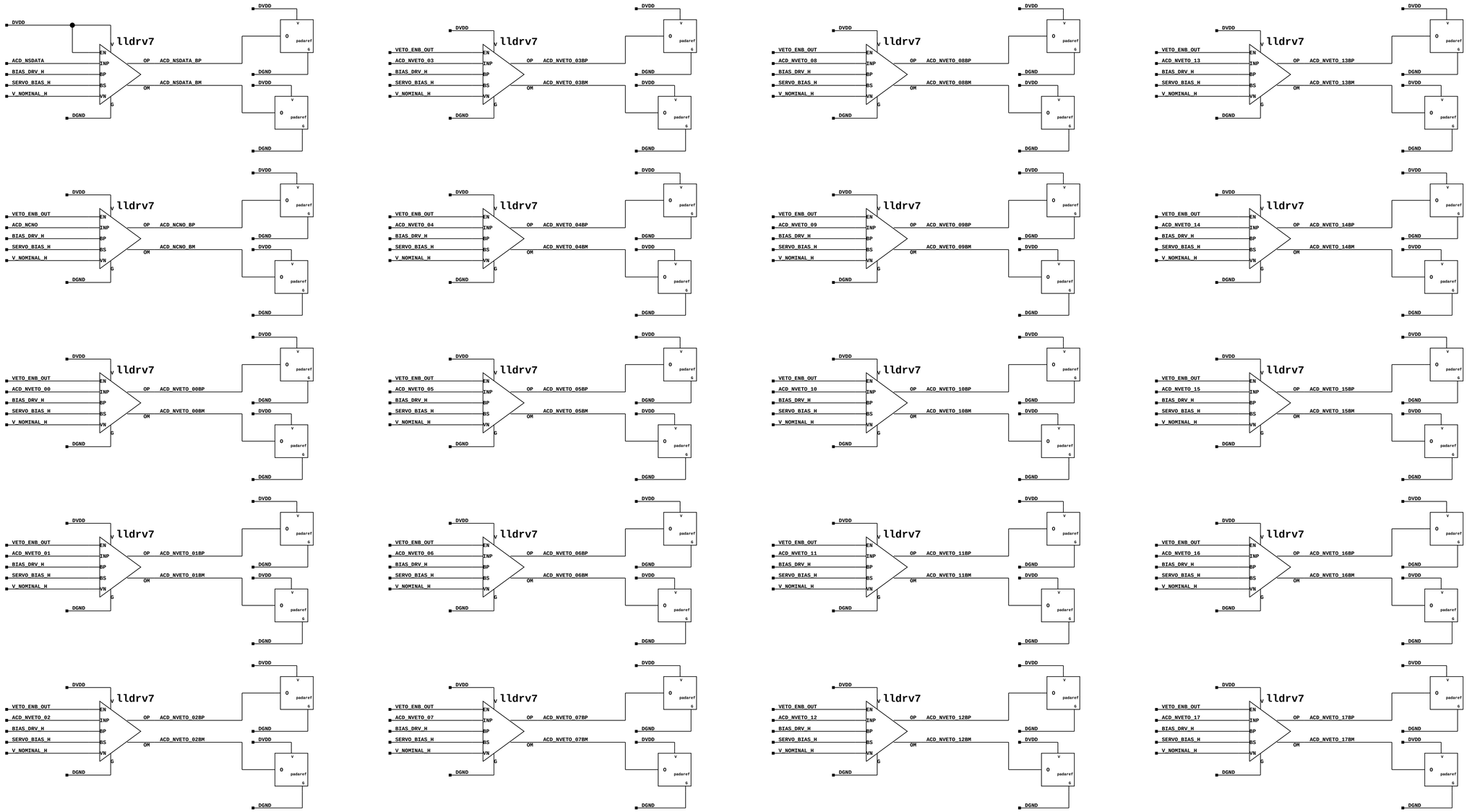
GARC_VER3

GARC AEM A OUTPUTS AND

LVDS DRIVERS (3.5mA each)

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE



garc_aem_b_outputs

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	DATE	APPROVALS
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SHEET OF

SLAC GLAST

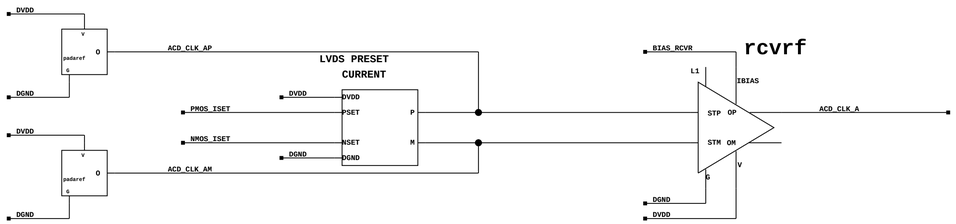
GARC_VER3

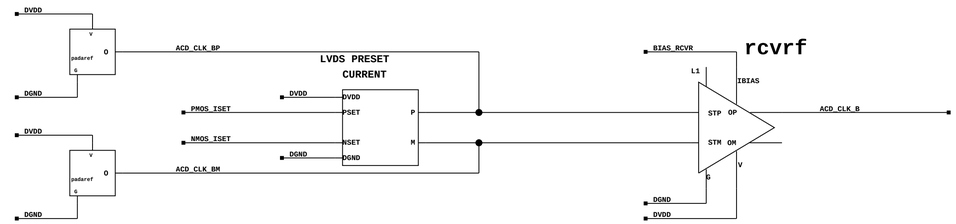
GARC AEM B OUTPUTS AND

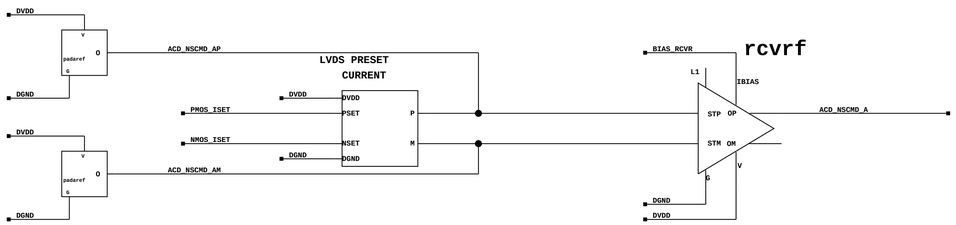
LVDS DRIVERS (3.5 mA each)

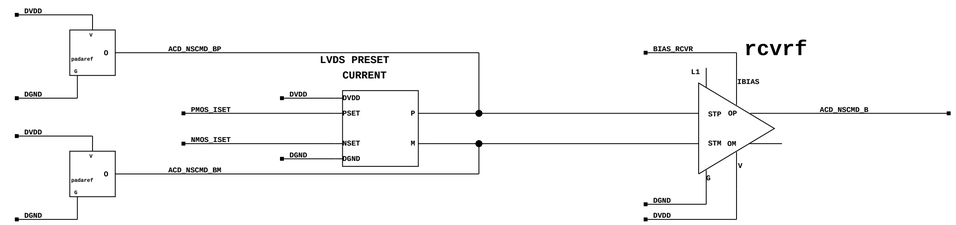
D

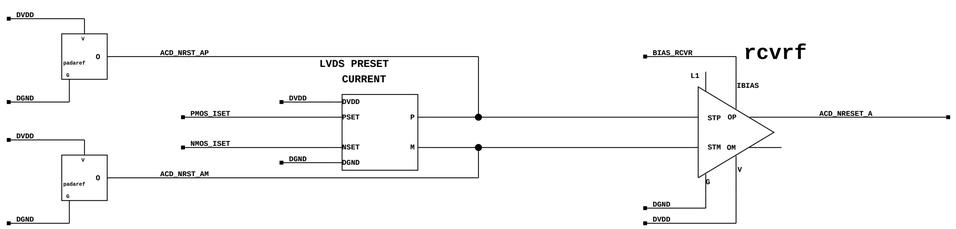
REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

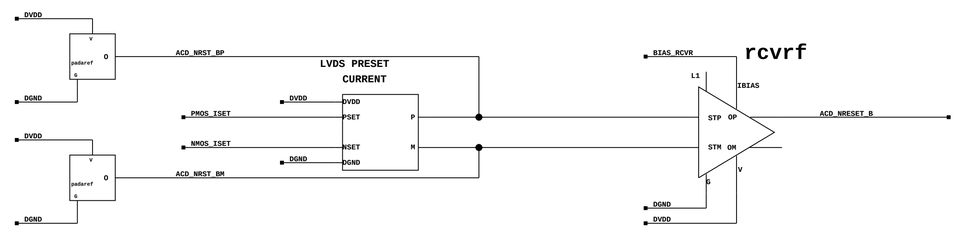












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	DATE	APPROVALS
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SLAC GLAST

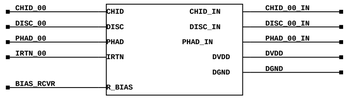
GARC_VER3

GARC AEM INPUTS

D

REV	DESCRIPTION	DFTR	CHKR	ENGR APPV	DATE

PADAREFS and 3 LVDS RCVR'S



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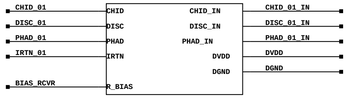

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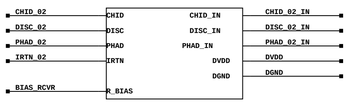

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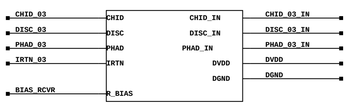

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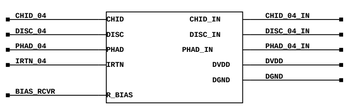

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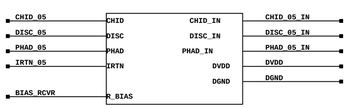

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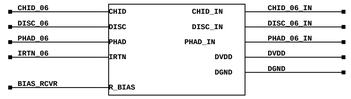

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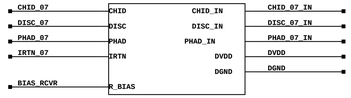
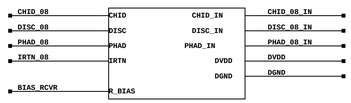


PADAREFS and 3 LVDS RCVR'S



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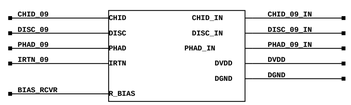

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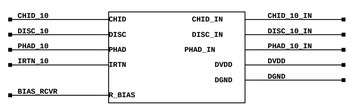
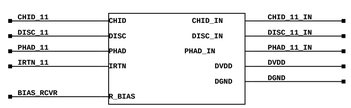
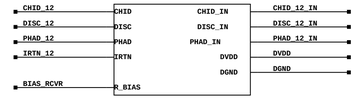


PADAREFS and 3 LVDS RCVR'S



PADAREFS and 3 LVDS RCVR'S



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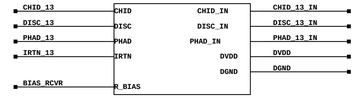

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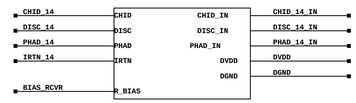

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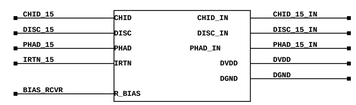

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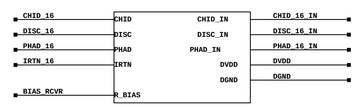

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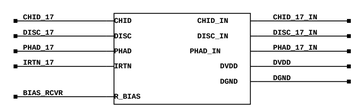

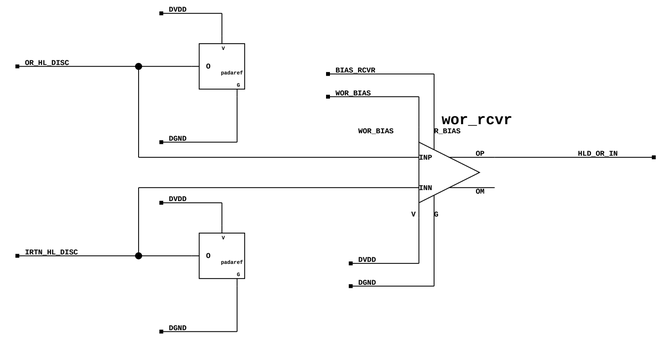

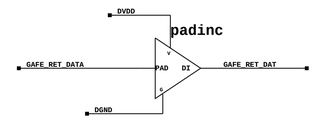

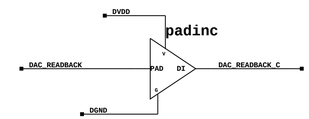

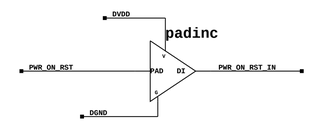

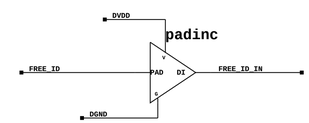

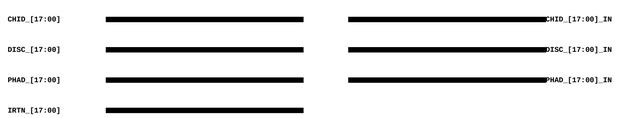

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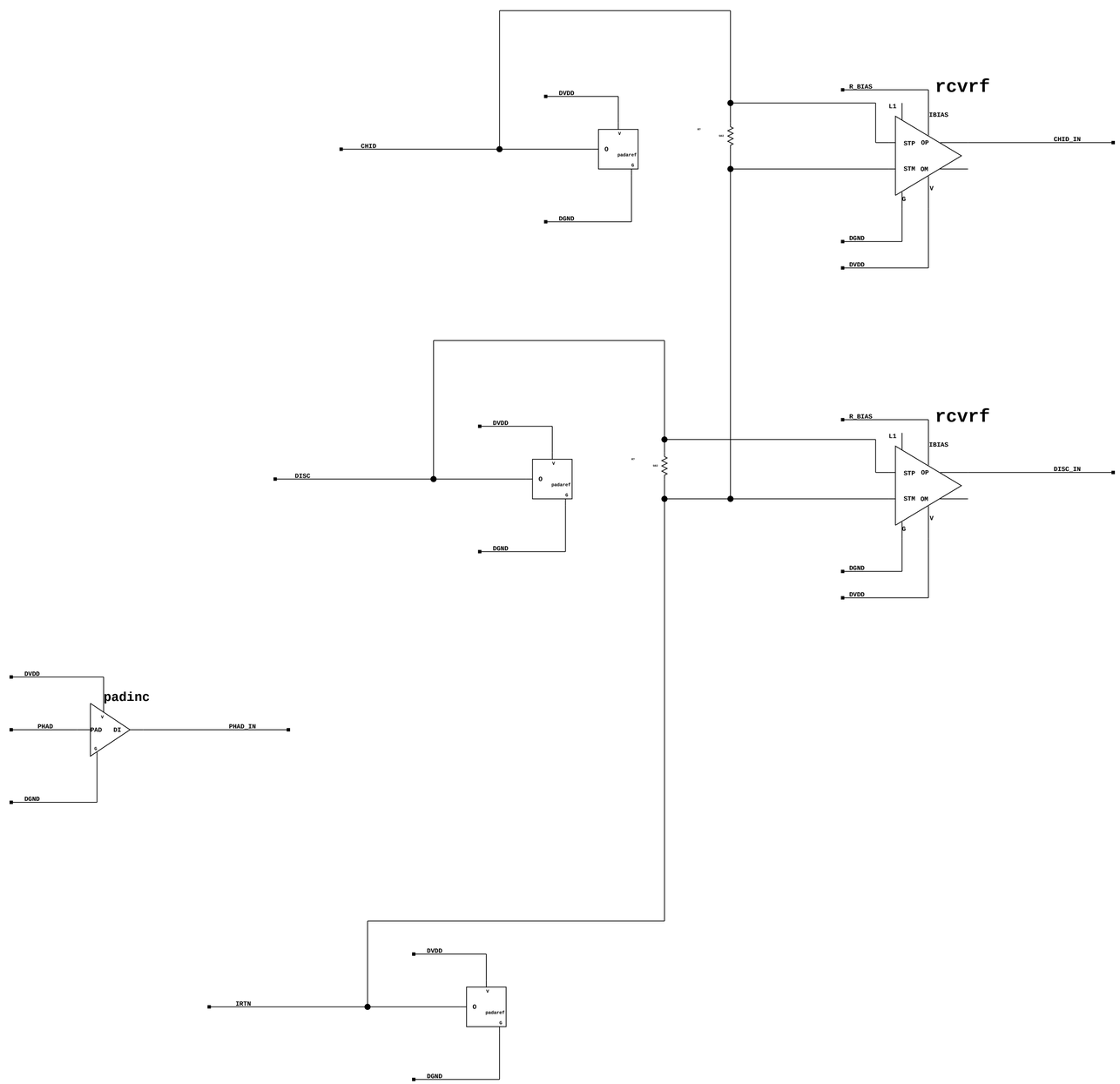

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